

664020" 2E041250

FIG. 2

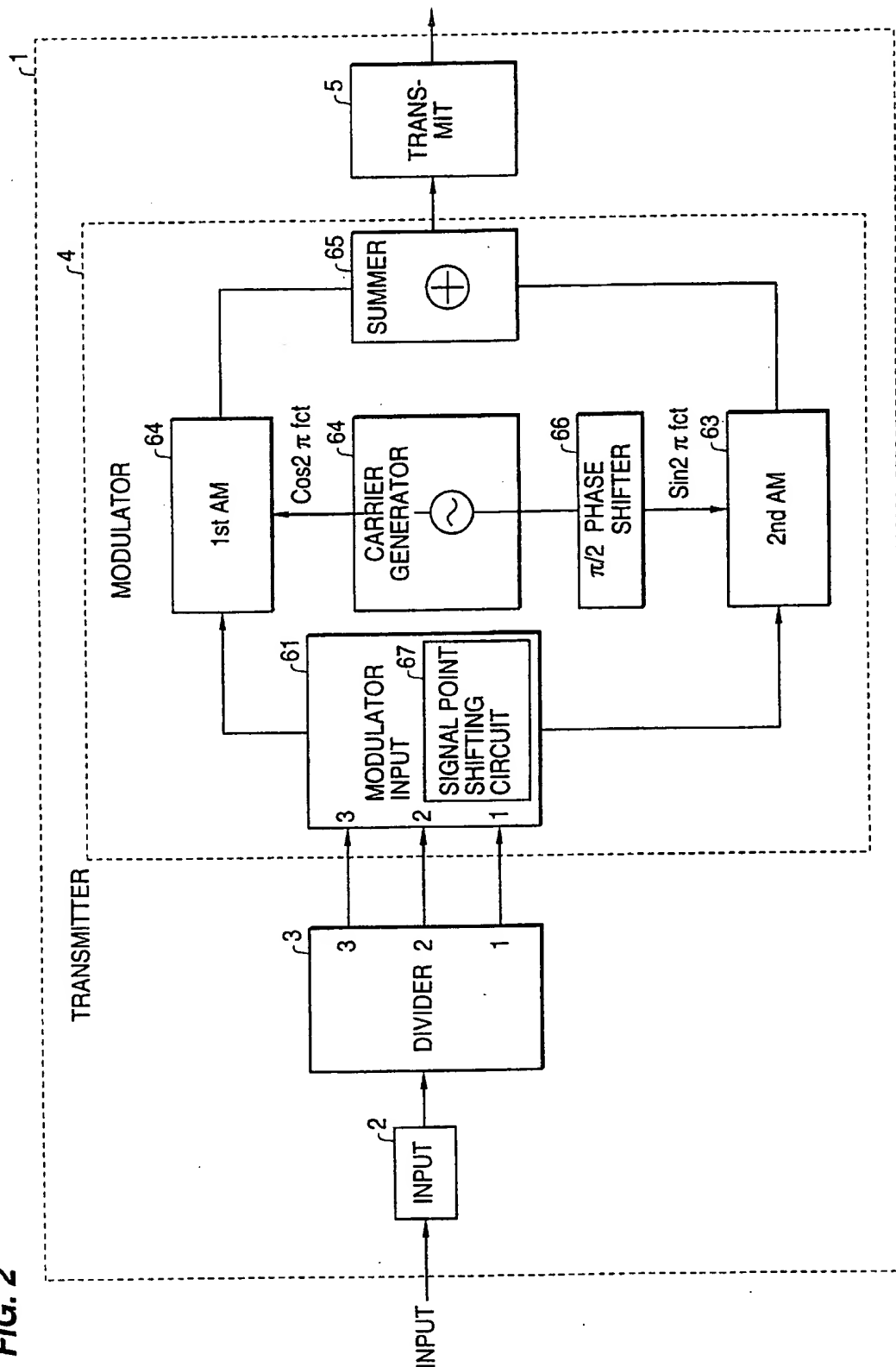
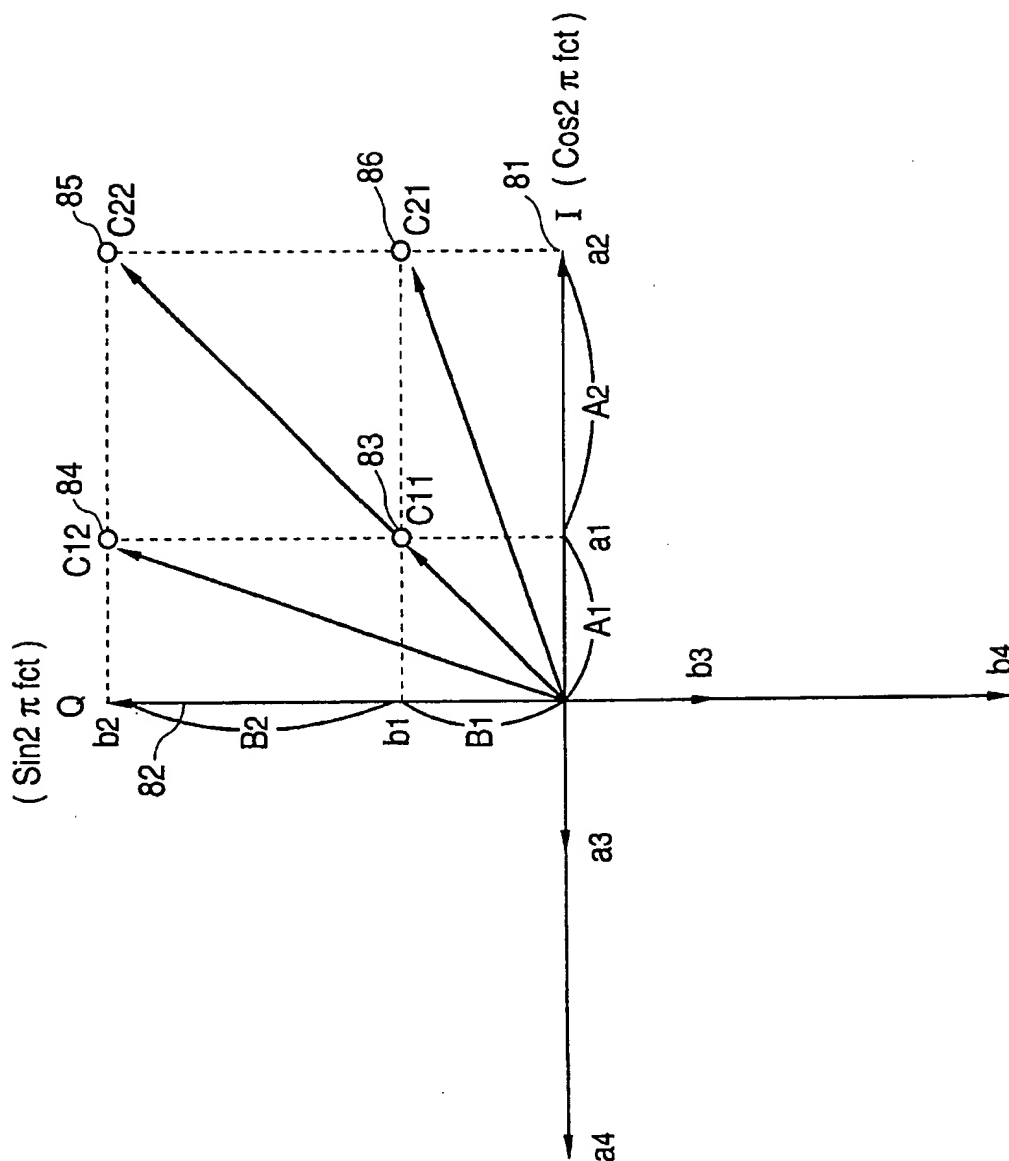


FIG. 3



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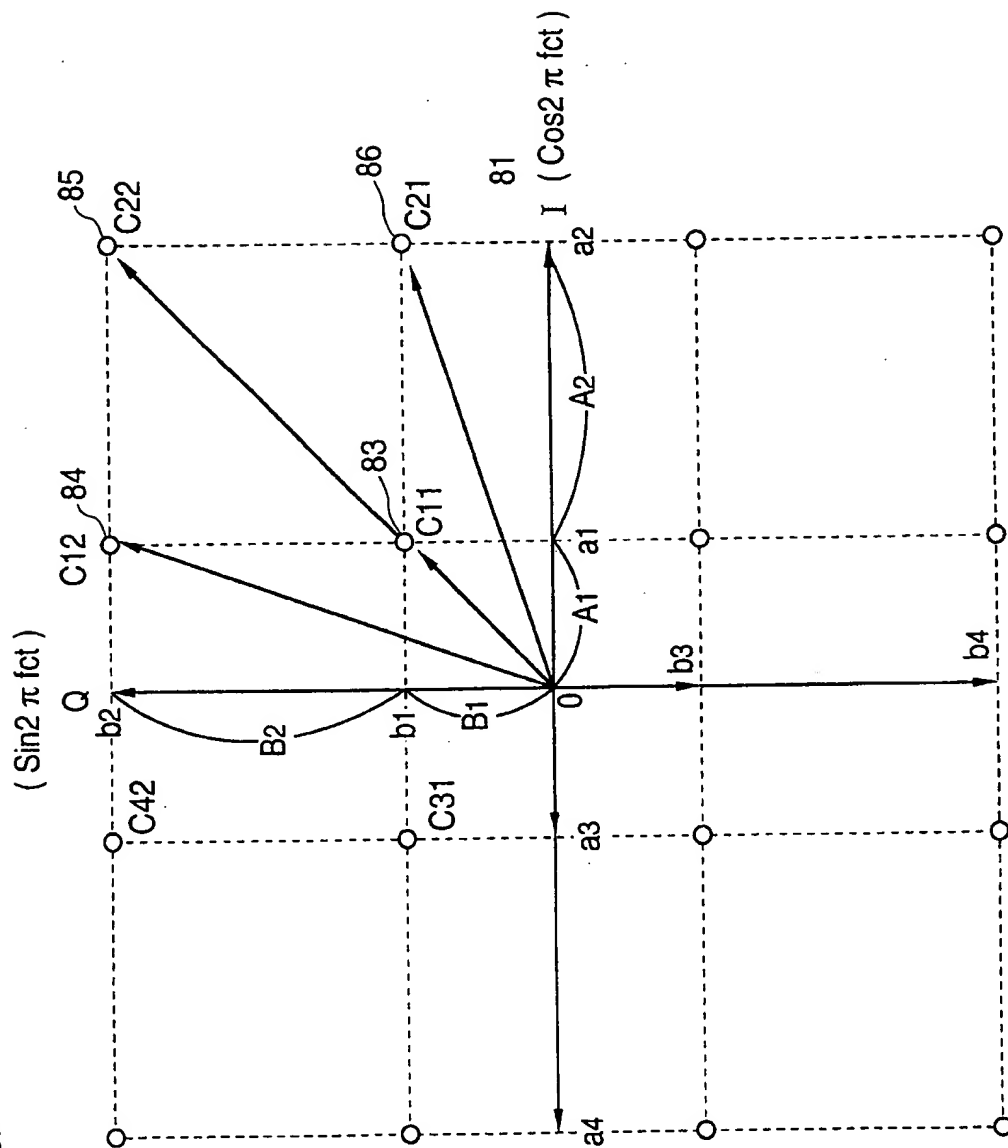
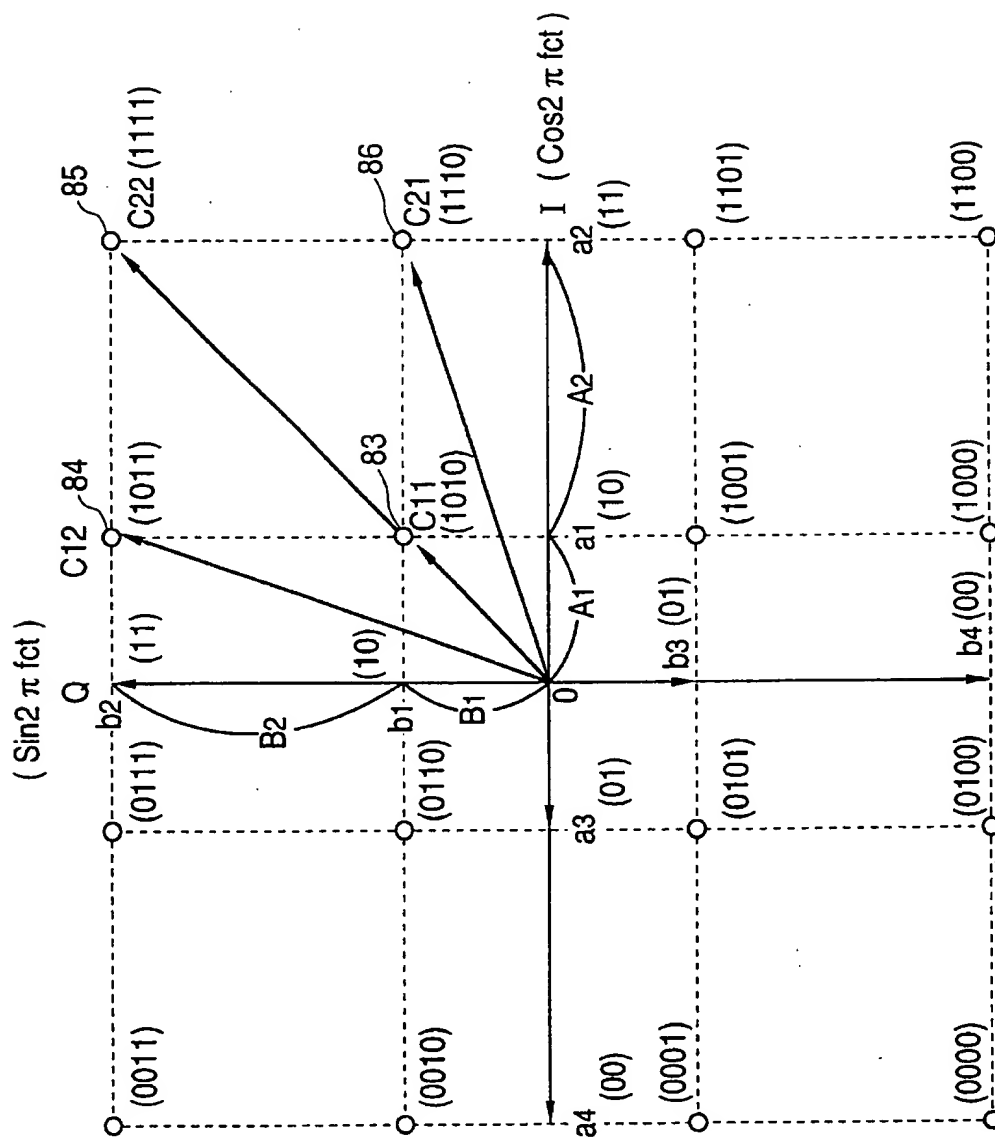
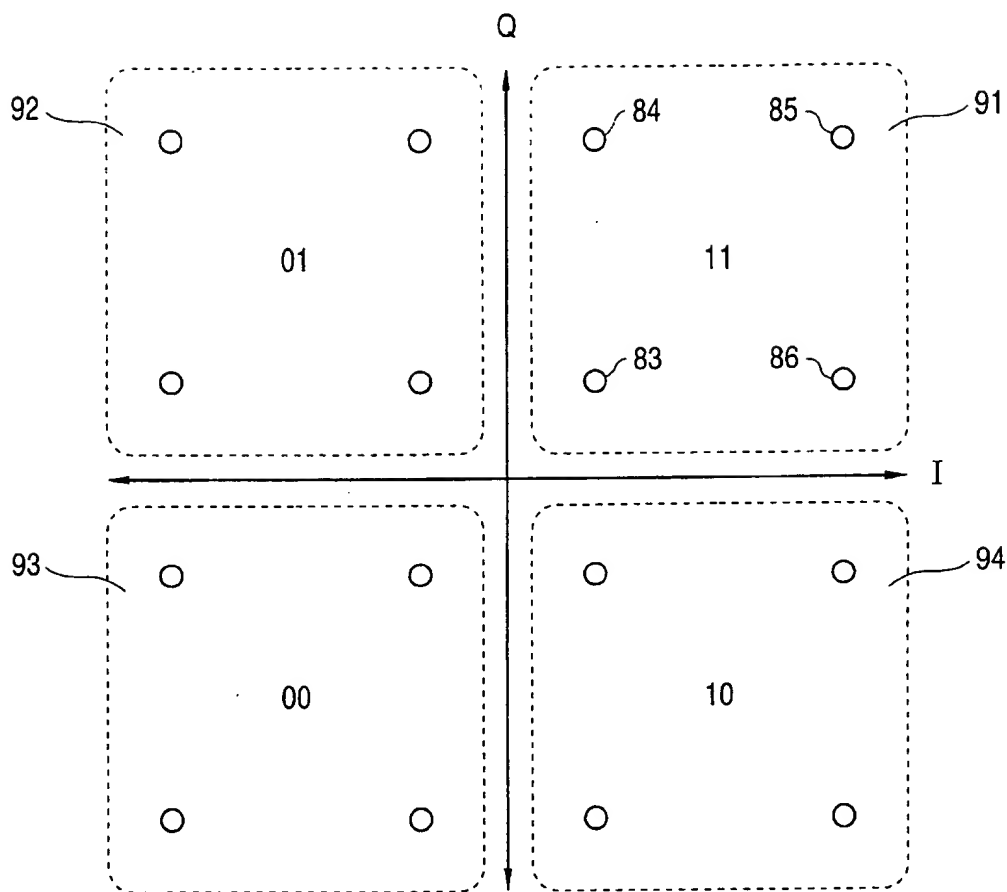


FIG. 5



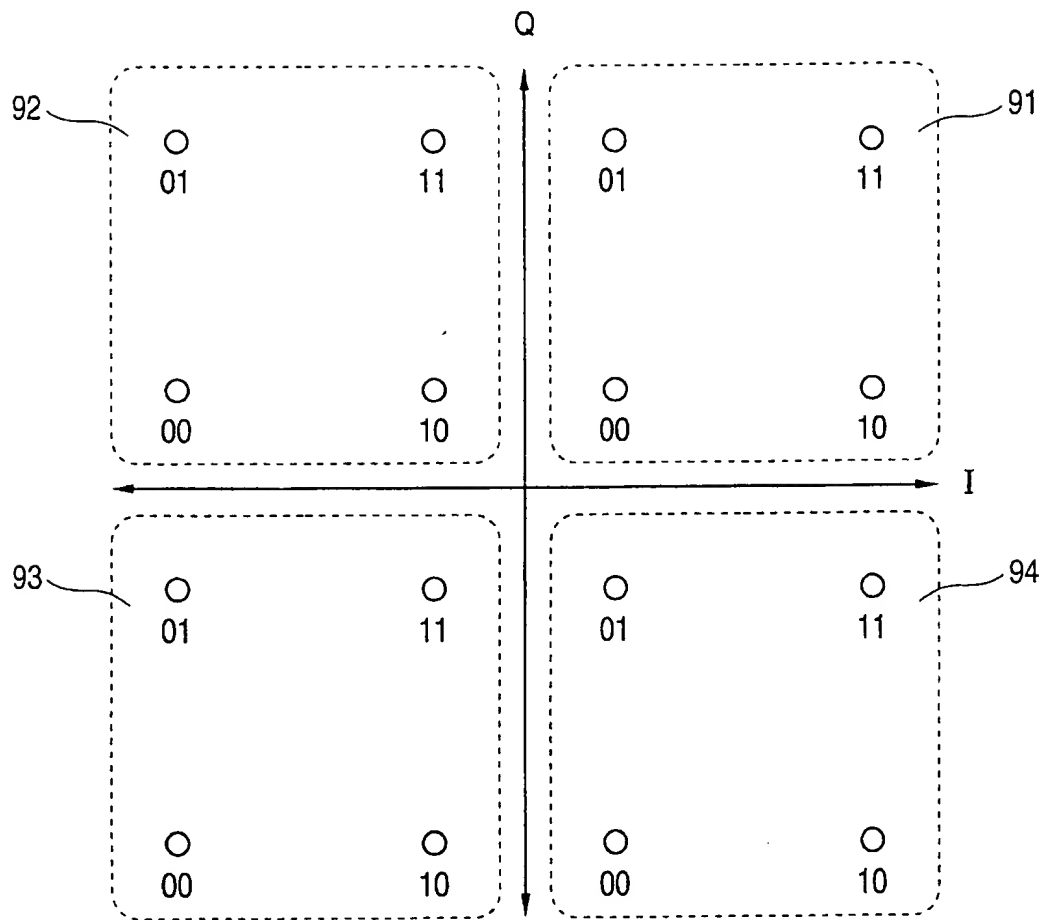
654020 26044260

FIG. 6



654020" 48044260

FIG. 7



664020" 2E044260

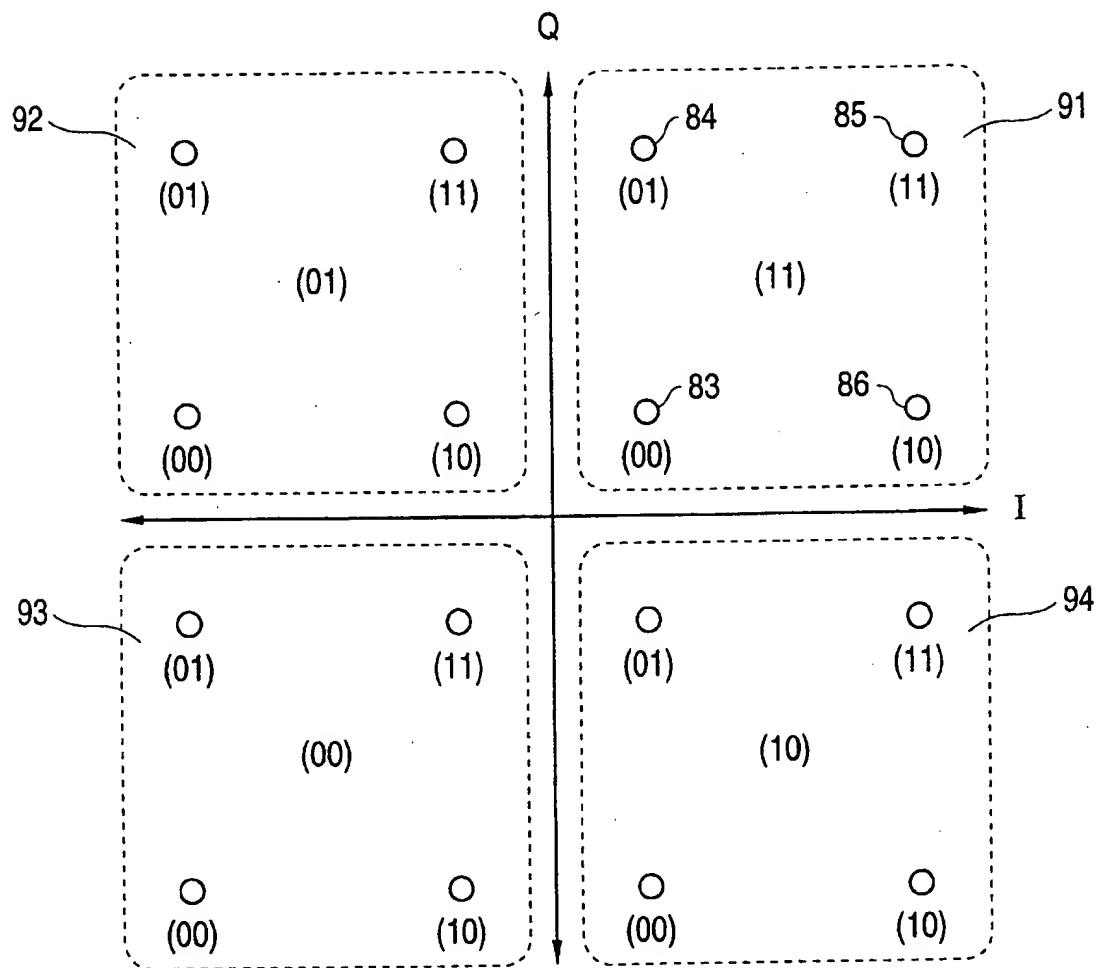
FIG. 8

FIG. 9

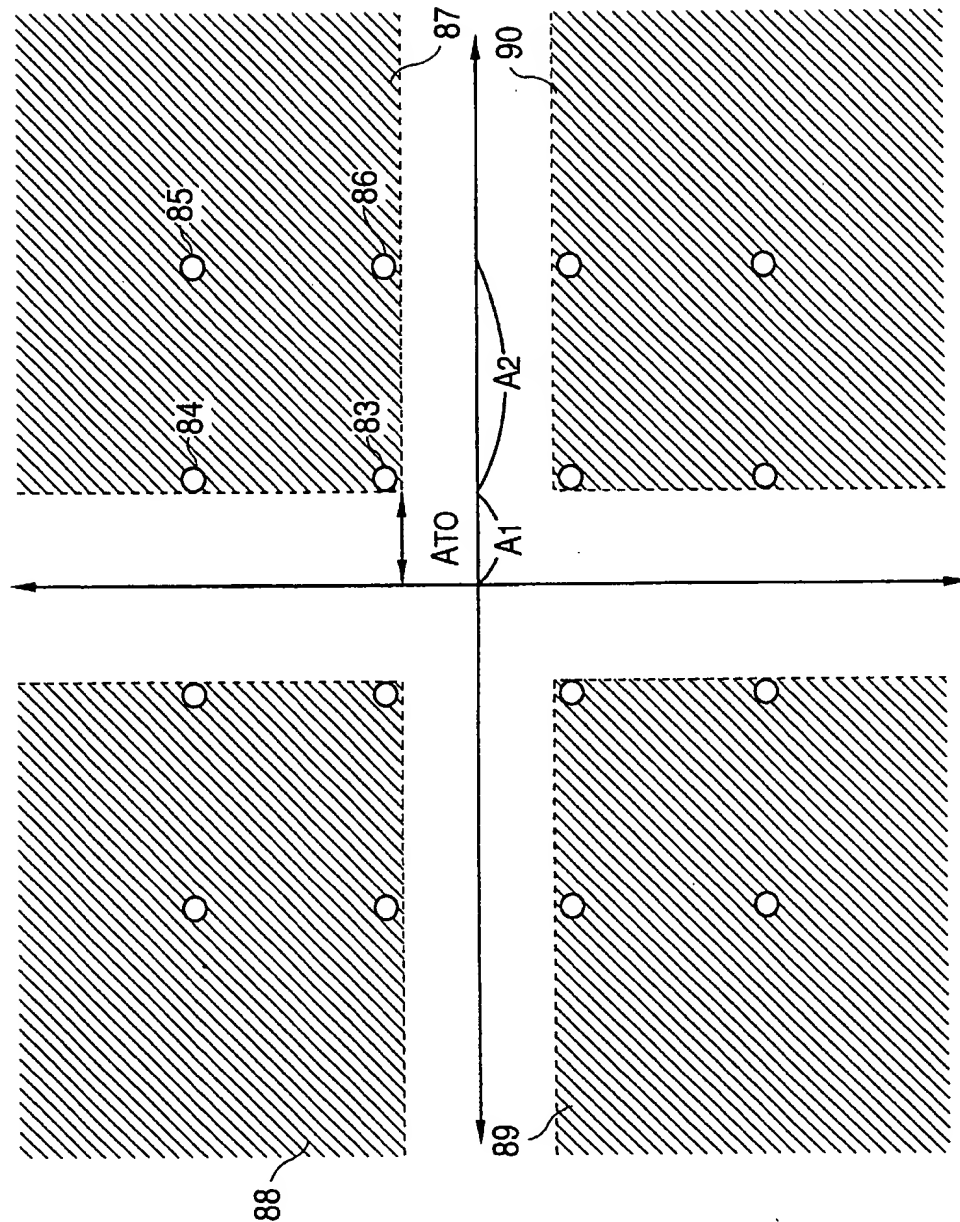
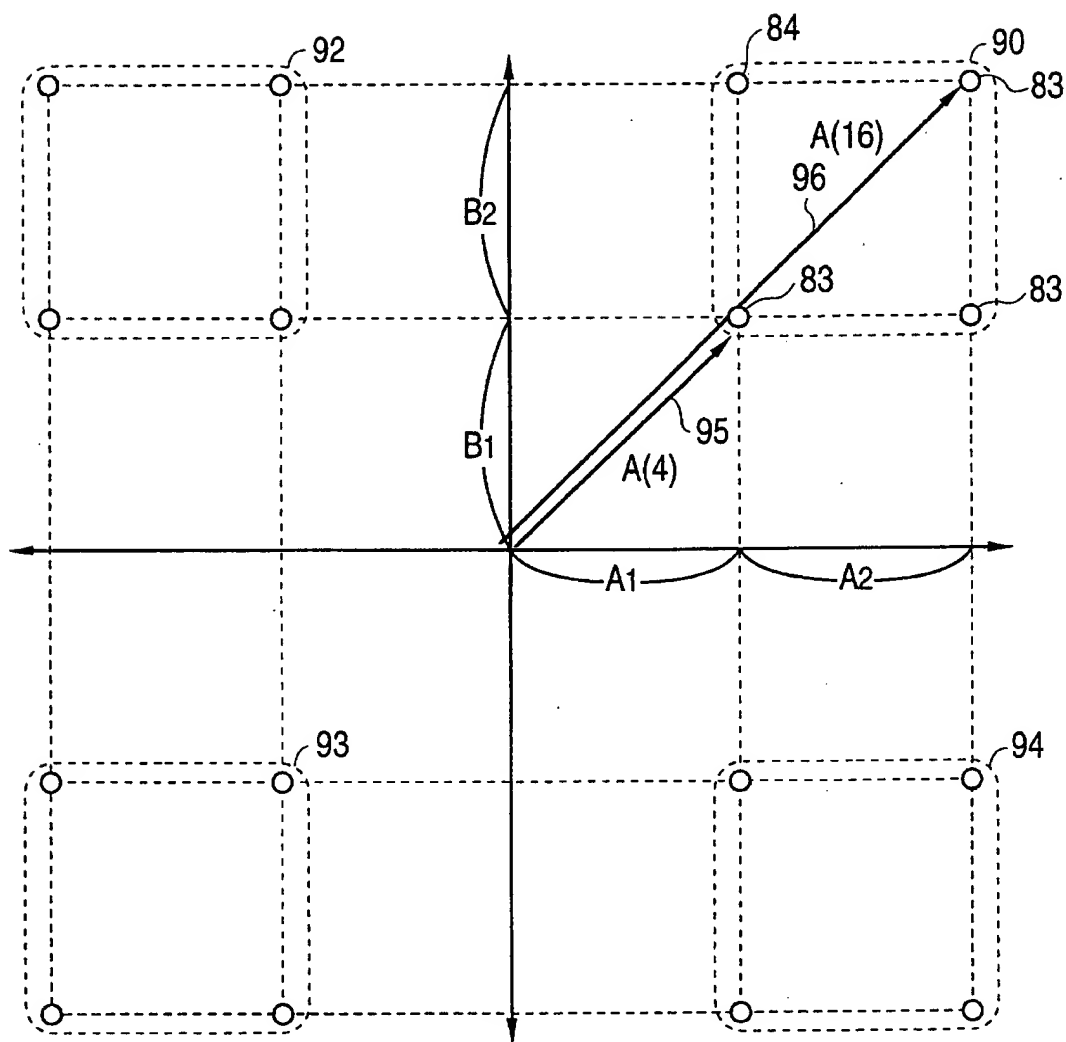


FIG. 10



554020" 22044260

FIG. 11

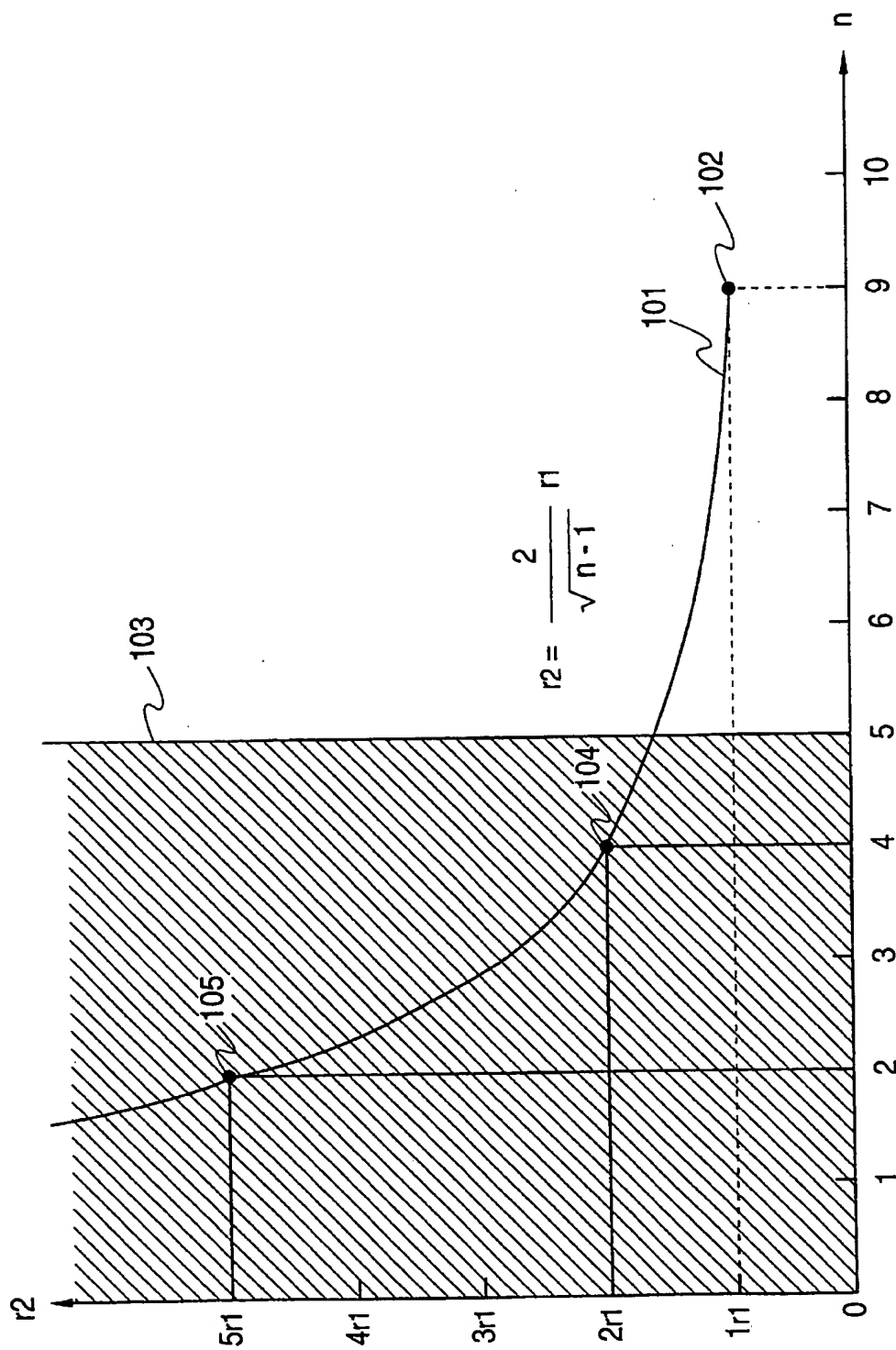
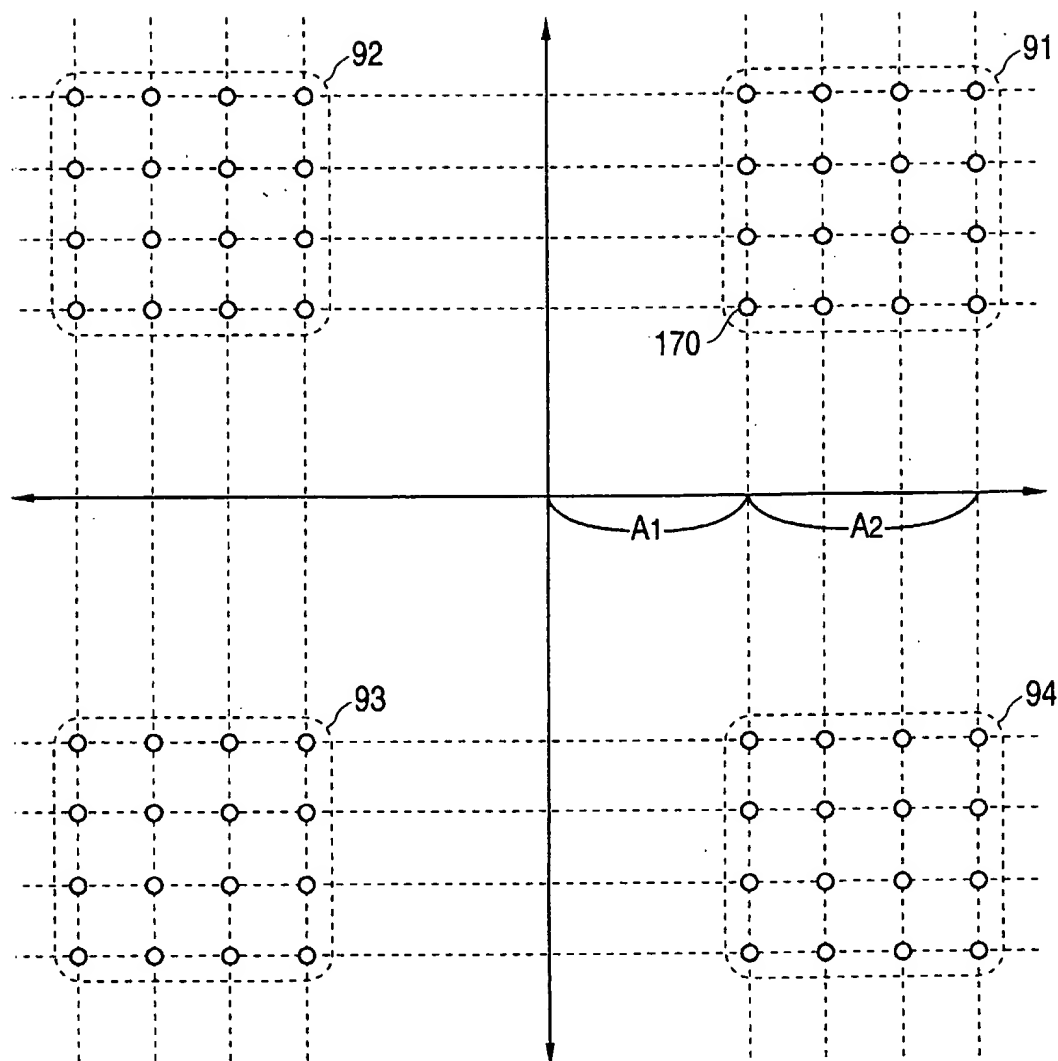


FIG. 12



654020" 2E044260

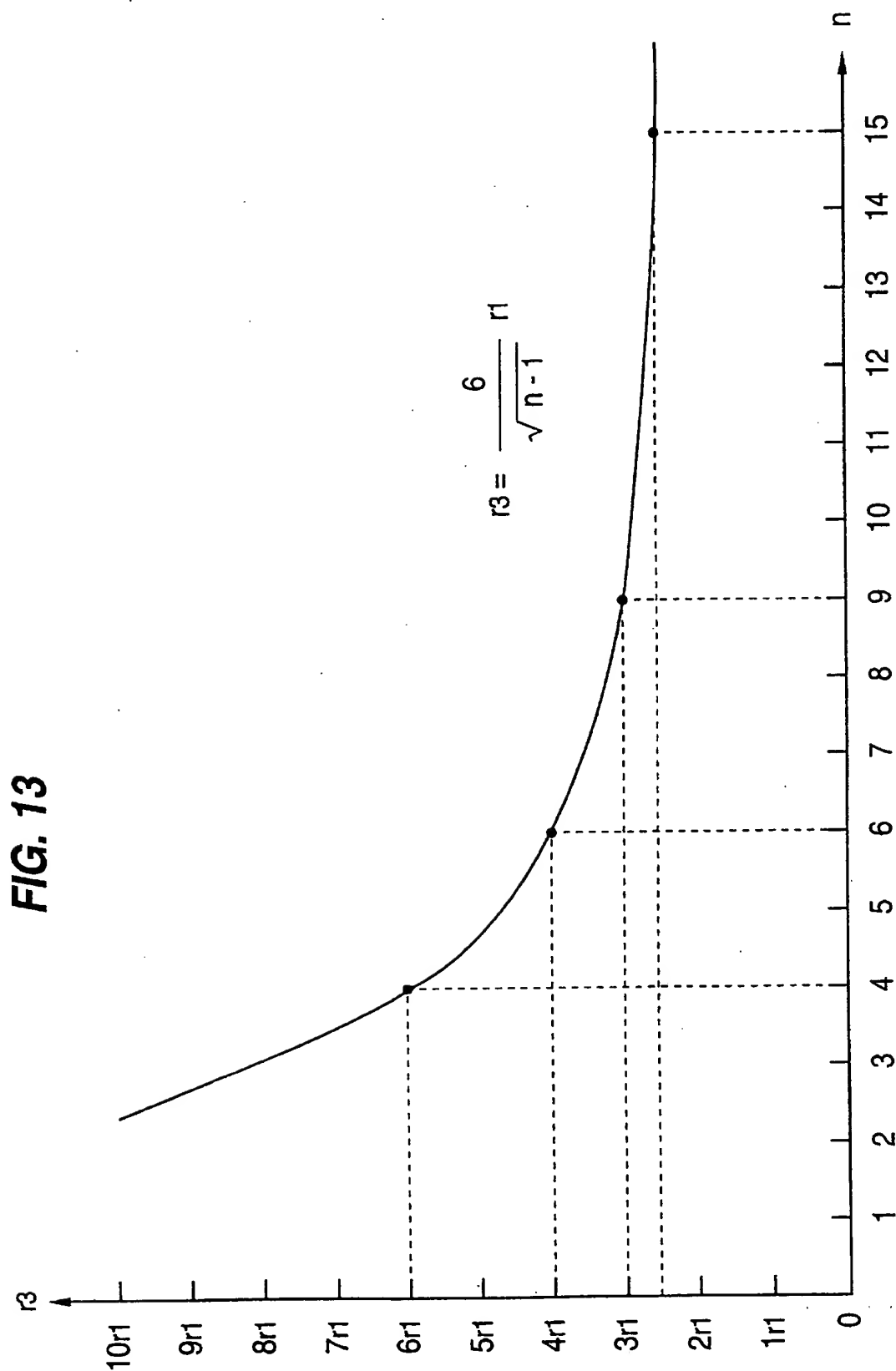


FIG. 14

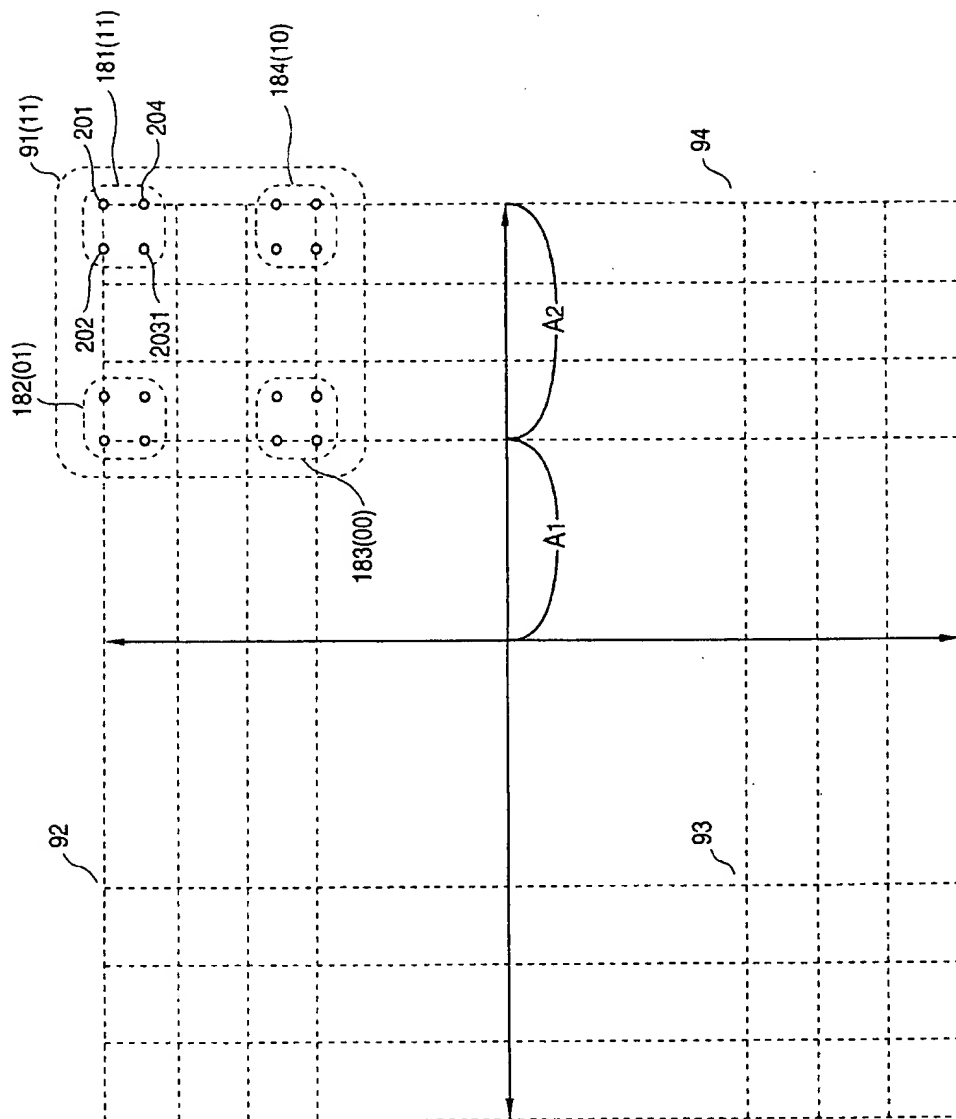


FIG. 15

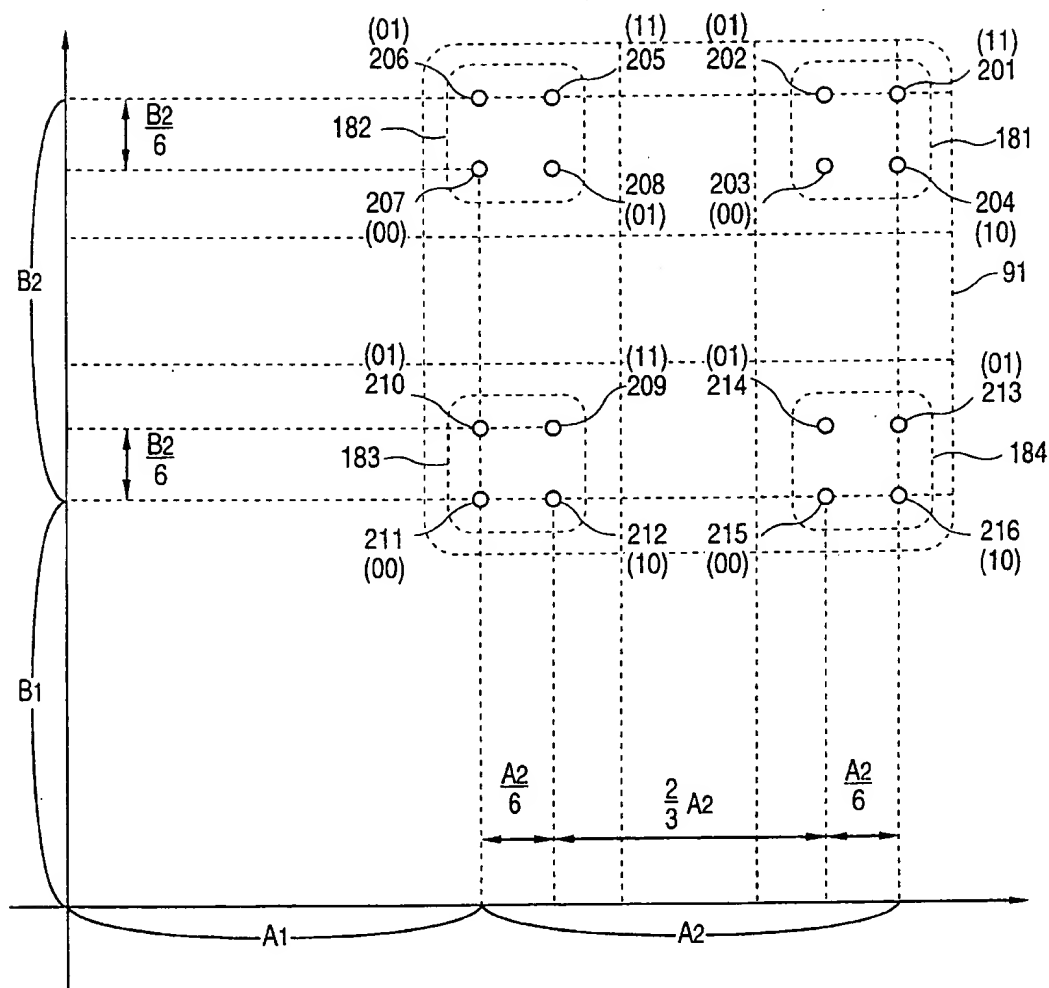


FIG. 16

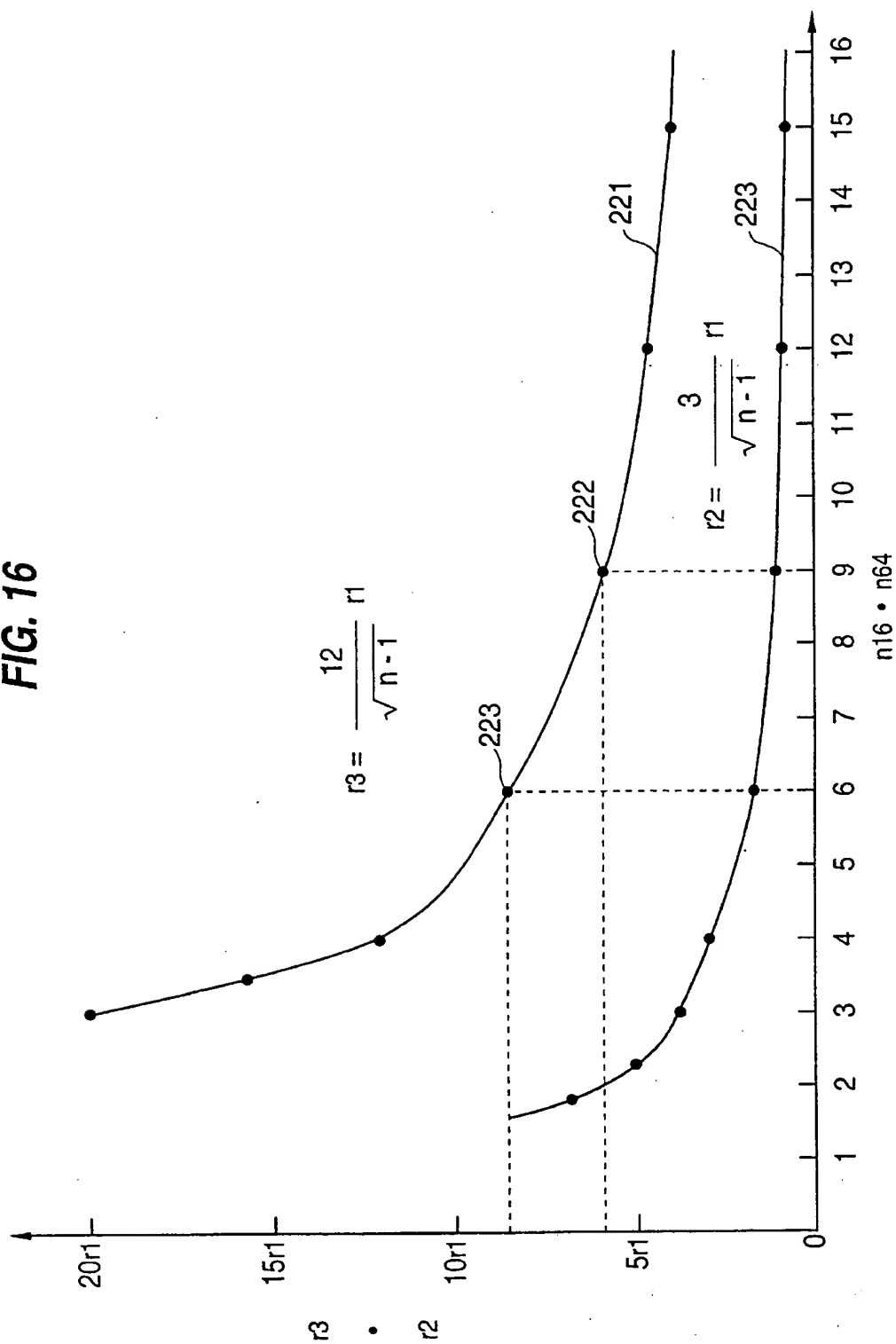


FIG. 17

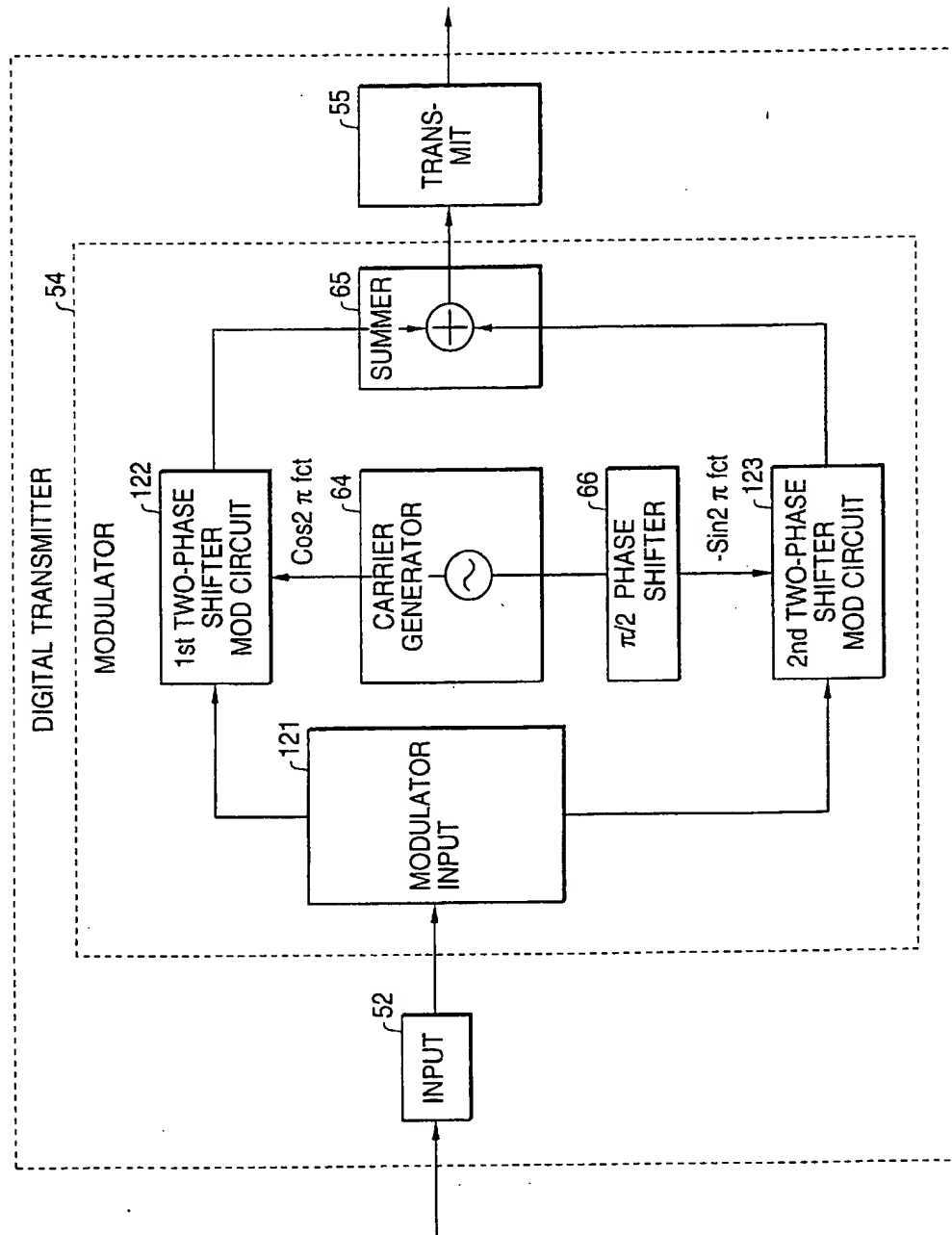
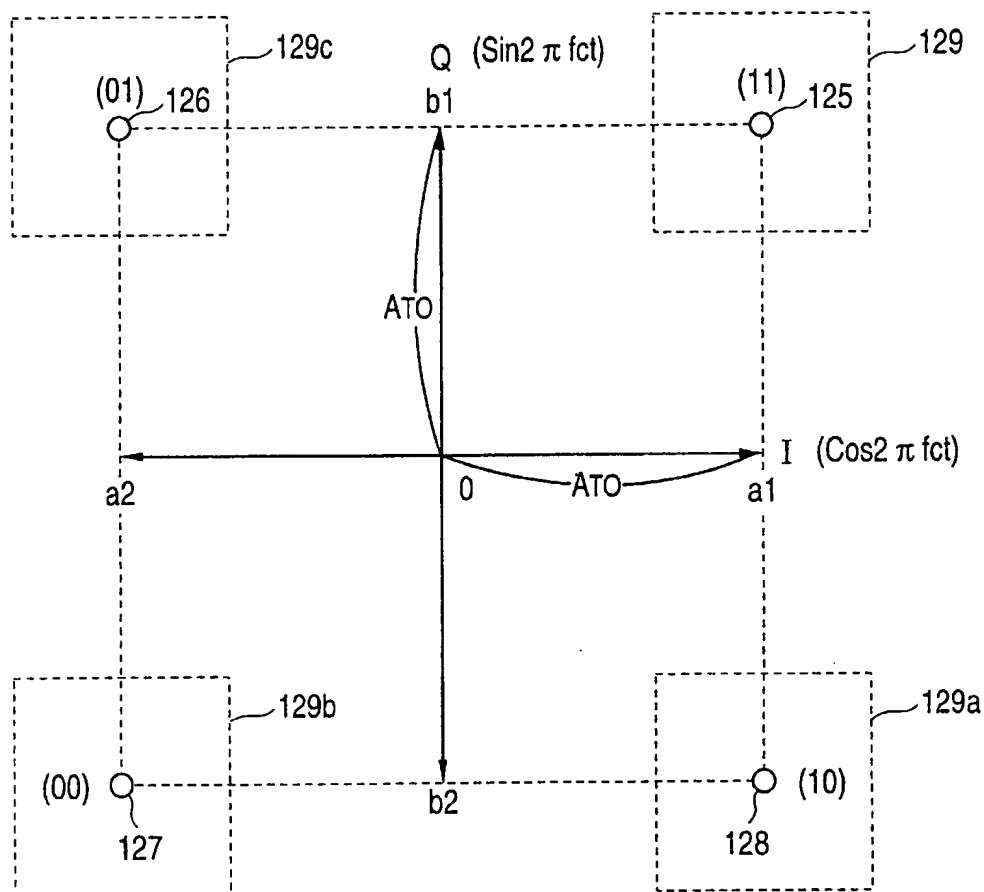


FIG. 18



664020" 2E044260

FIG. 19

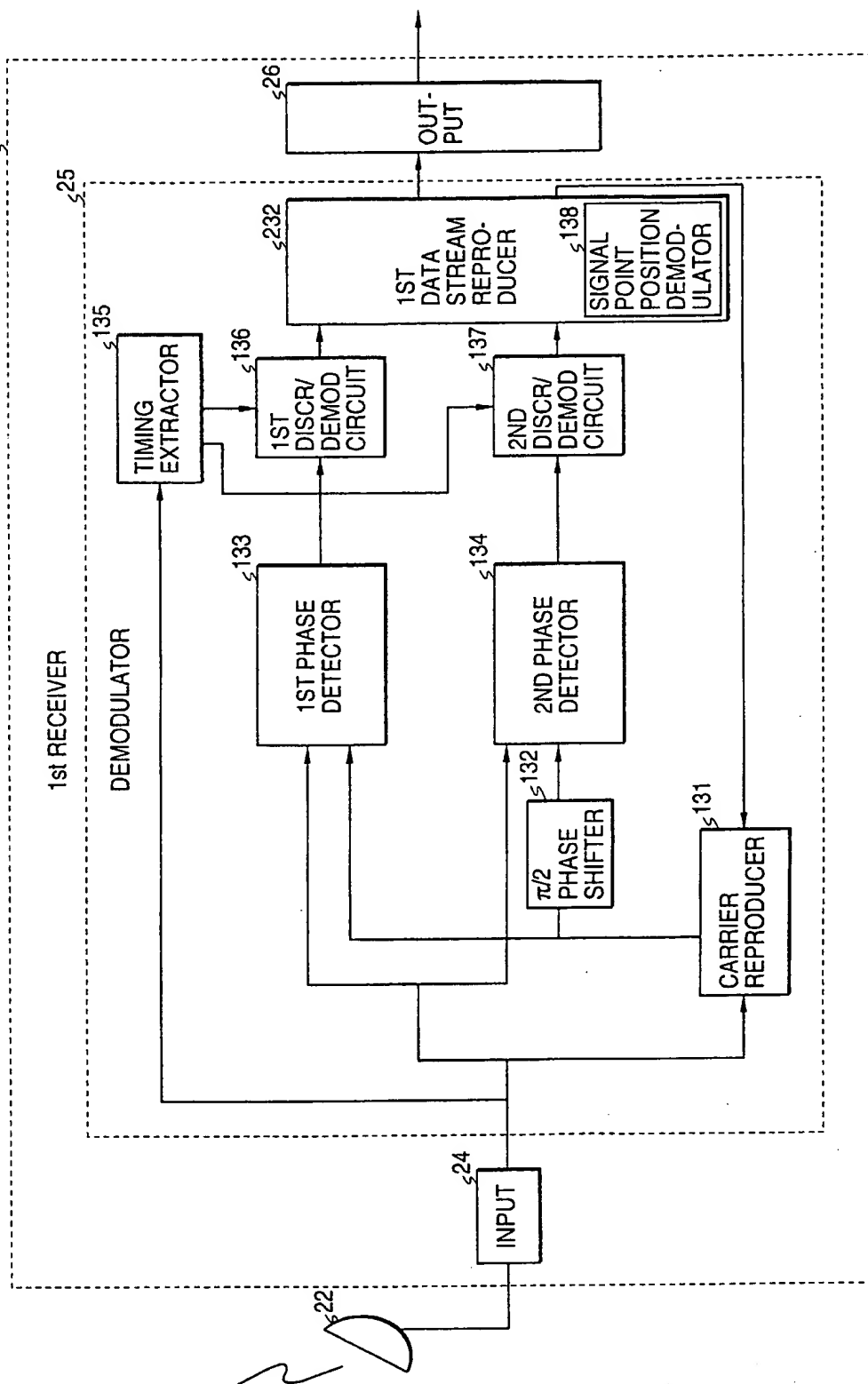
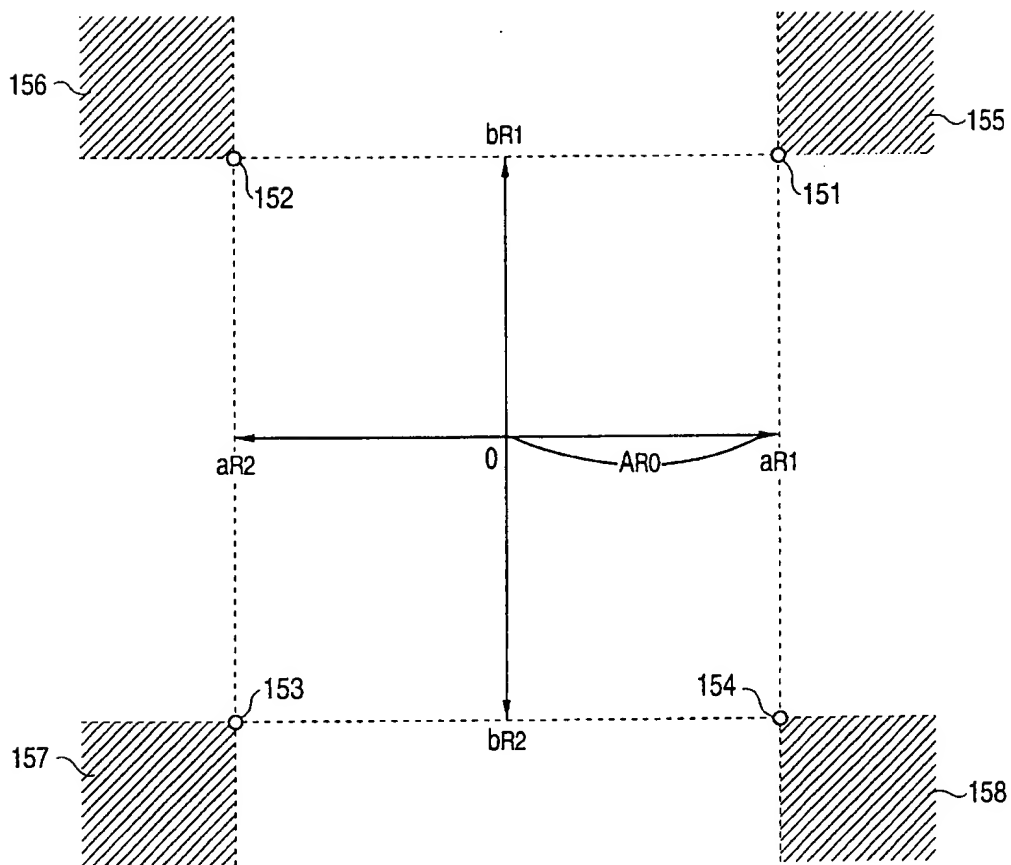


FIG. 20



654020-2E04260

FIG. 21

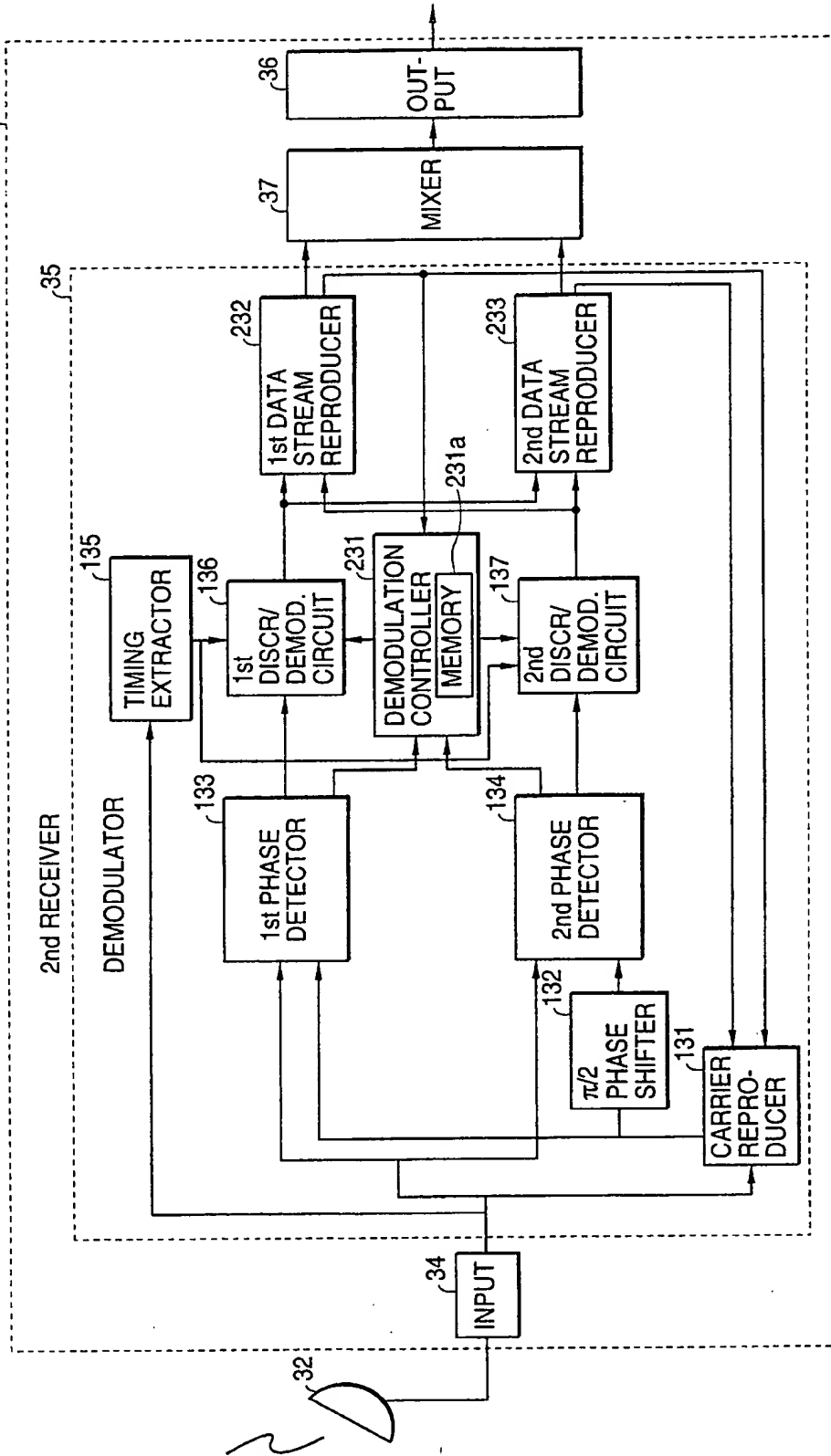
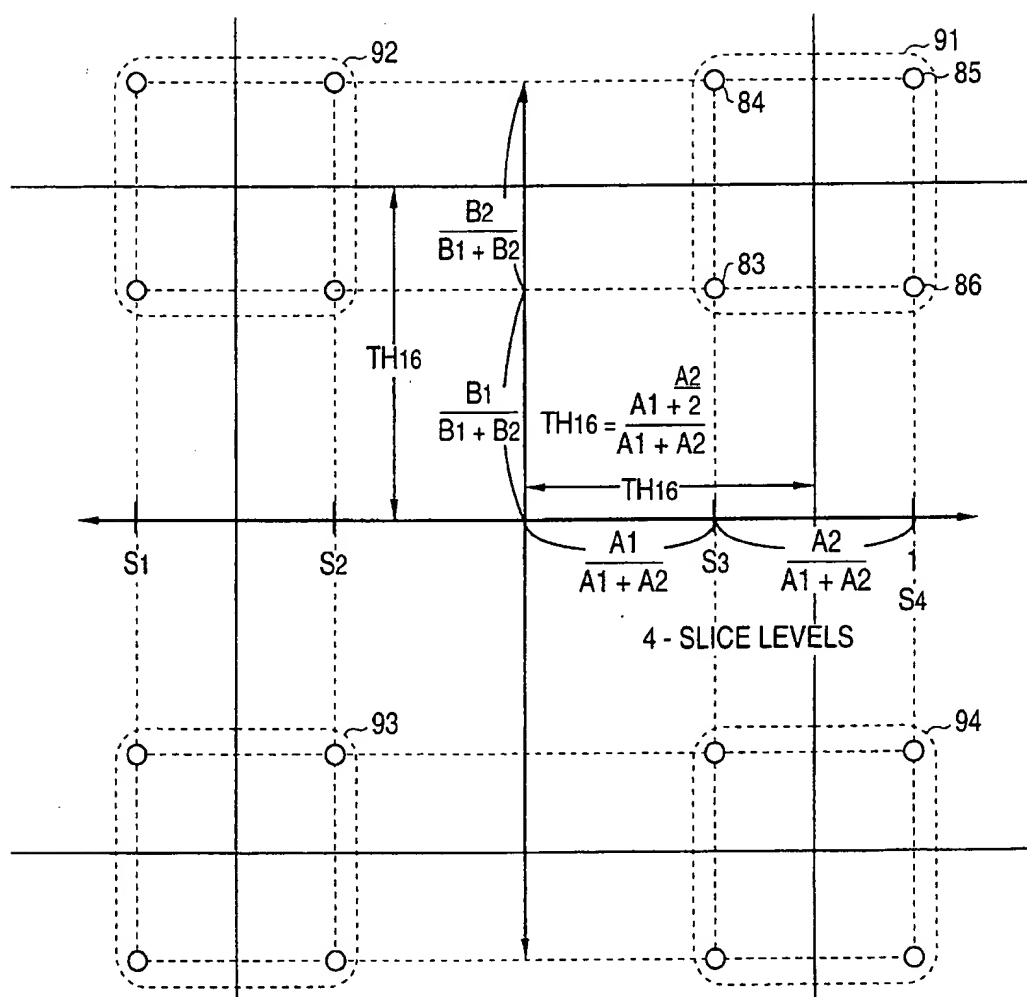


FIG. 22



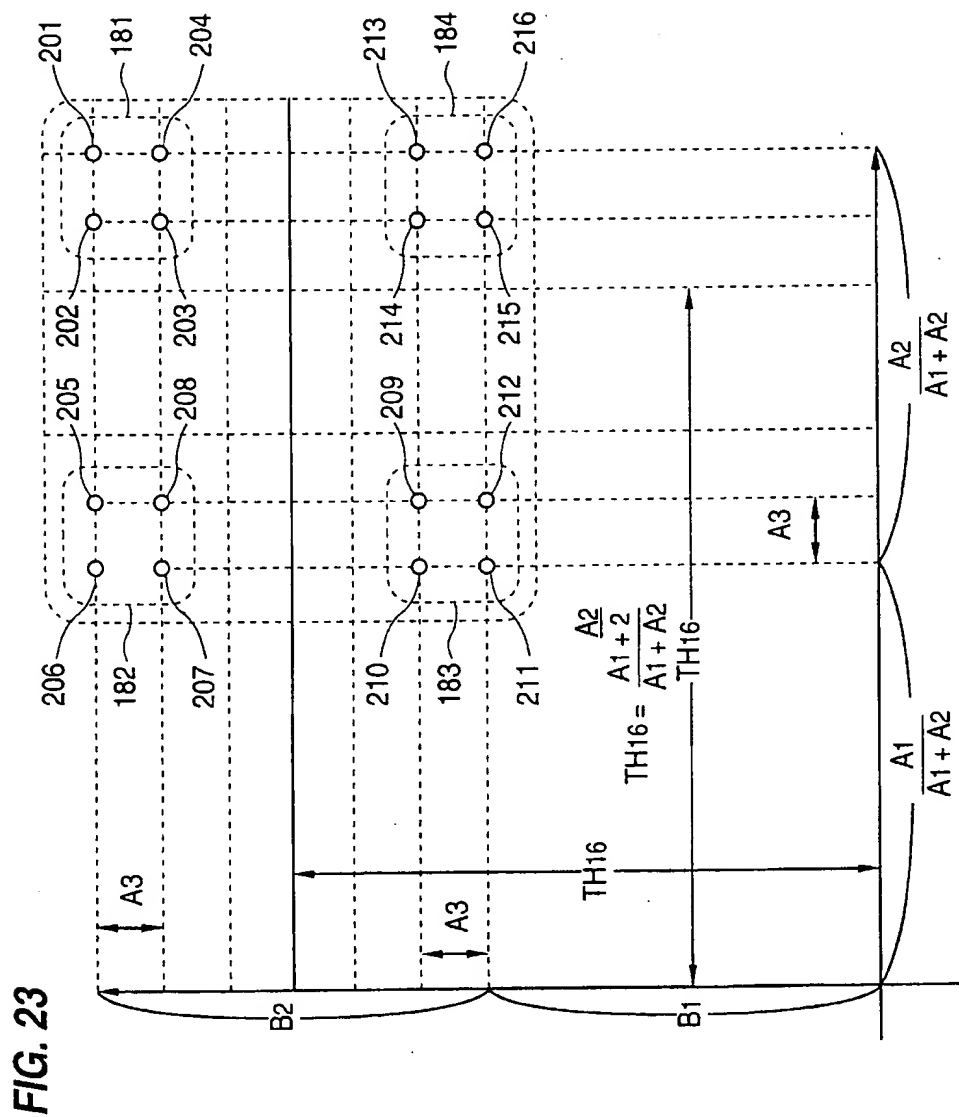


FIG. 24

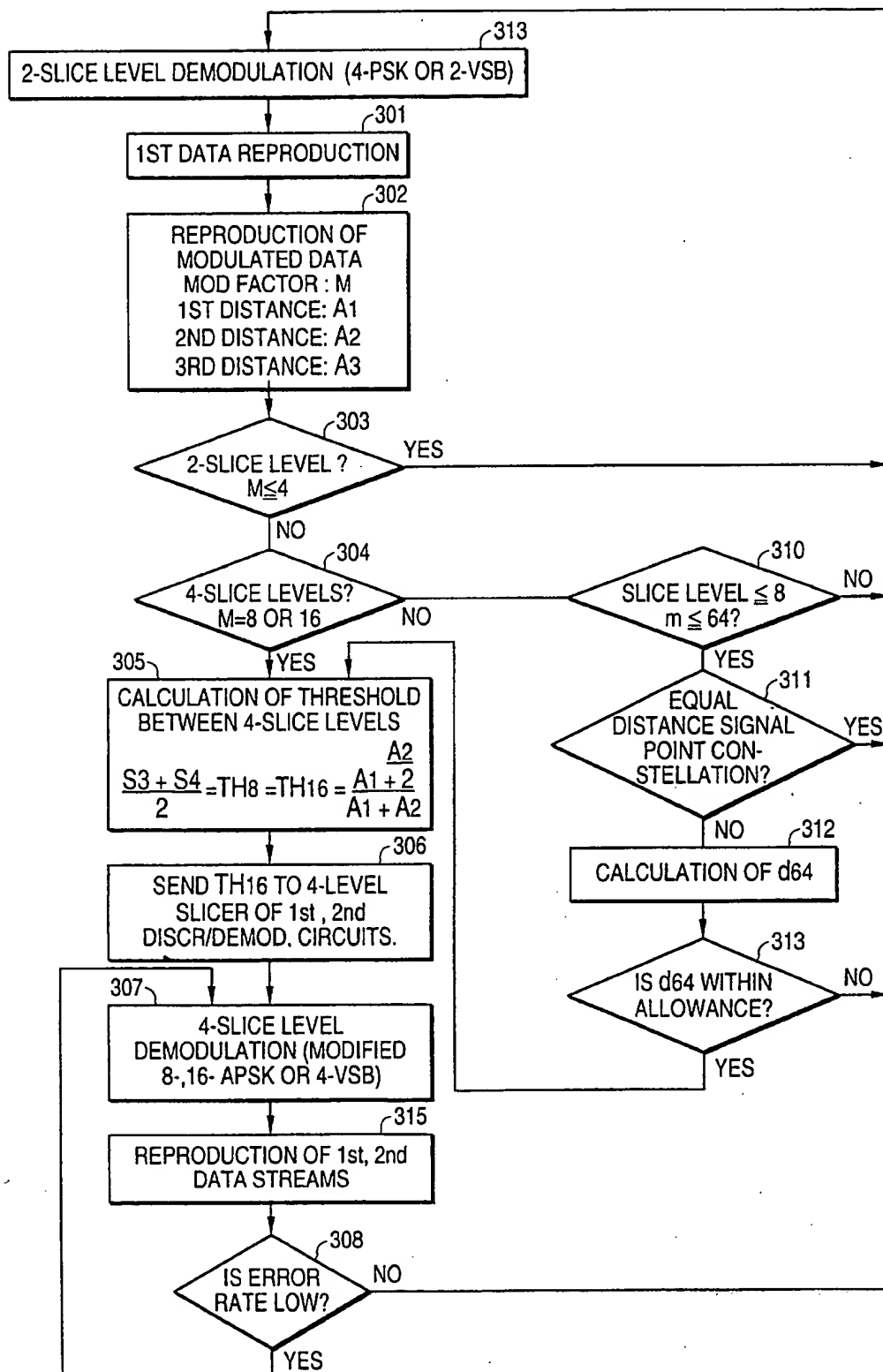


FIG. 25(a)

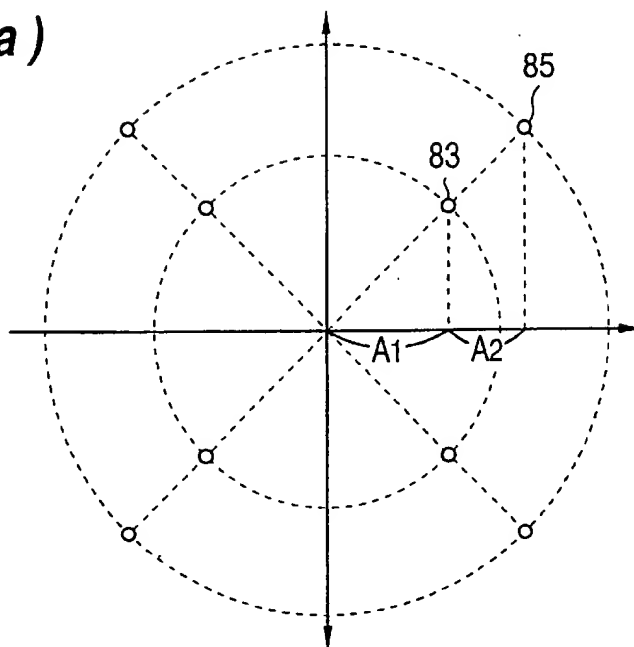
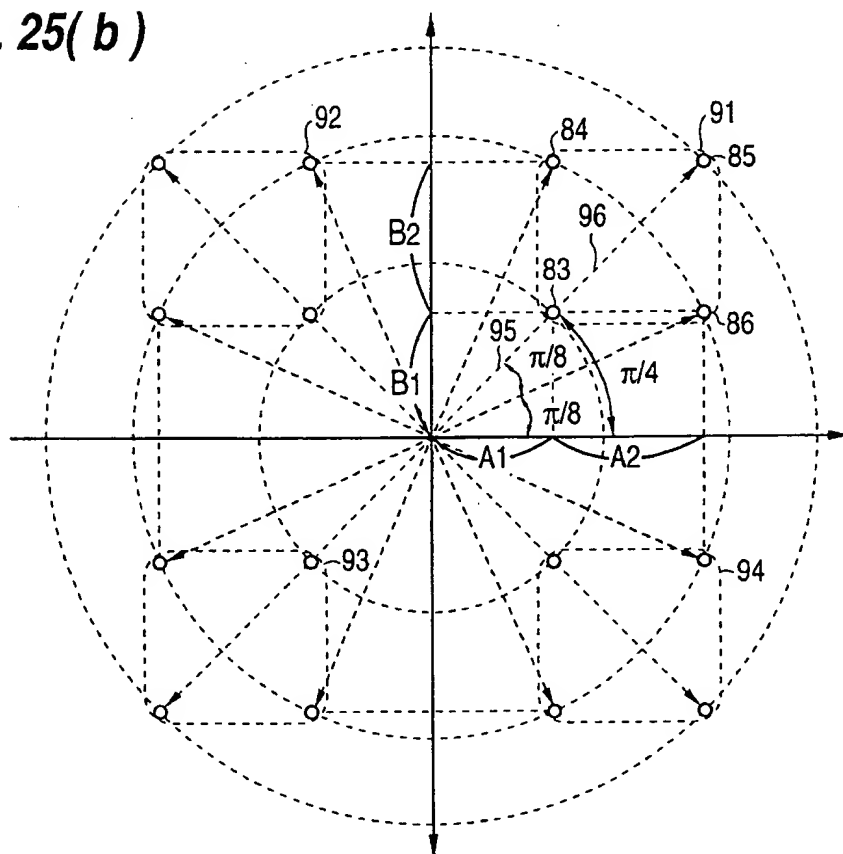


FIG. 25(b)



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FIG. 26

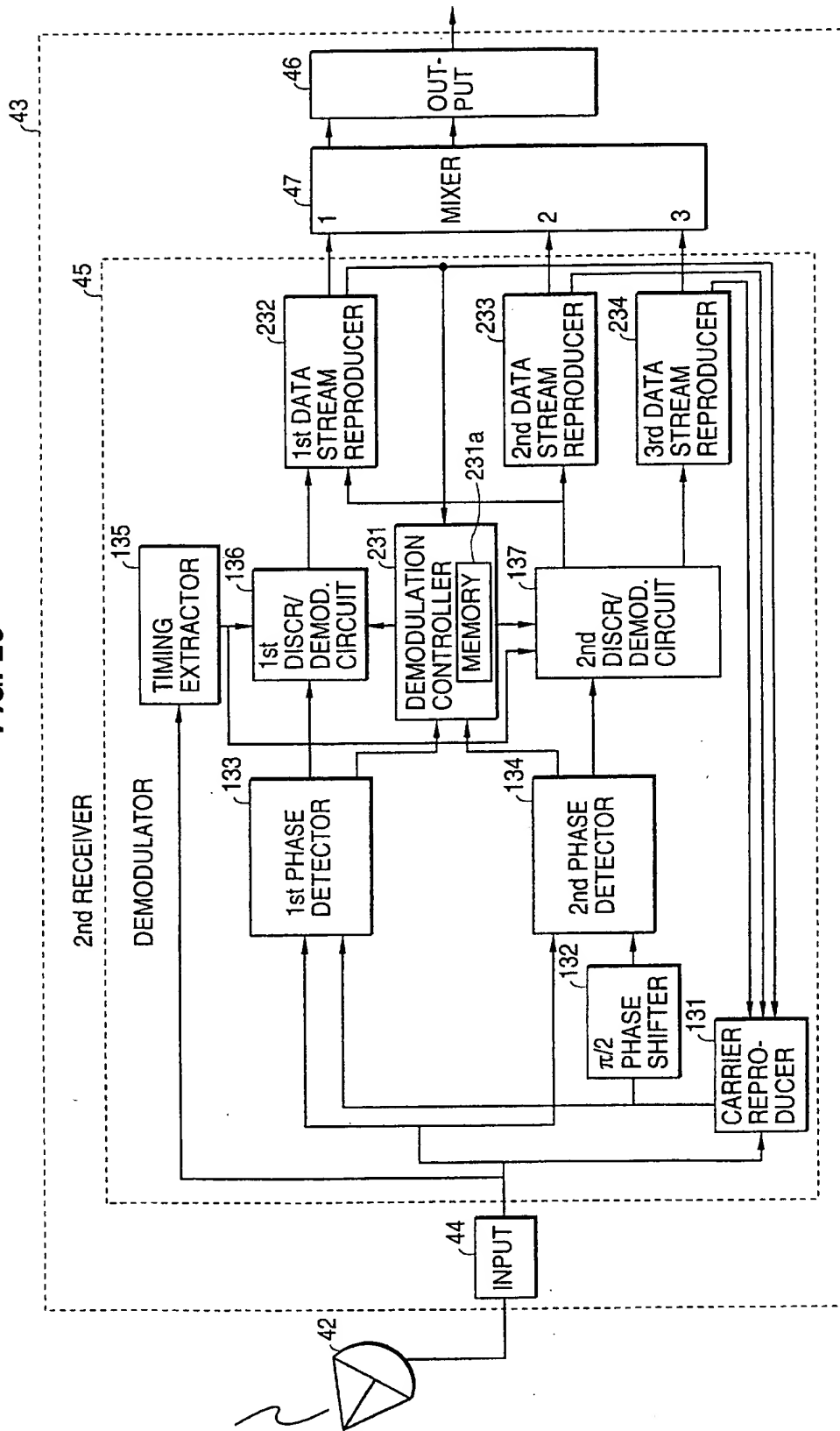


FIG. 27

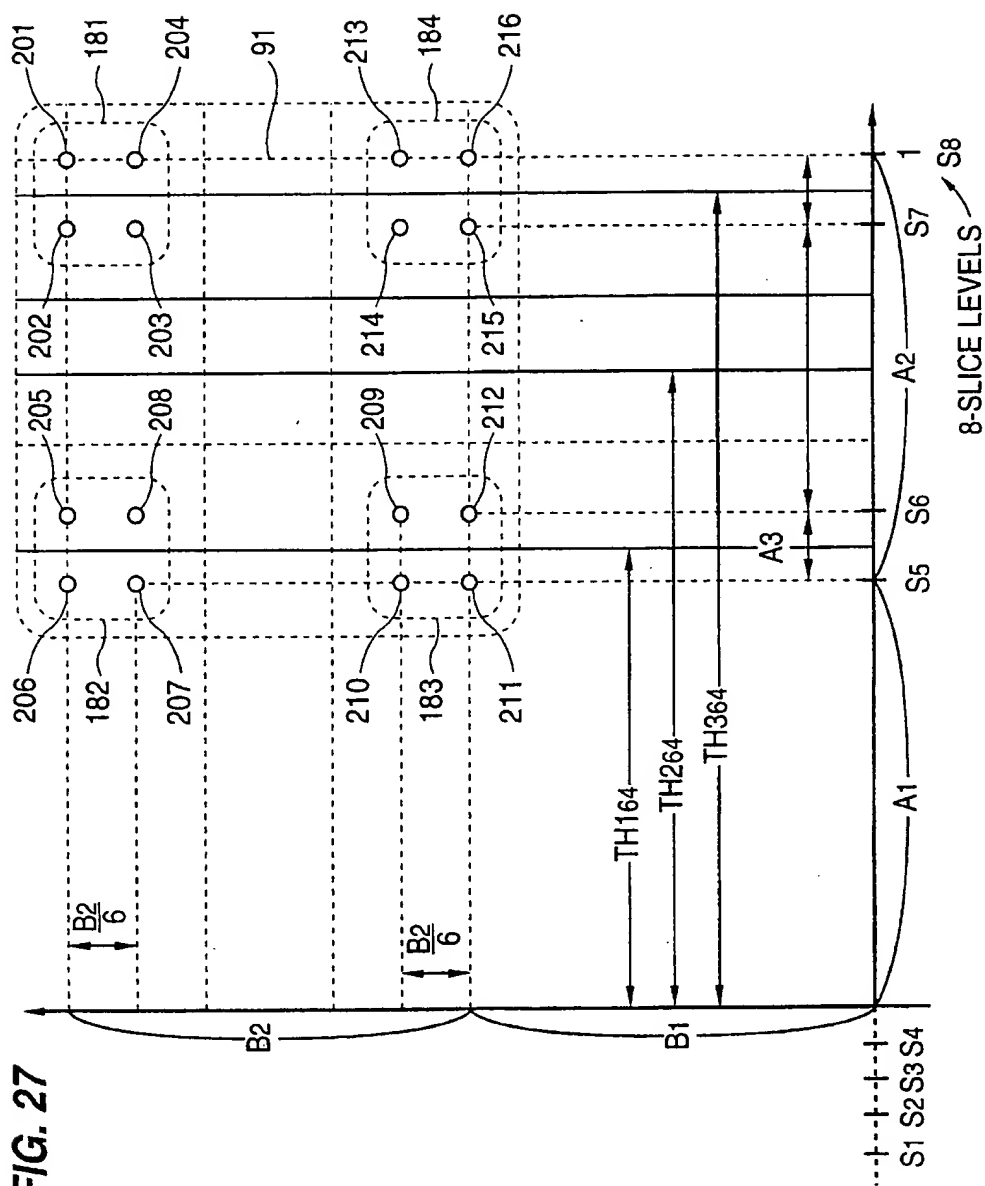
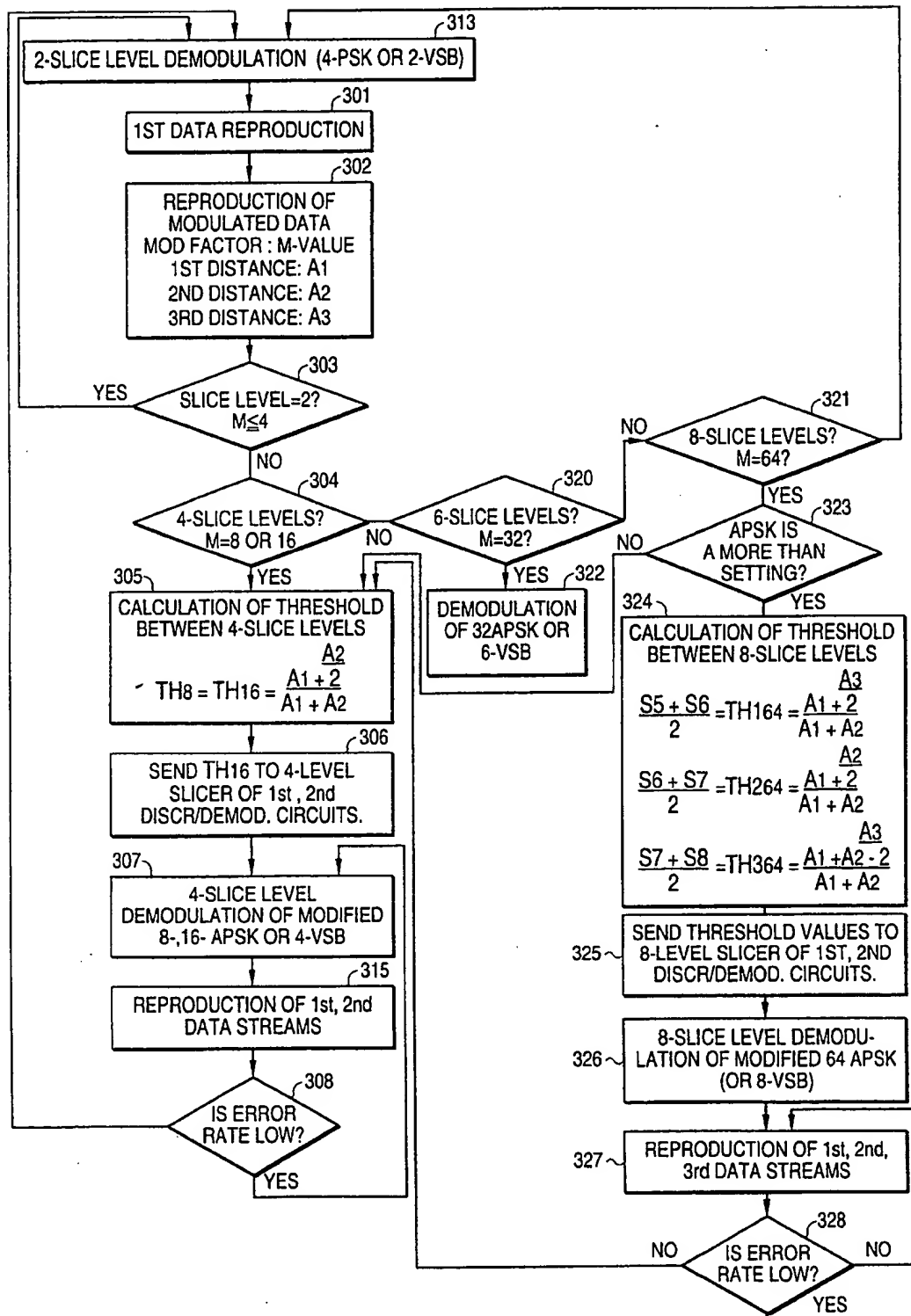


FIG. 28



664020-2E04260

FIG. 29

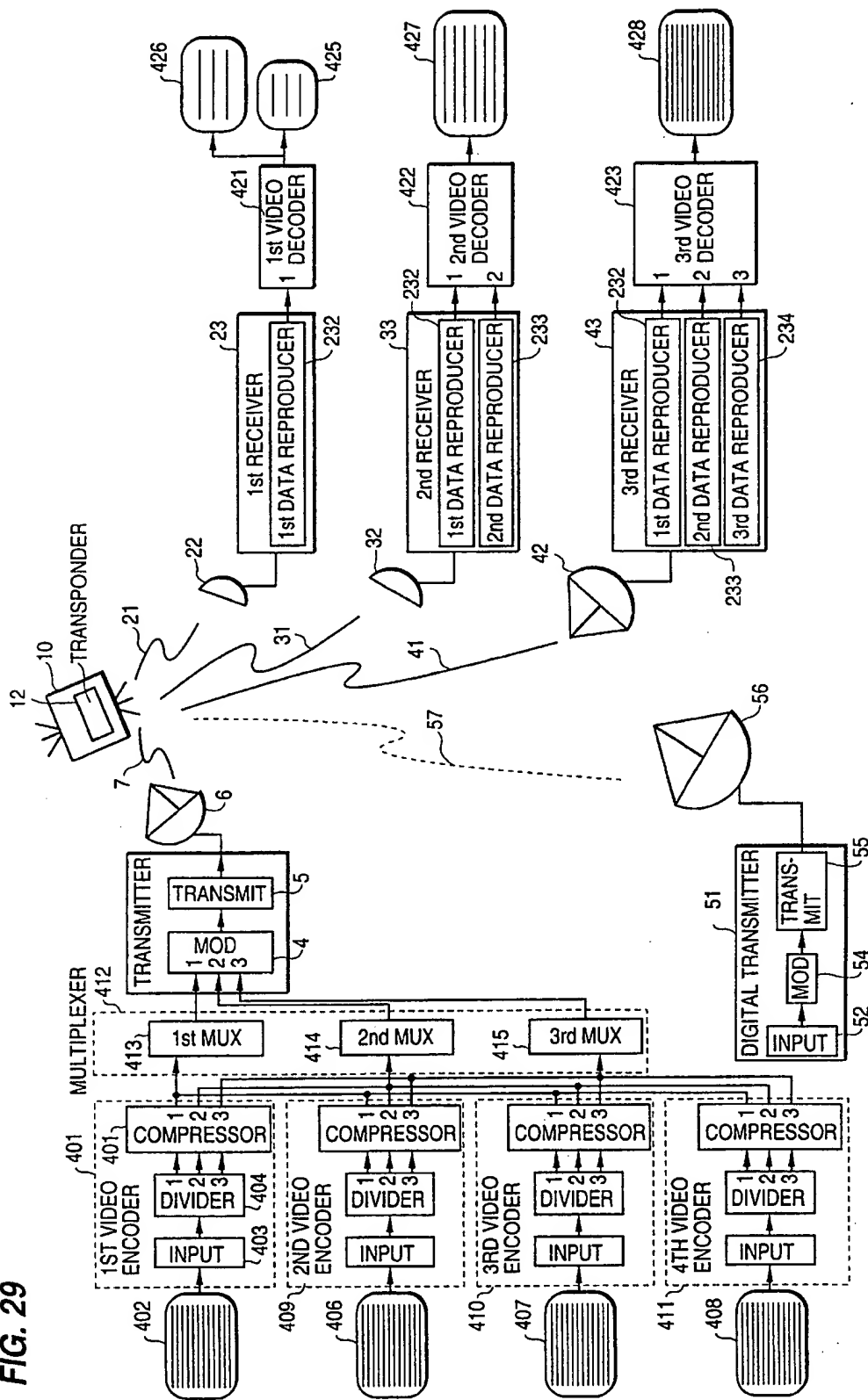


FIG. 30

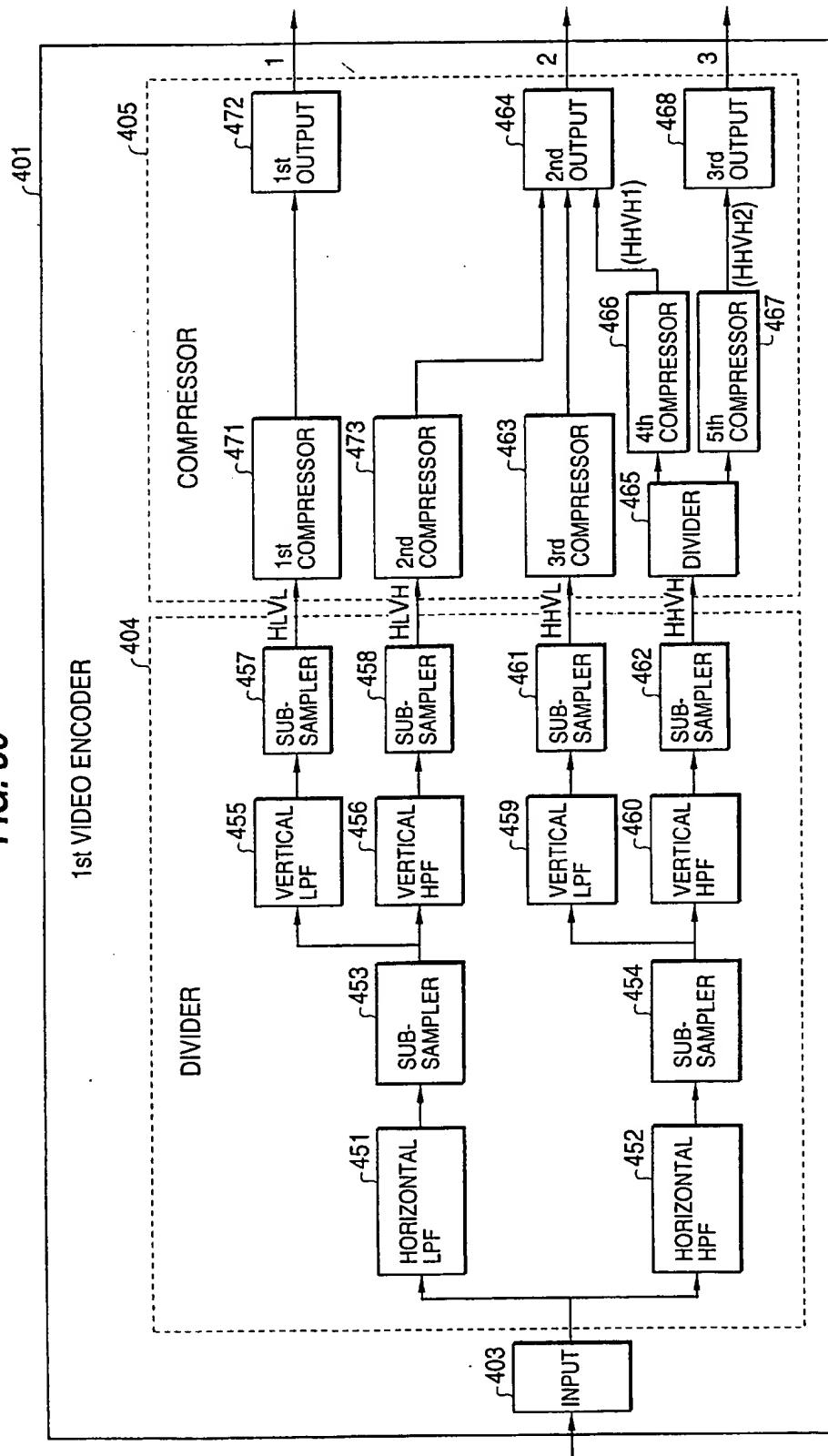


FIG. 31

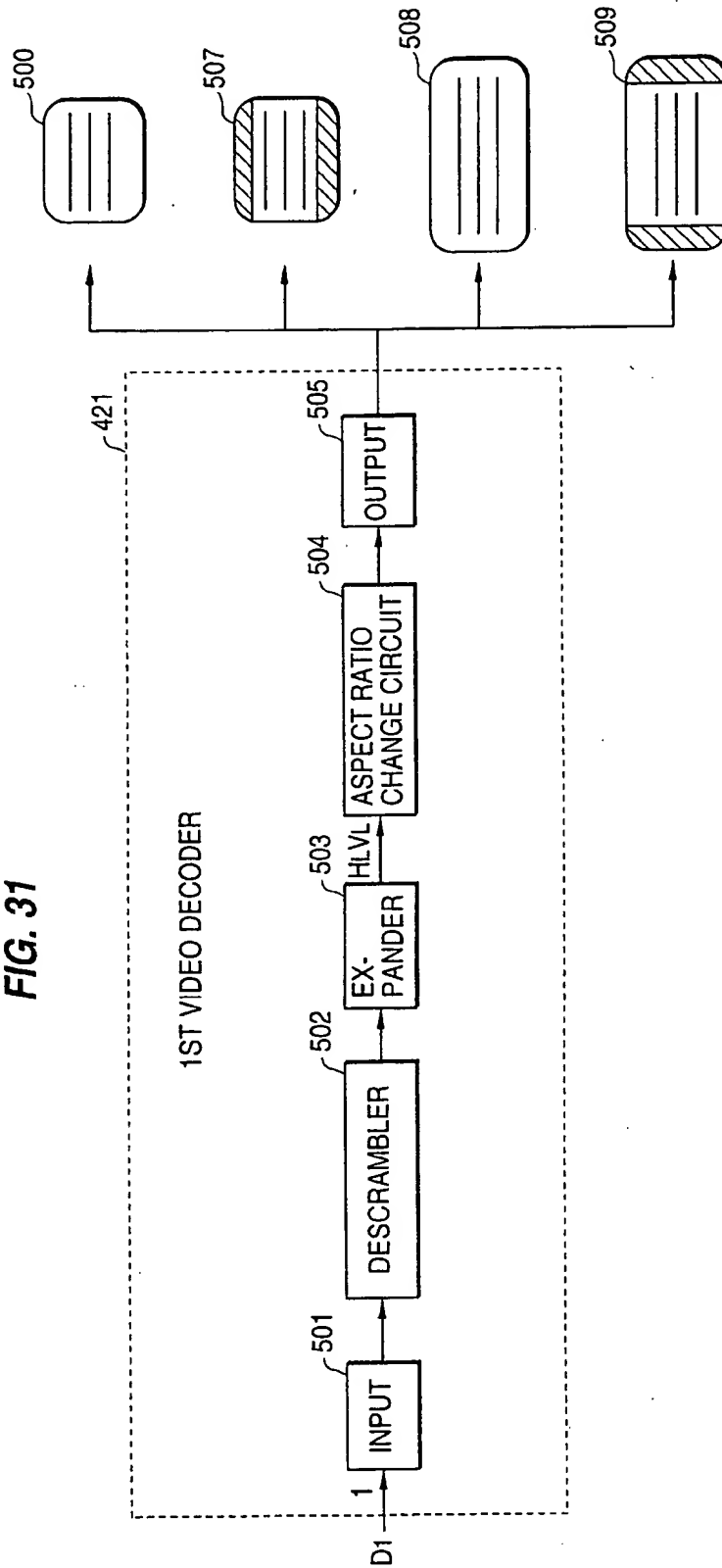


FIG. 32

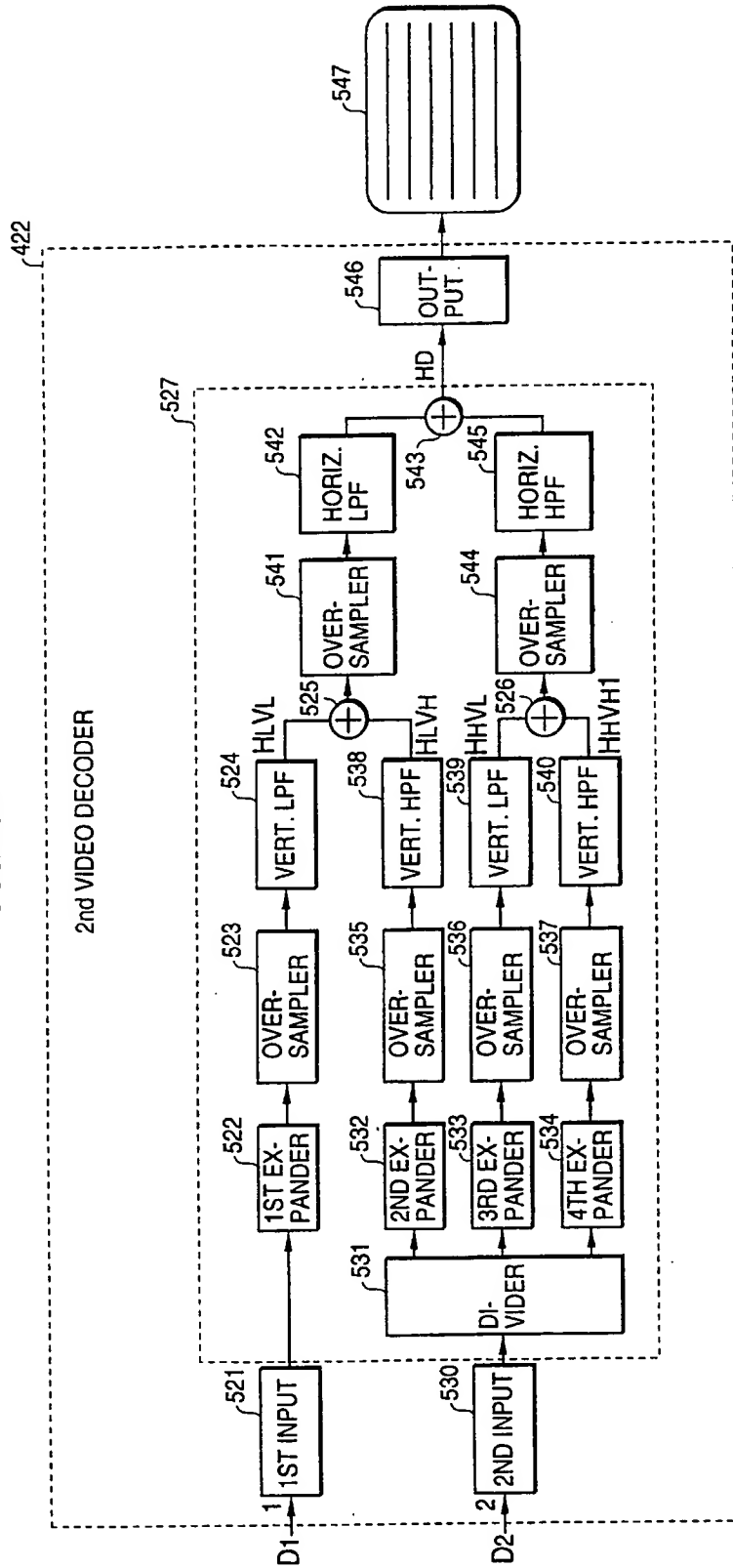


FIG. 33

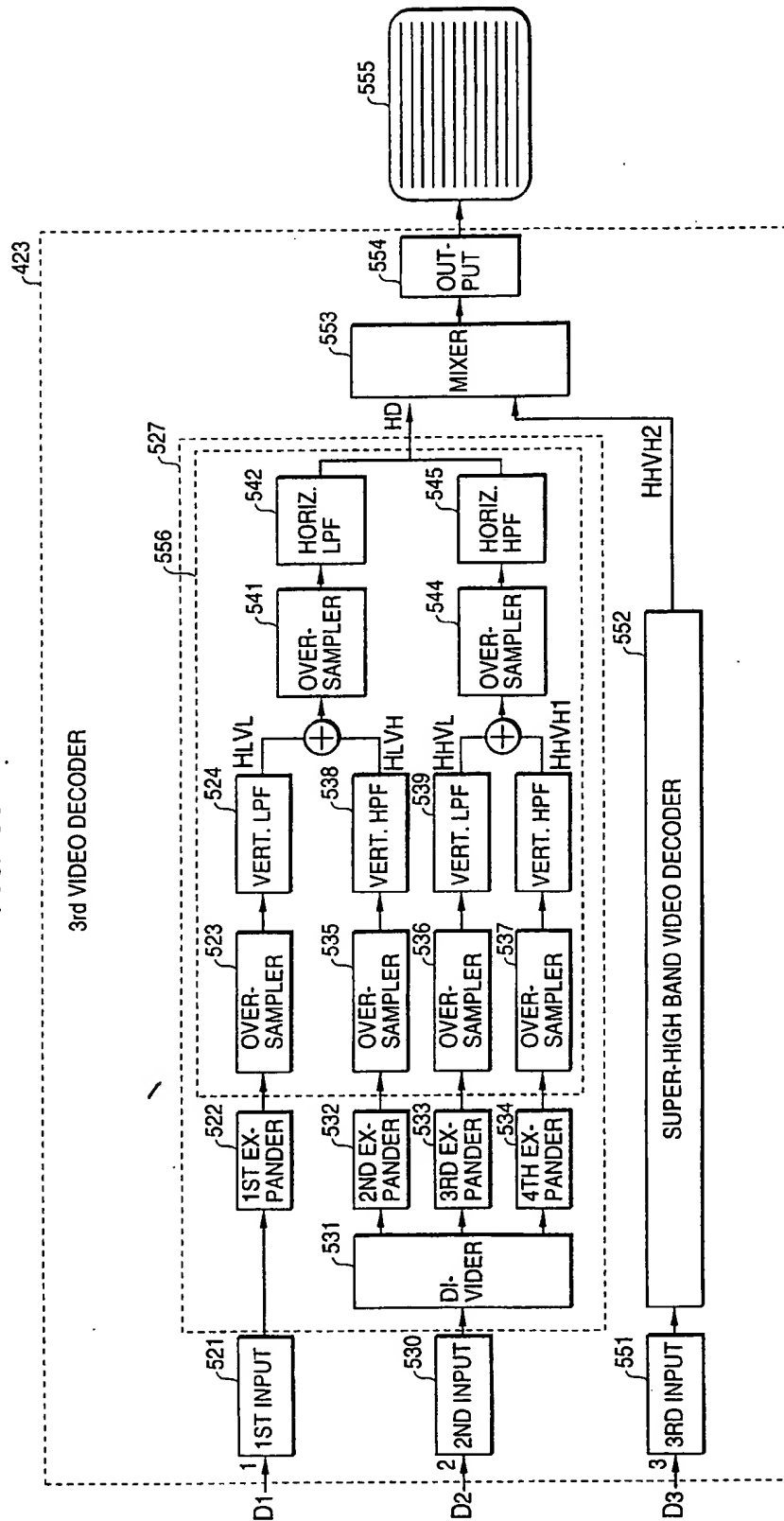


FIG. 34

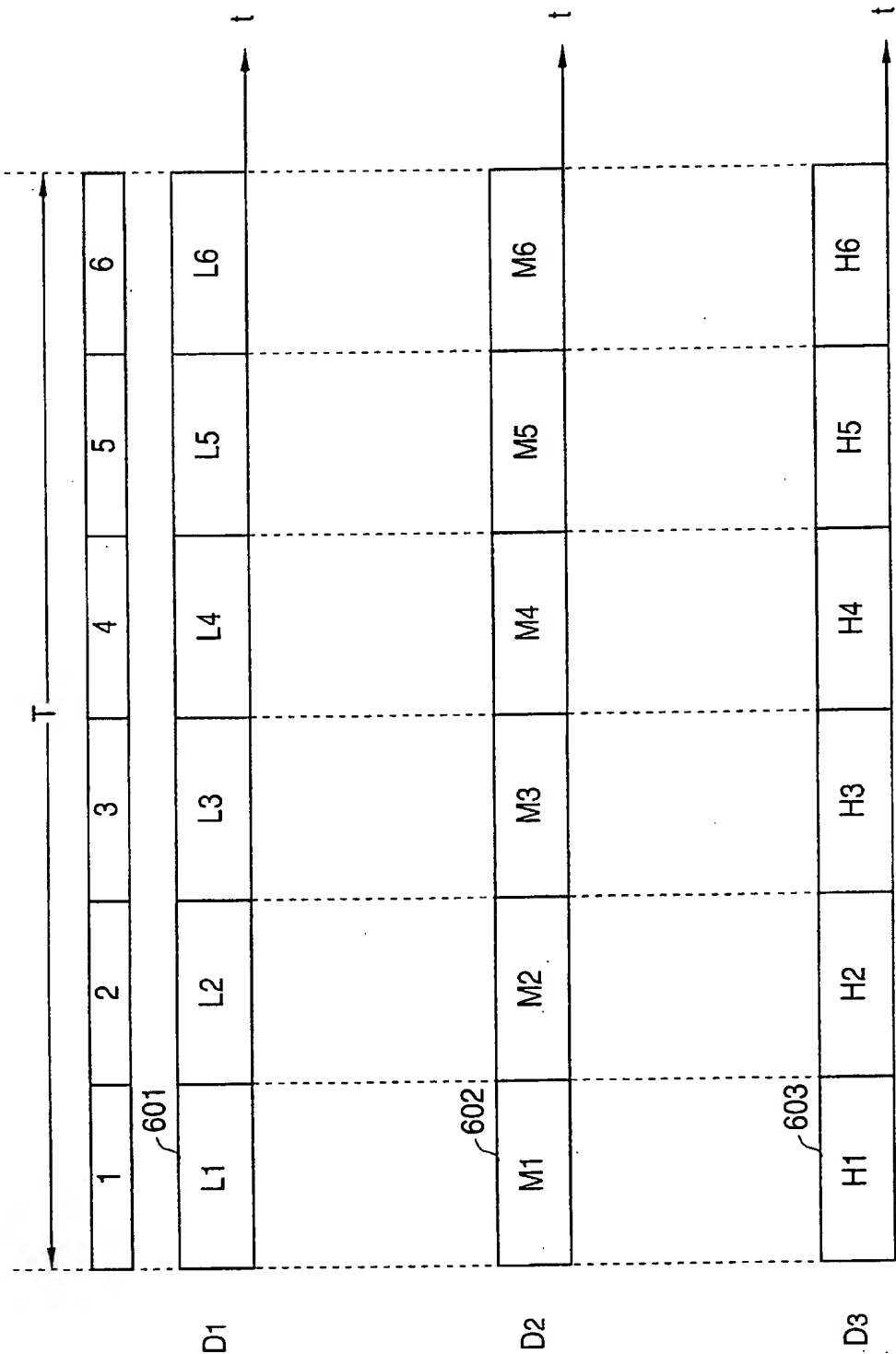


FIG. 35

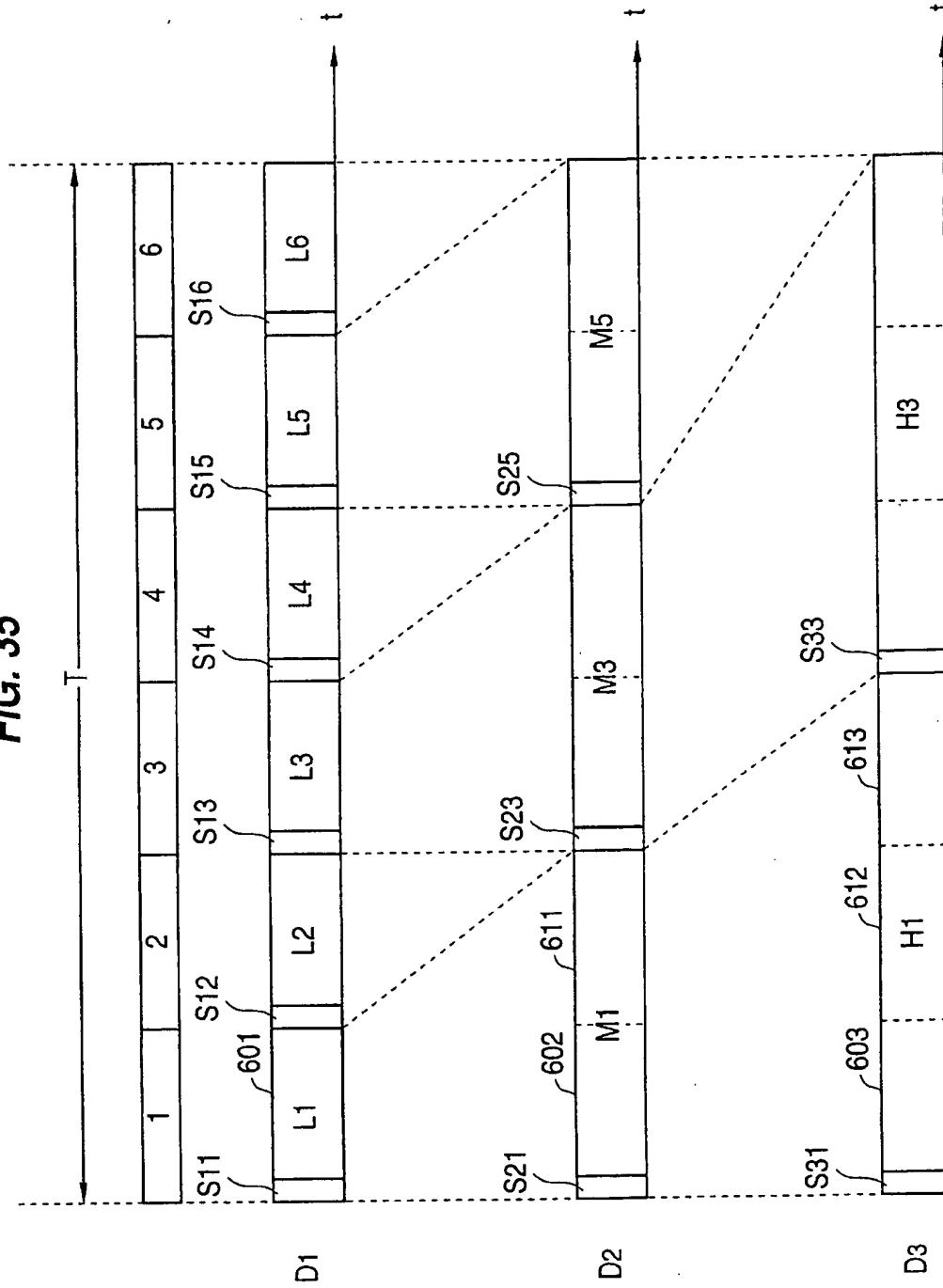


FIG. 36

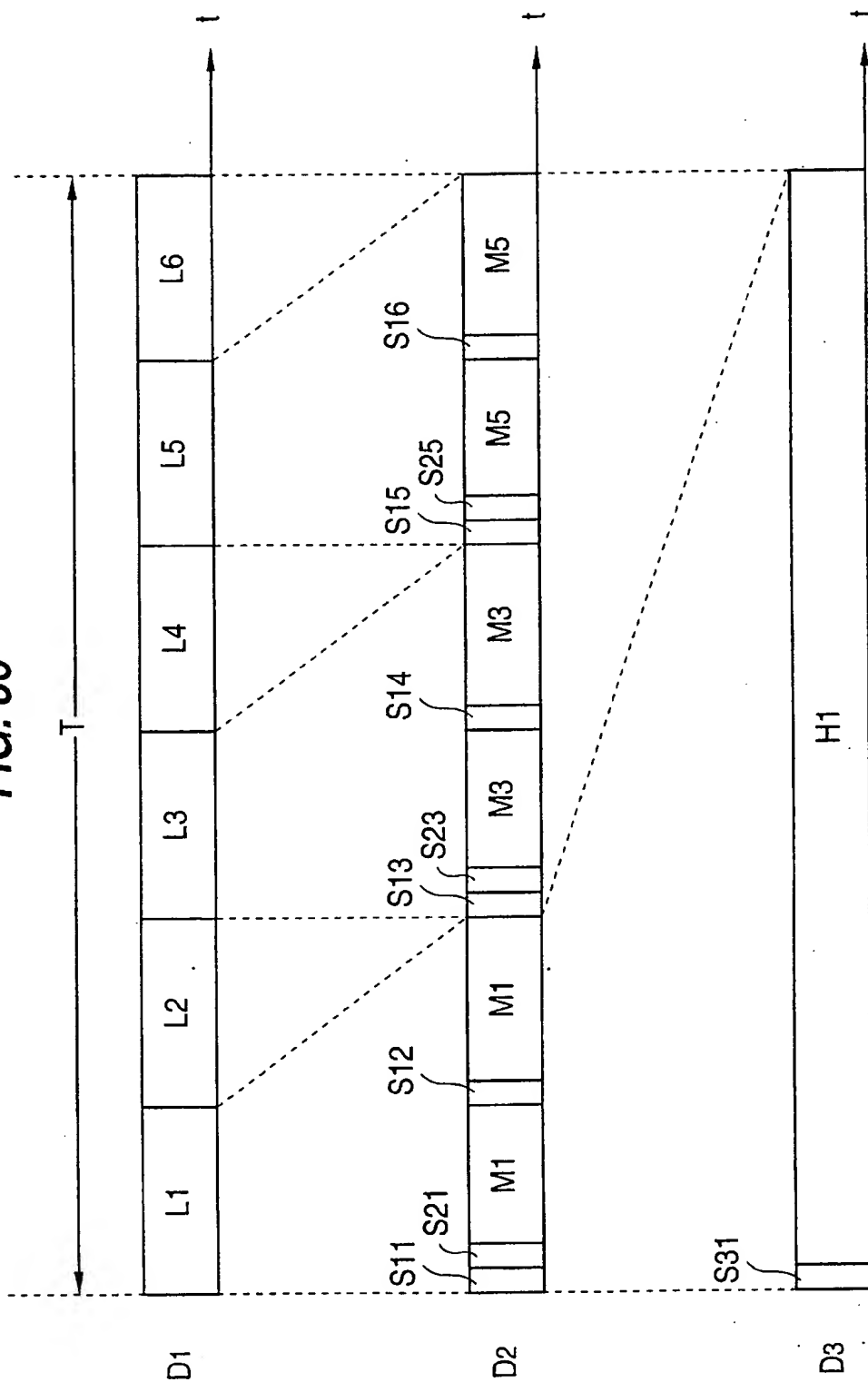


FIG. 37

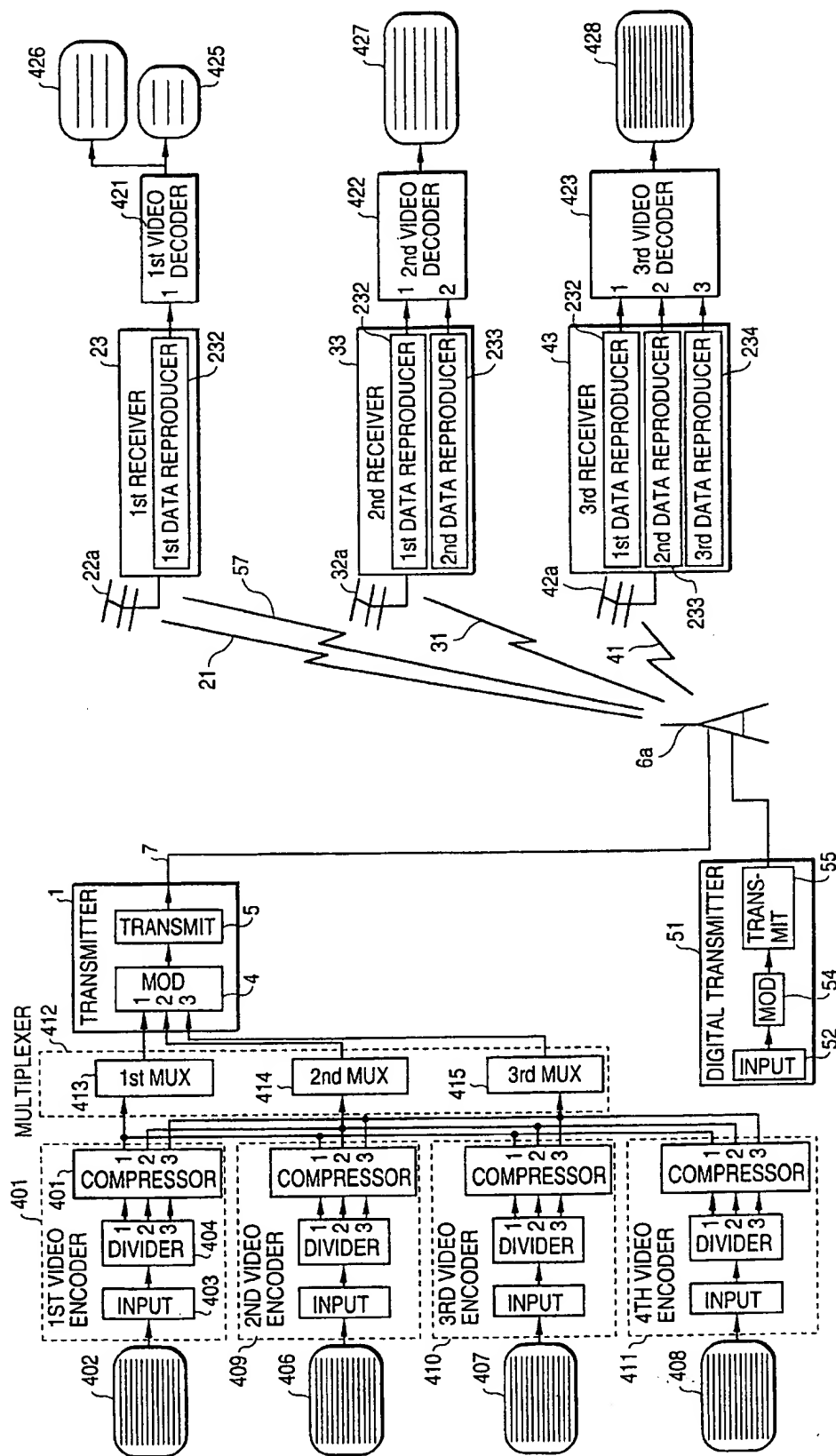


FIG. 38

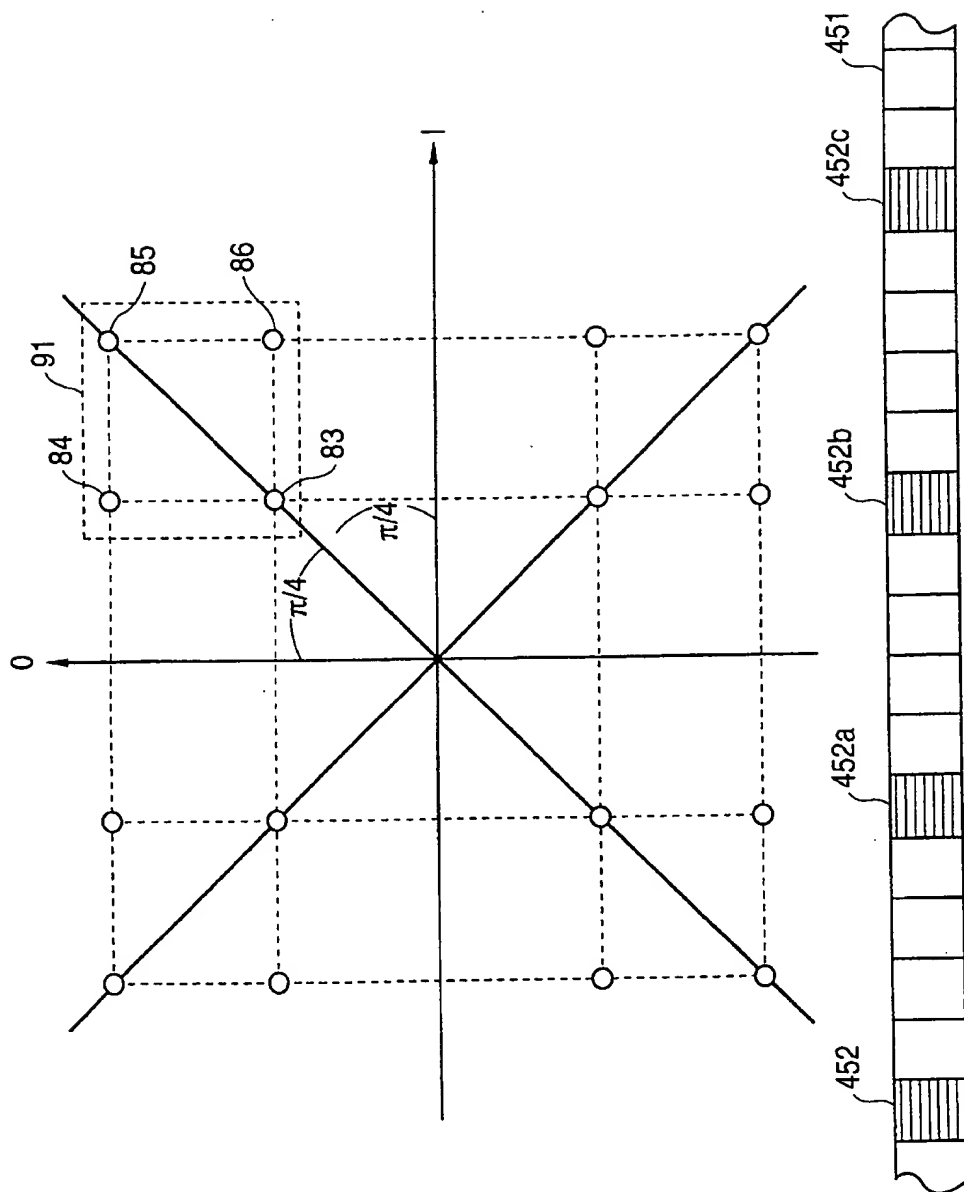


FIG. 39

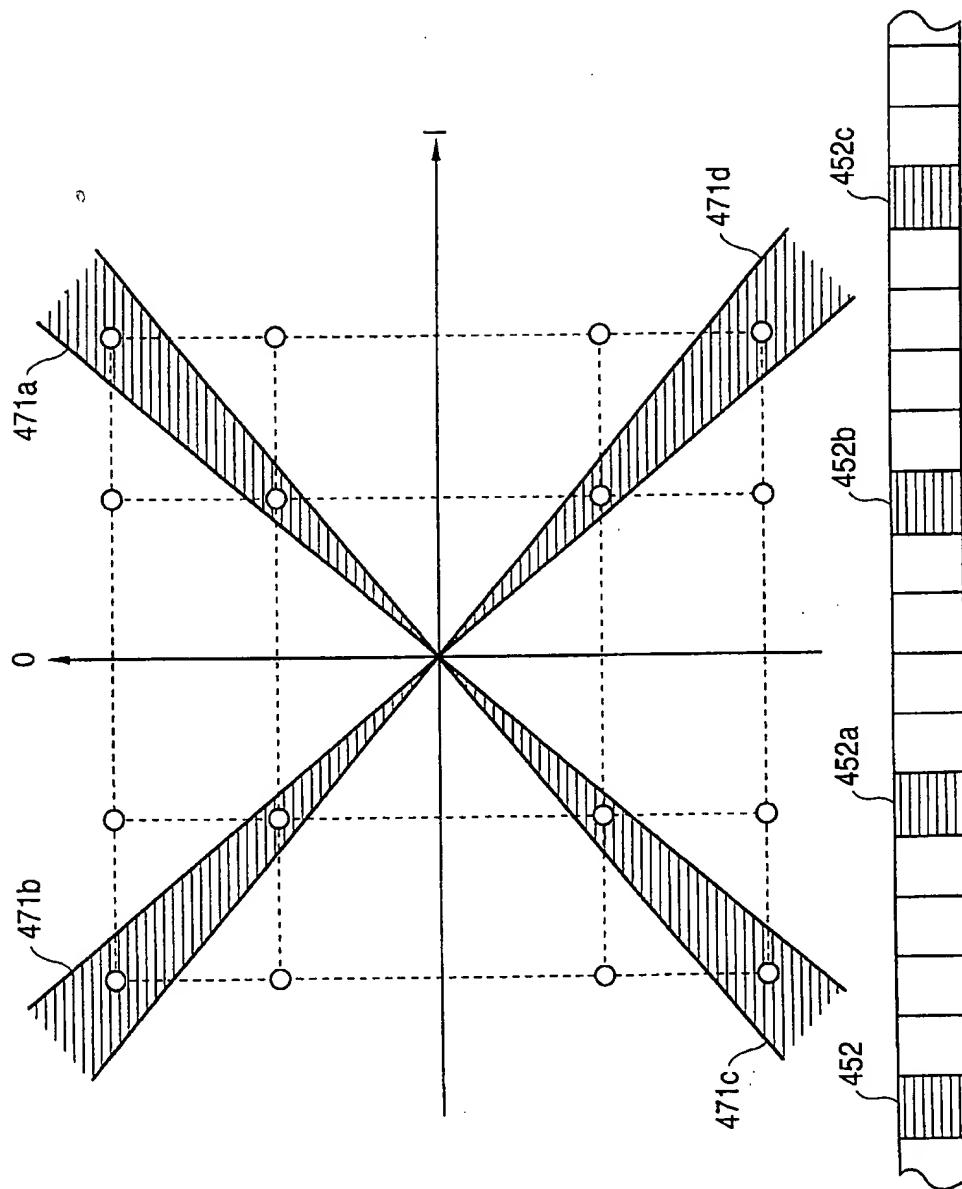


FIG. 40

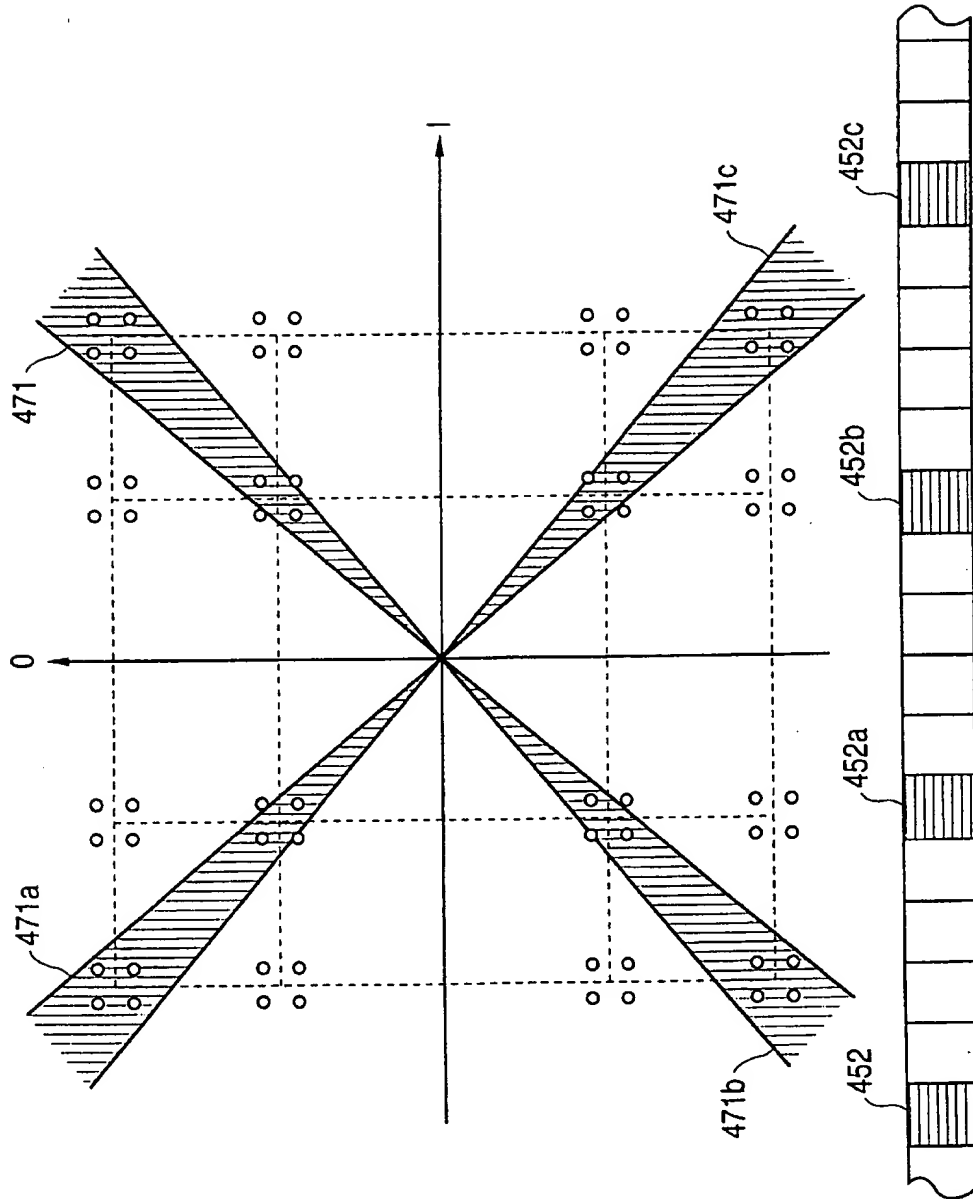


FIG. 42

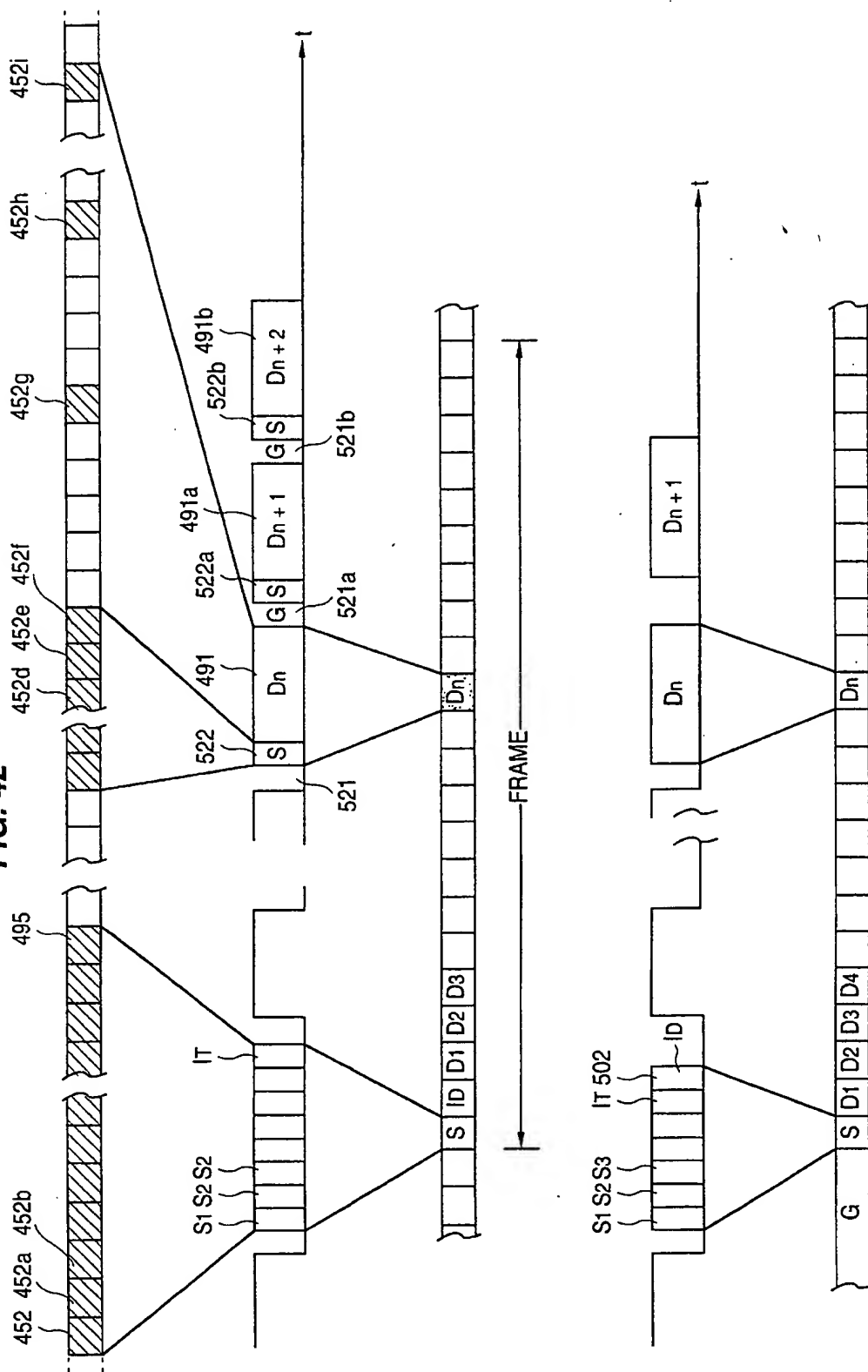


FIG. 43

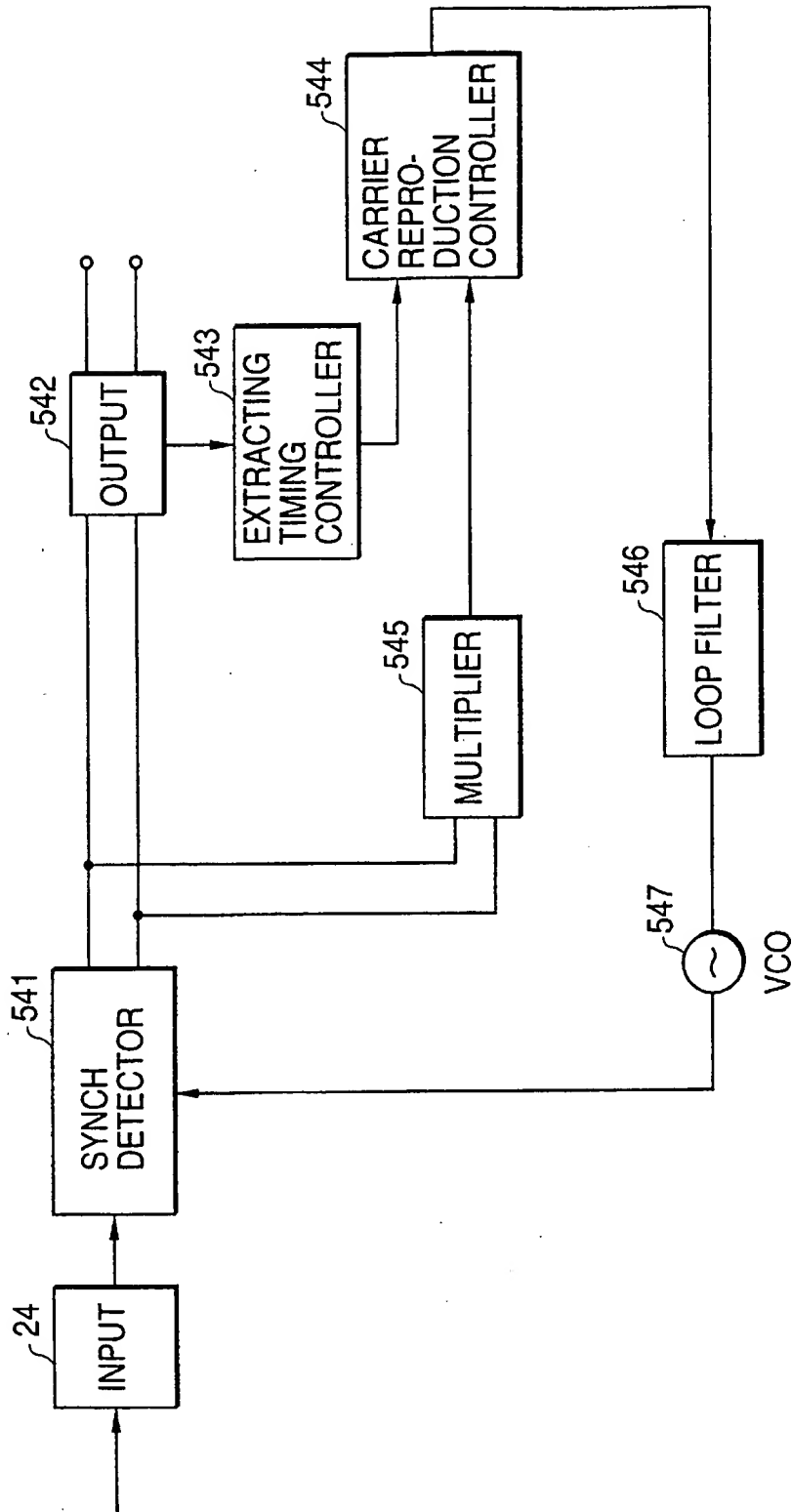


FIG. 44

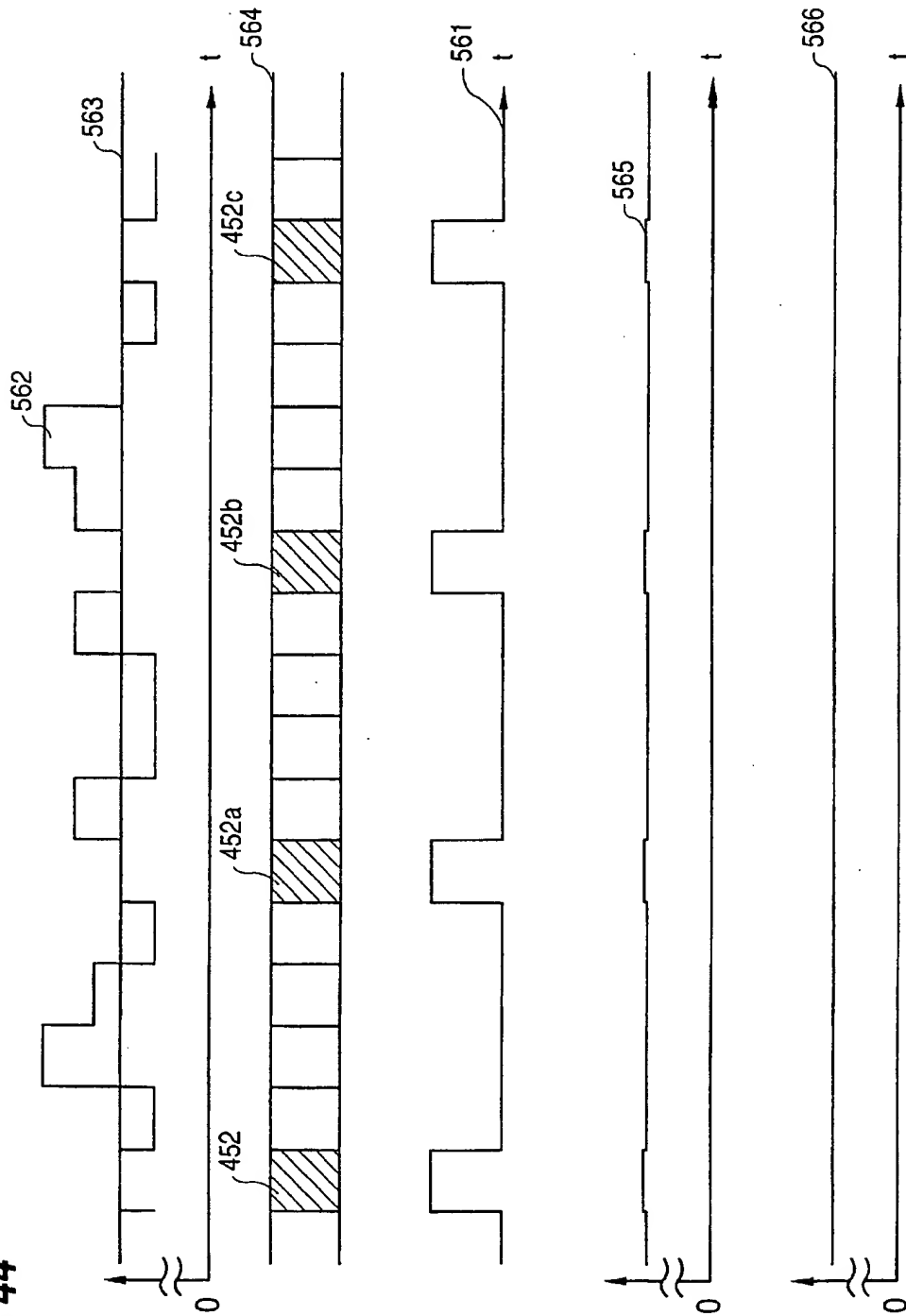


FIG. 45

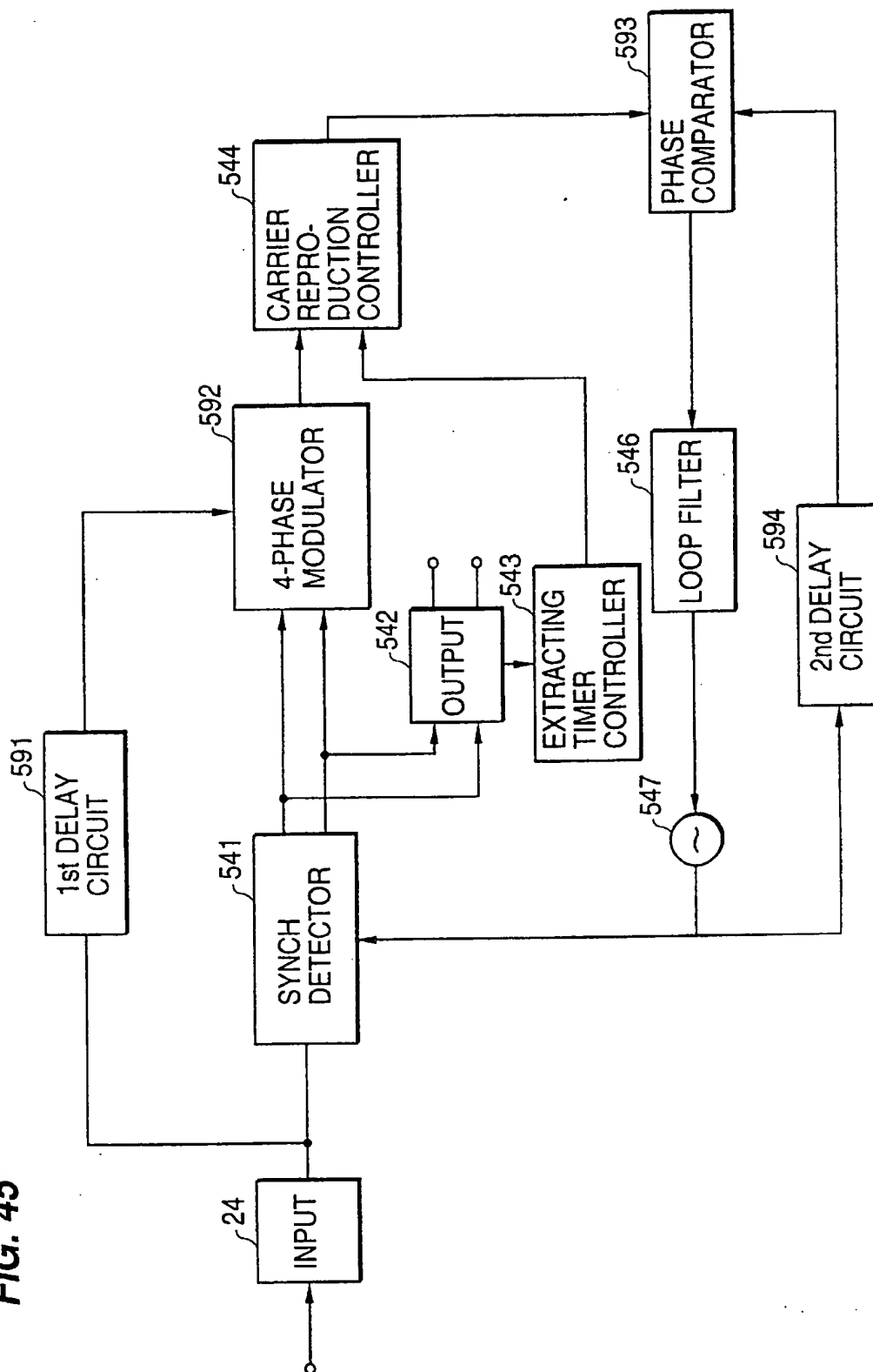


FIG. 46

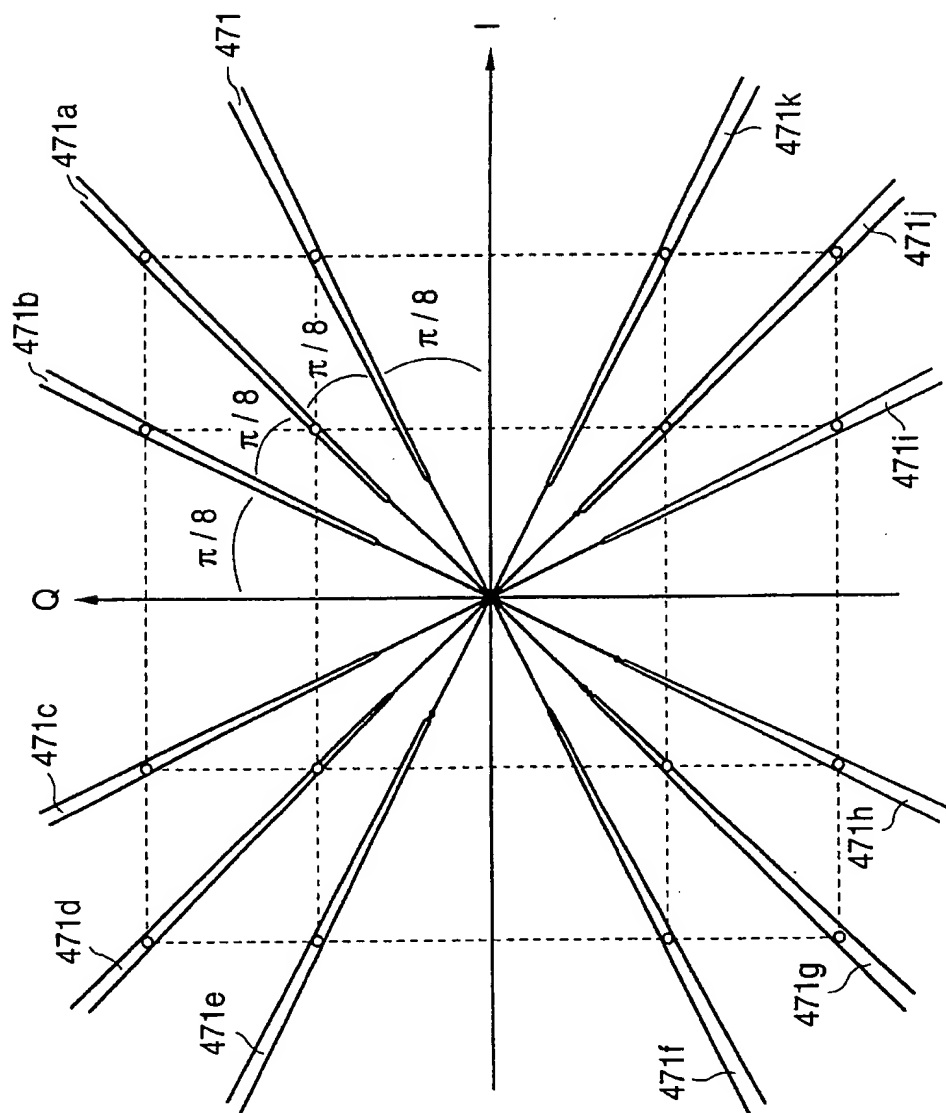


FIG. 47

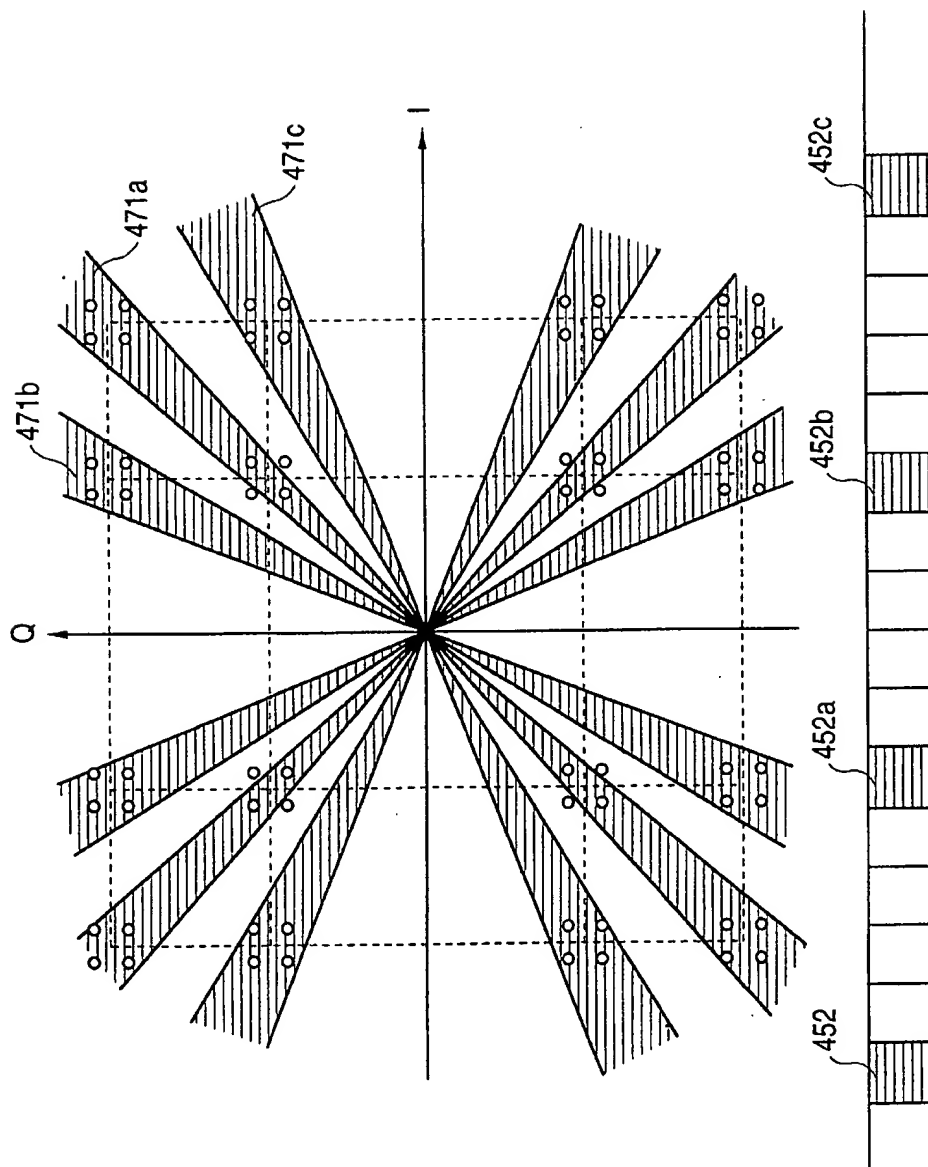


FIG. 48

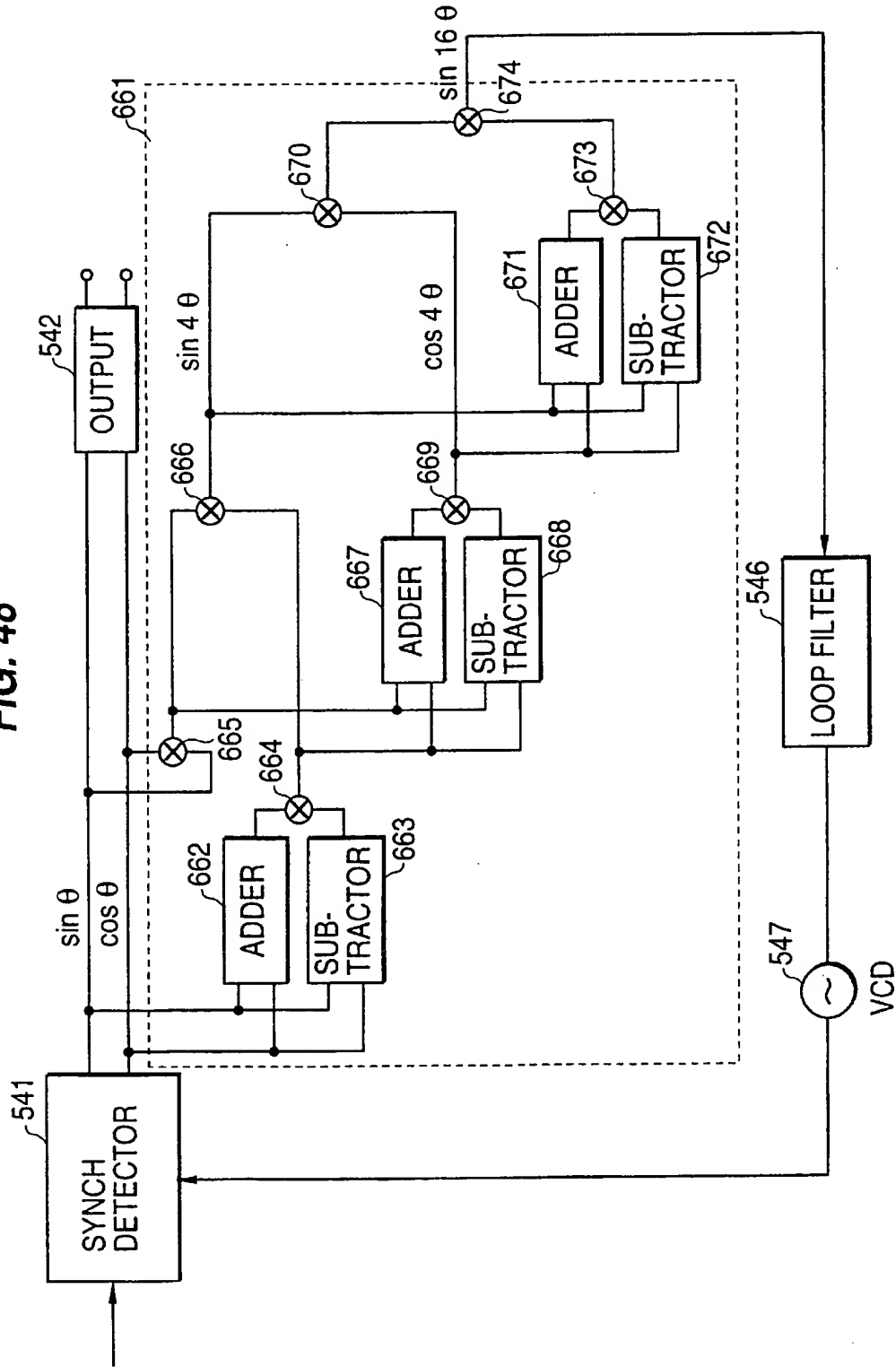


FIG. 49

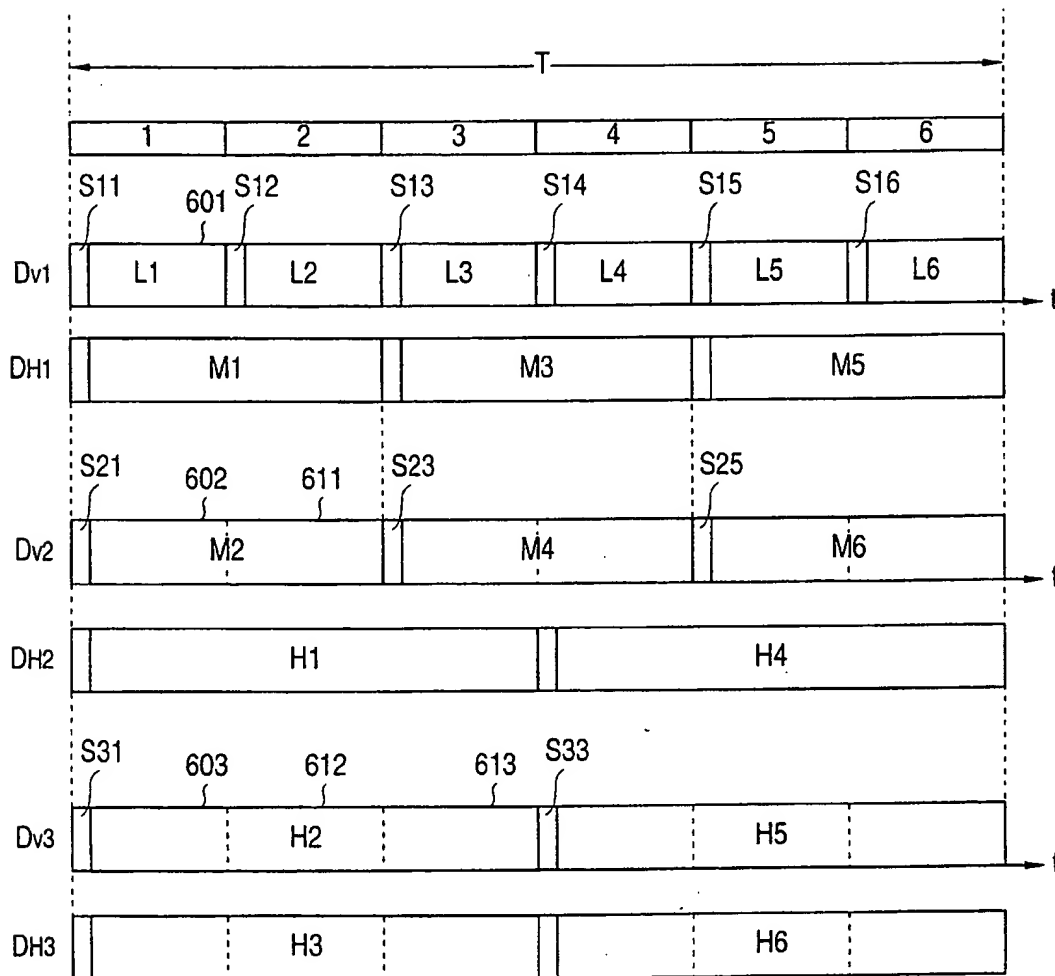


FIG. 50

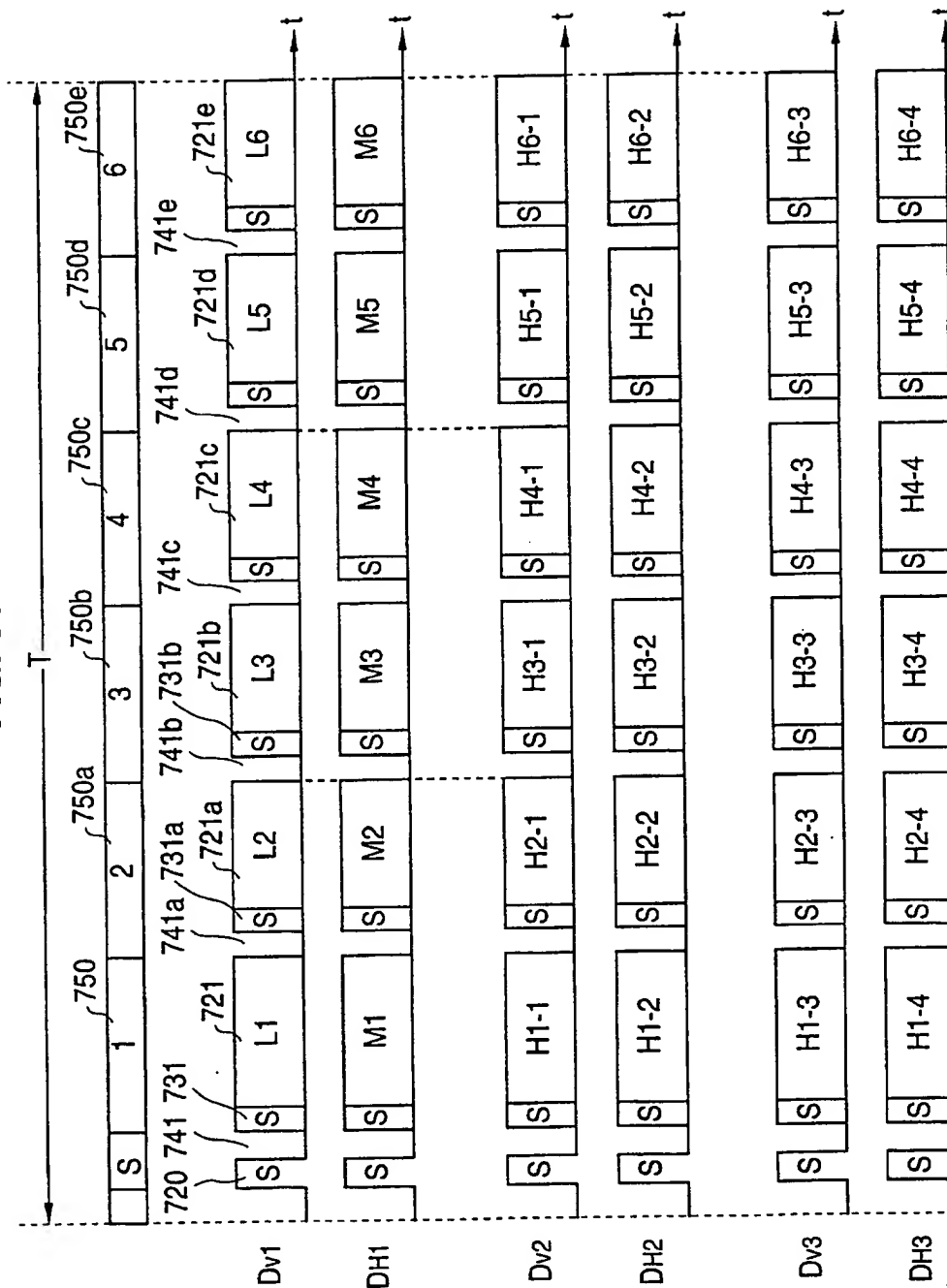


FIG. 51

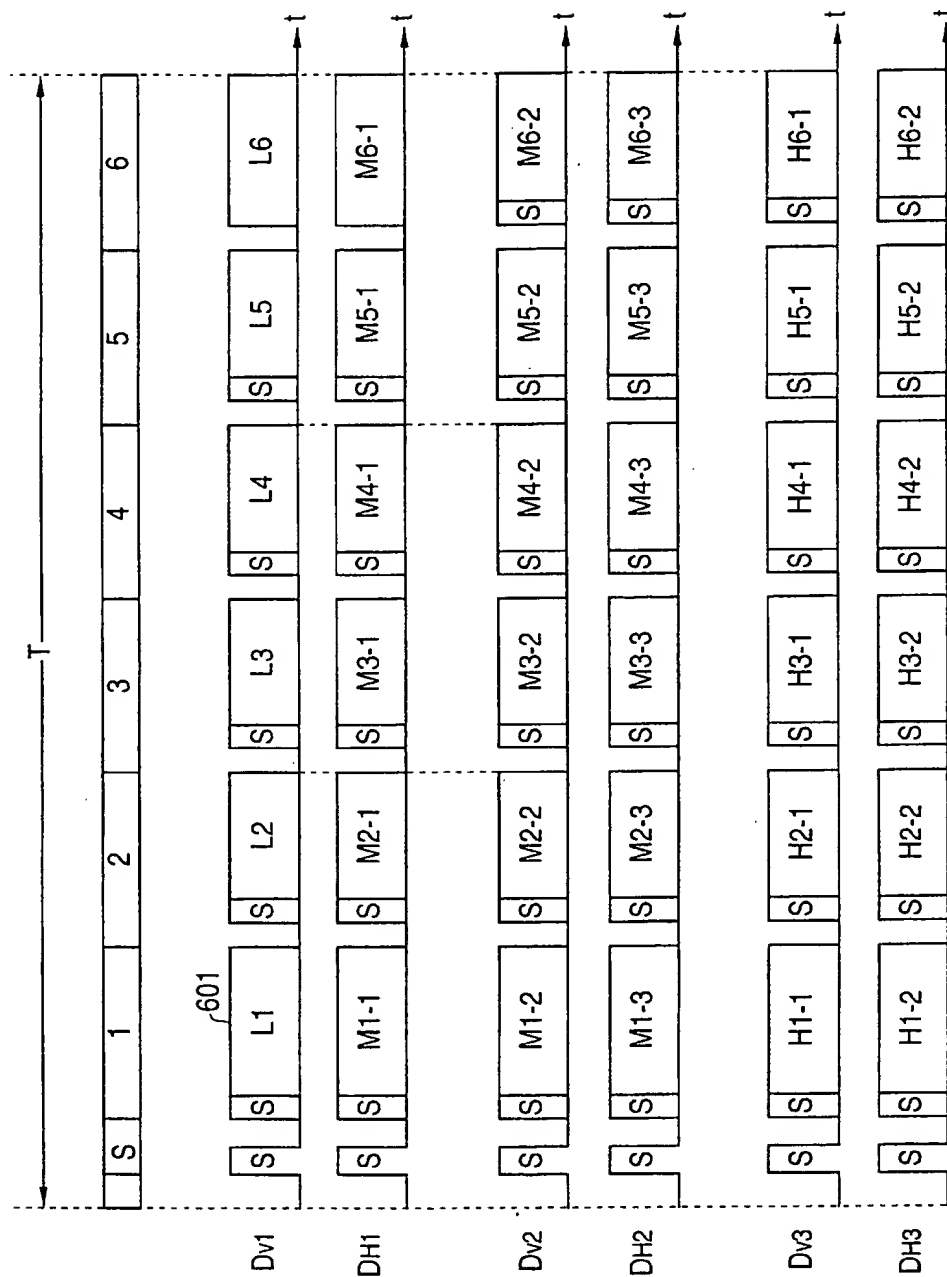
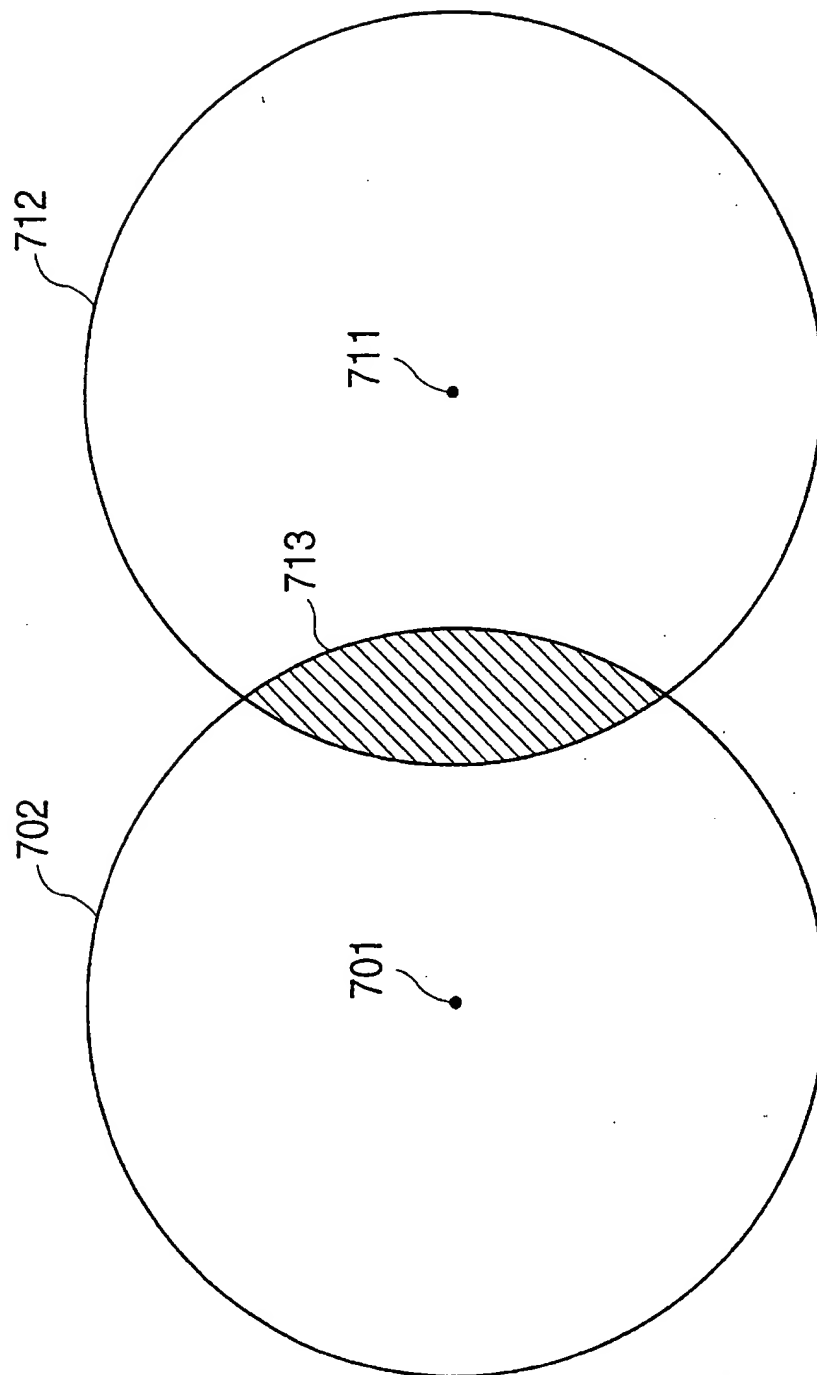
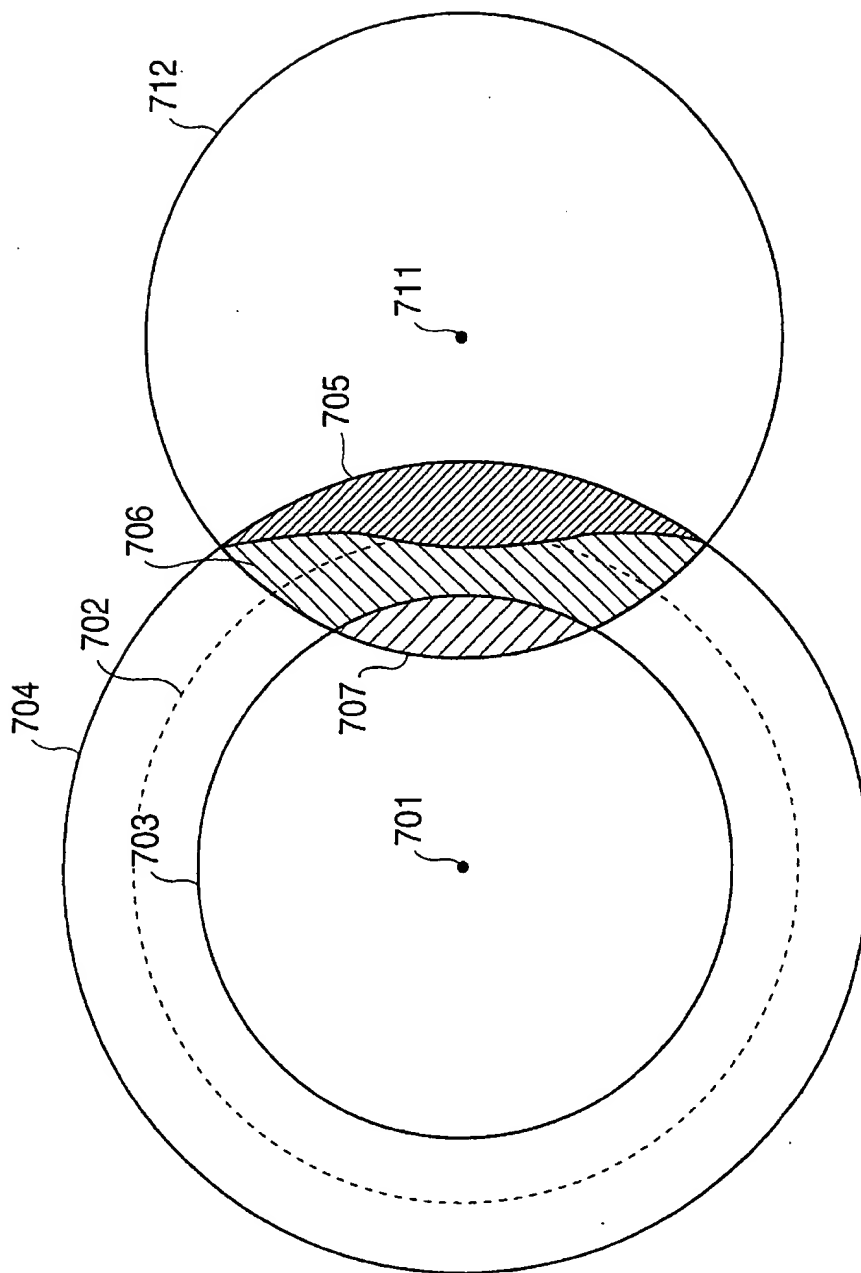


FIG. 52



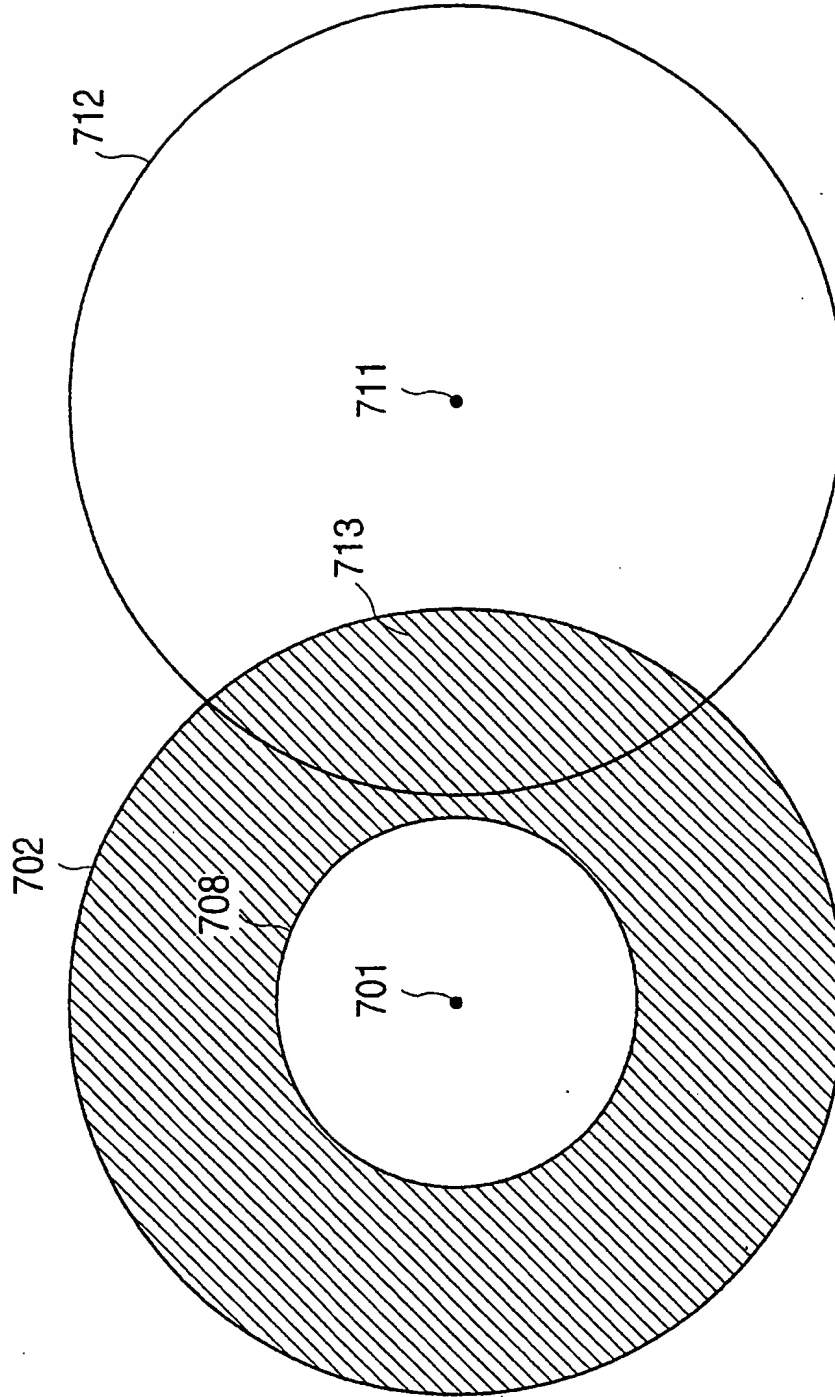
664020* 2E044260

FIG. 53



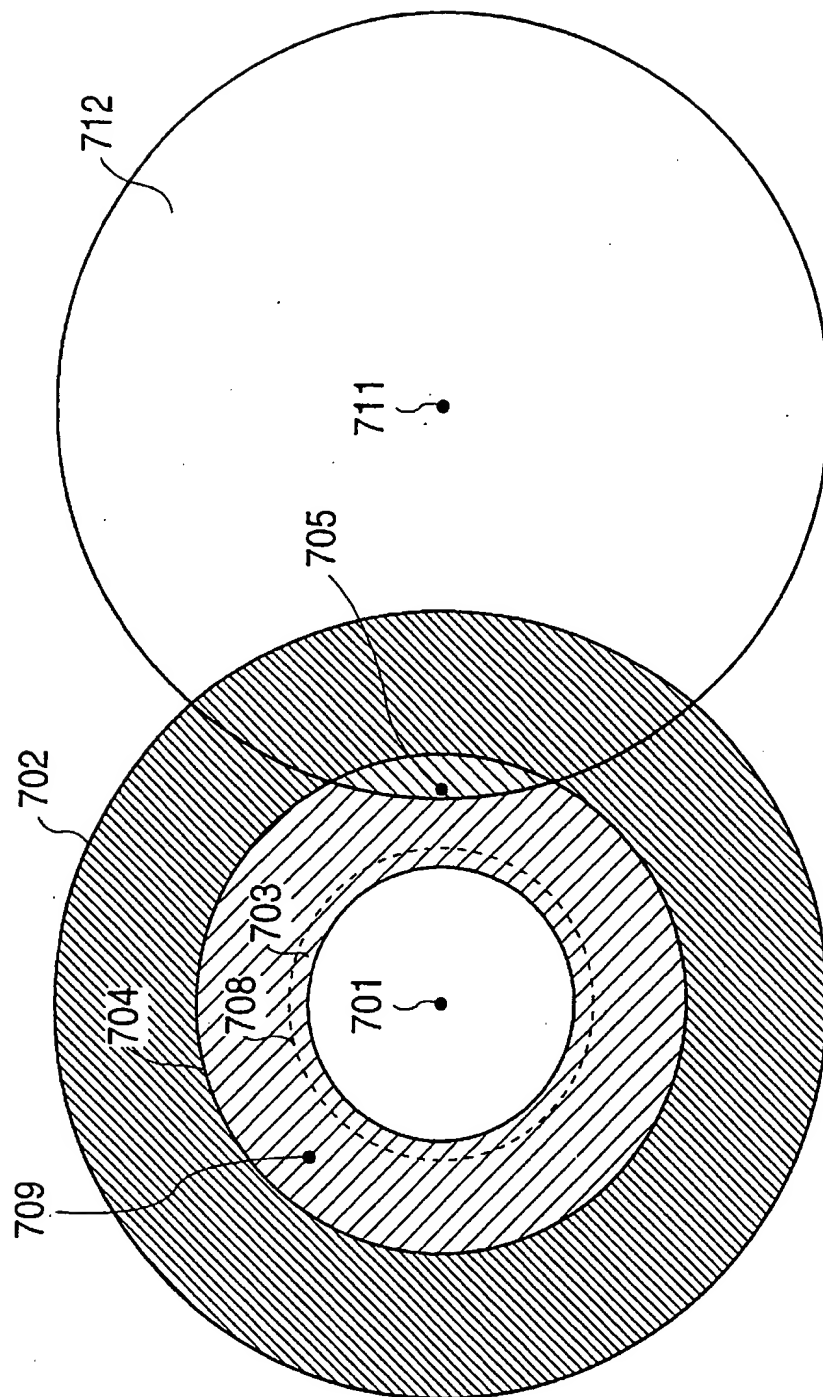
654020" 2E044260

FIG. 54



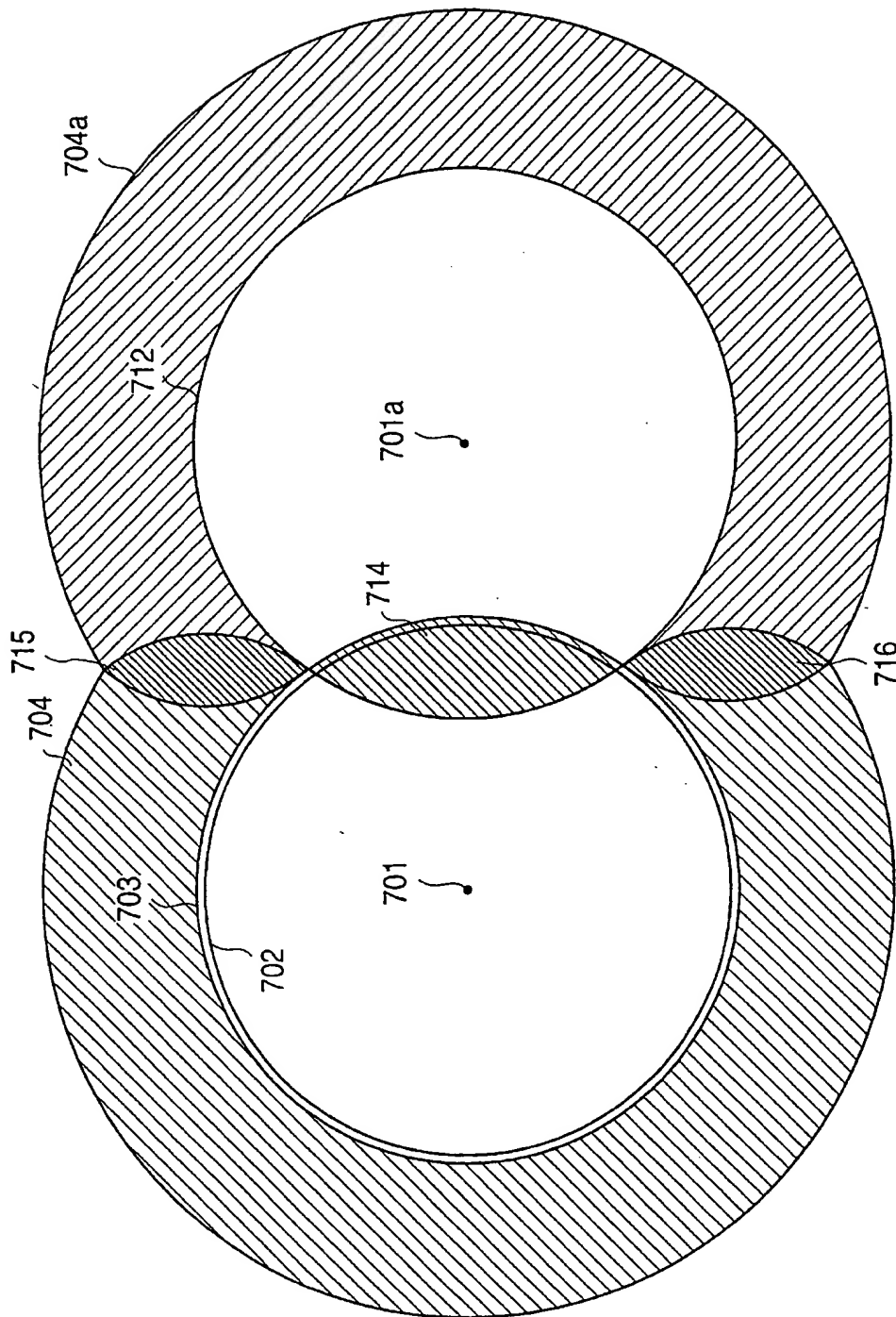
664020" 2E044260

FIG. 55



654020" 2E044260

FIG. 56



664020" 2E044260

FIG. 57

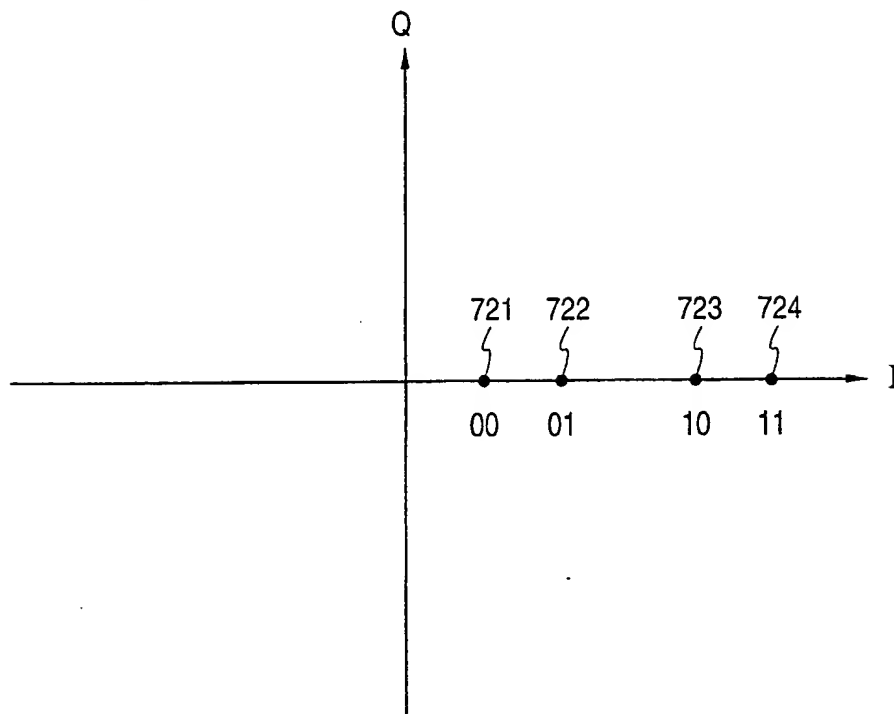
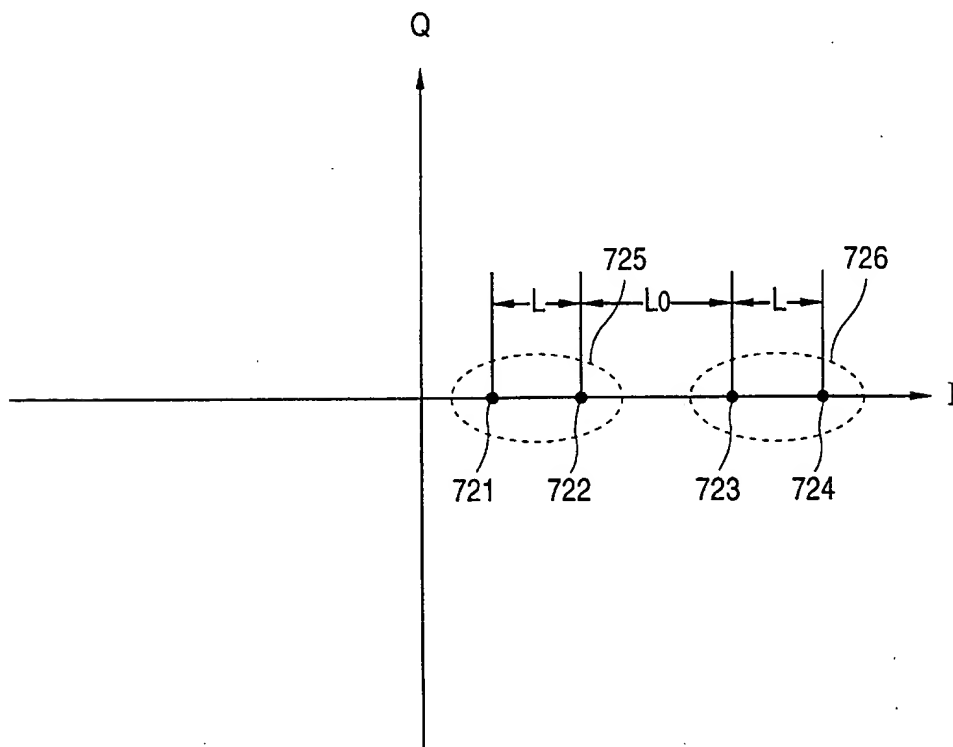


FIG. 58



664020" 2E044260

FIG. 59(a)

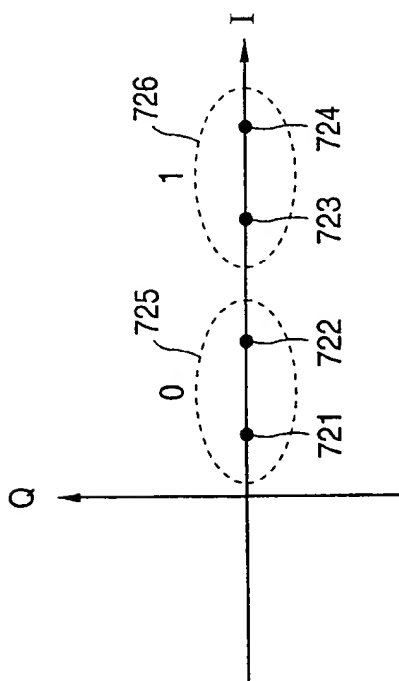


FIG. 59(c)

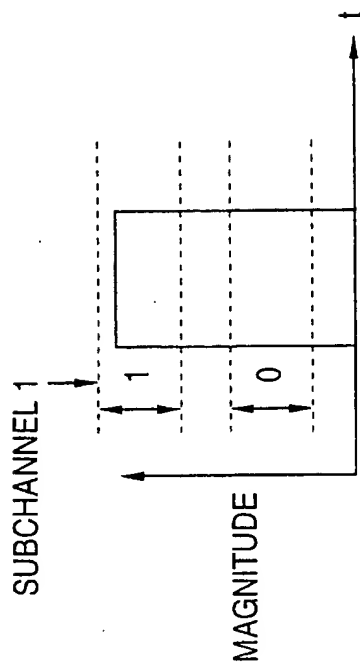


FIG. 59(b)

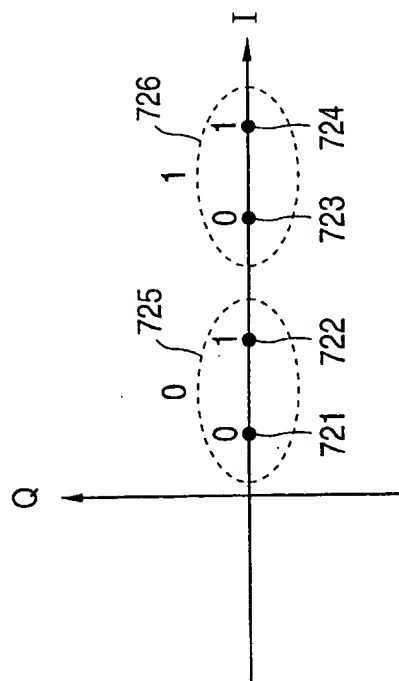


FIG. 59(d)

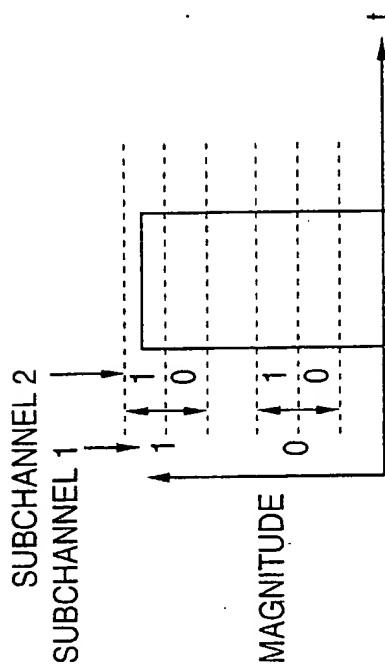
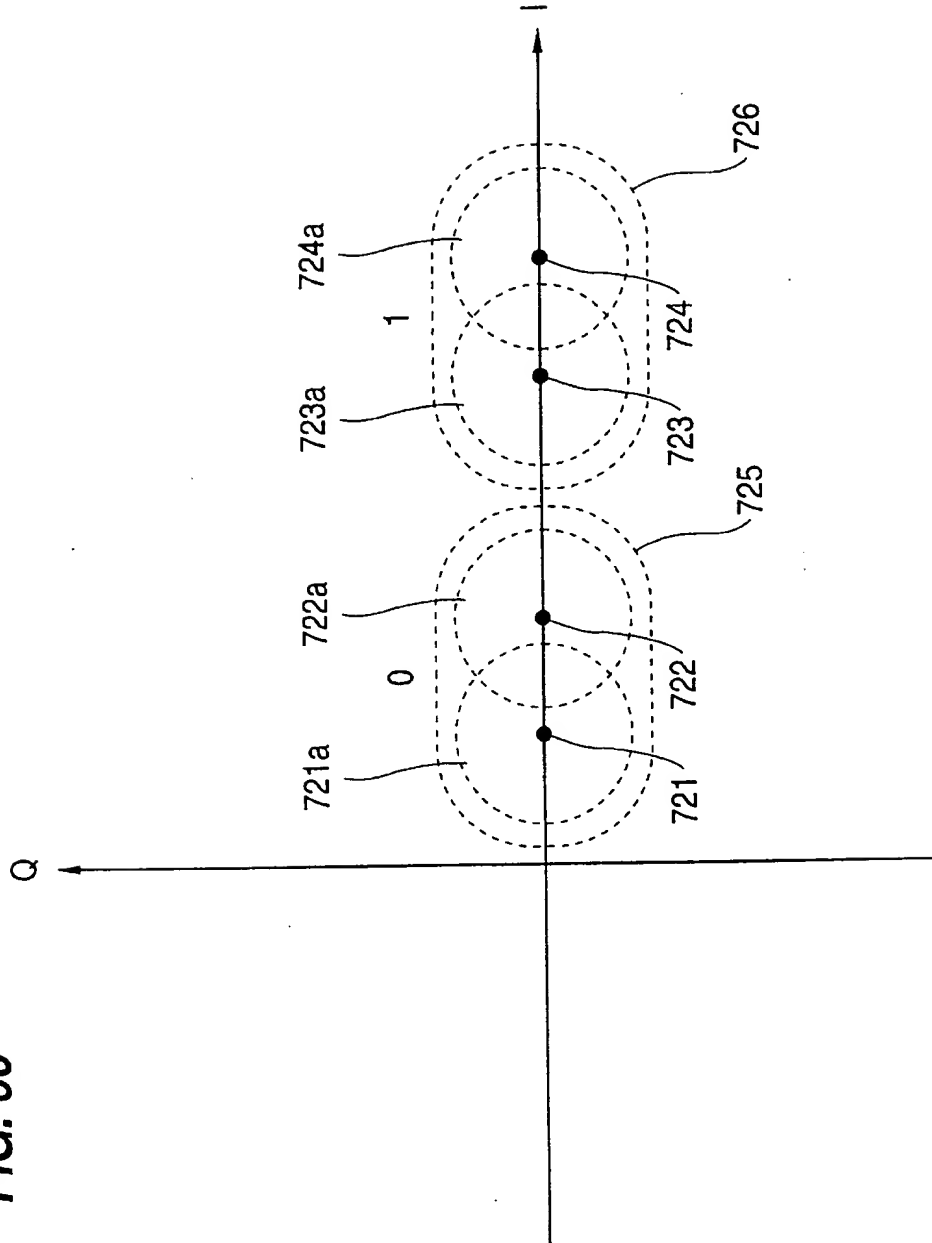


FIG. 60



654020-2E04250

FIG. 61

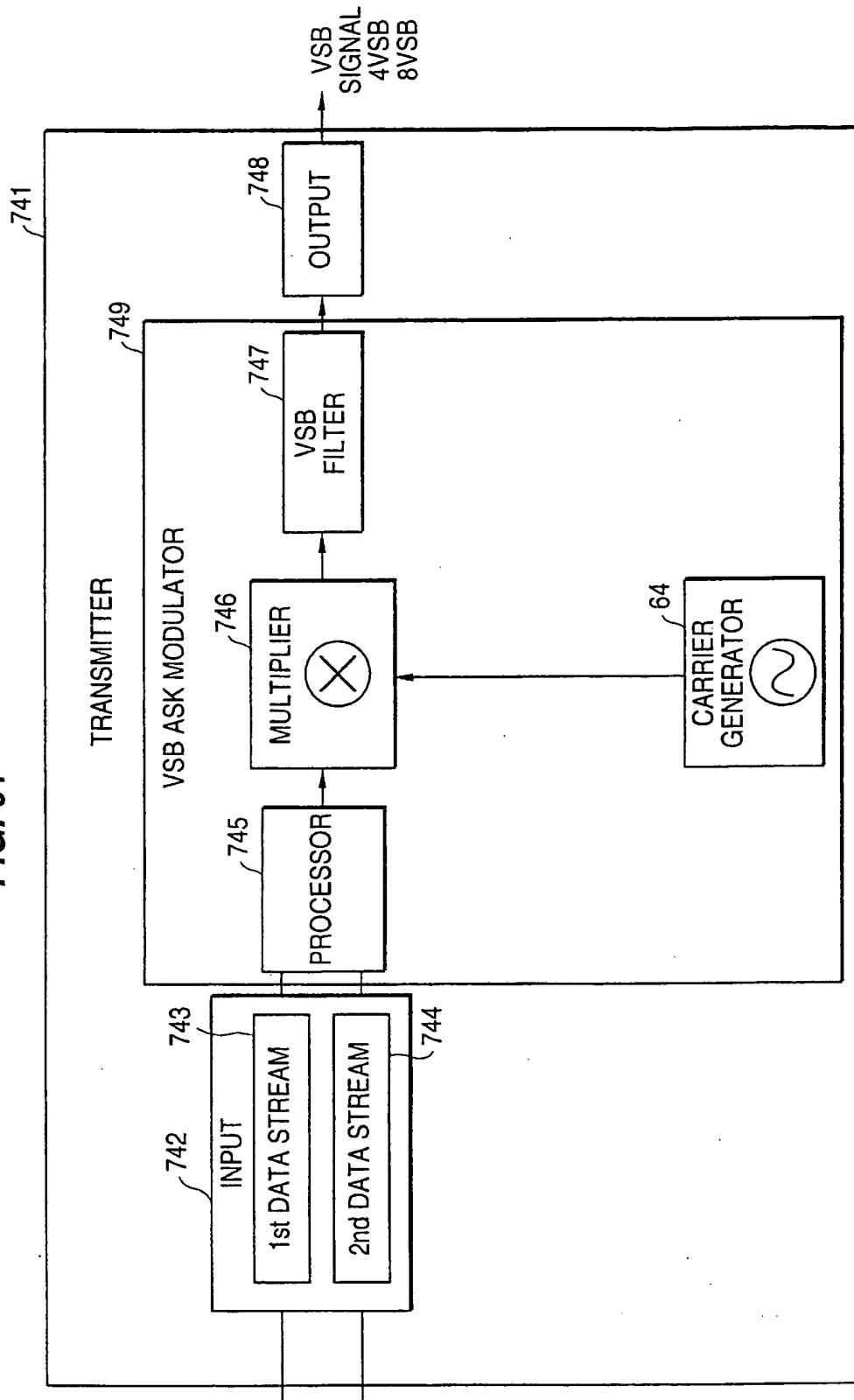


FIG. 62(a)

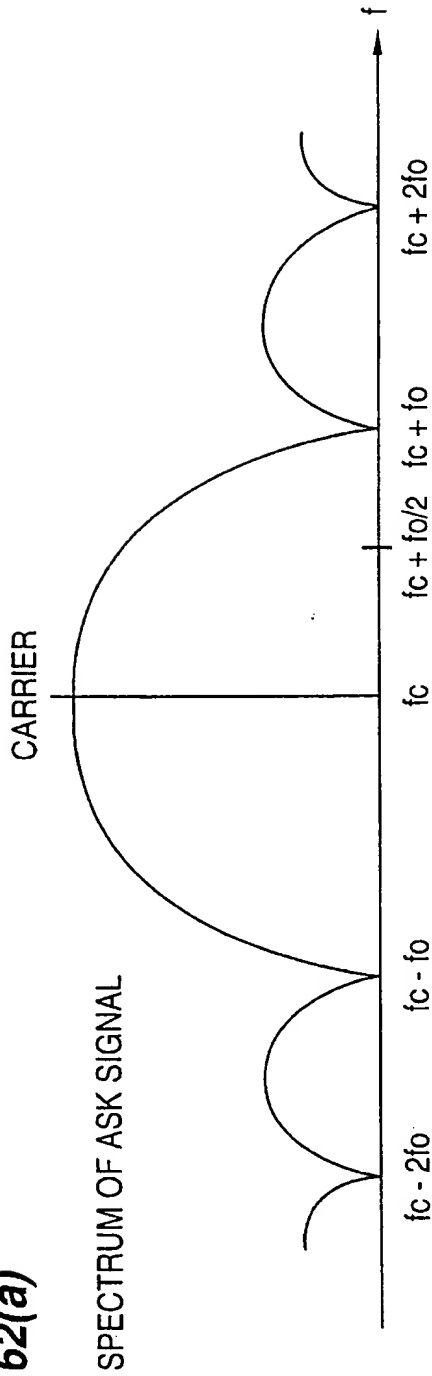


FIG. 62(b)

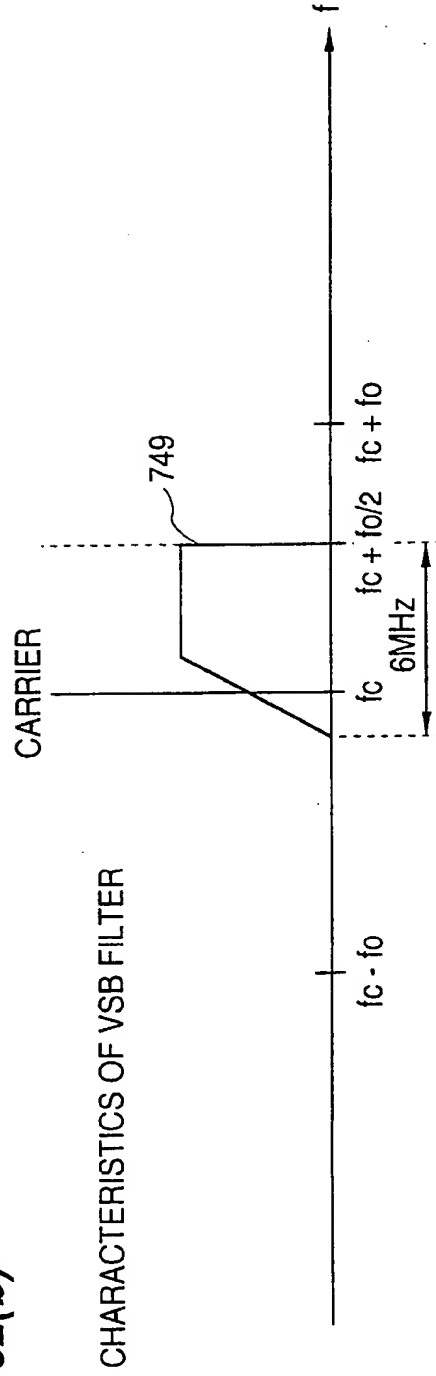


FIG. 63

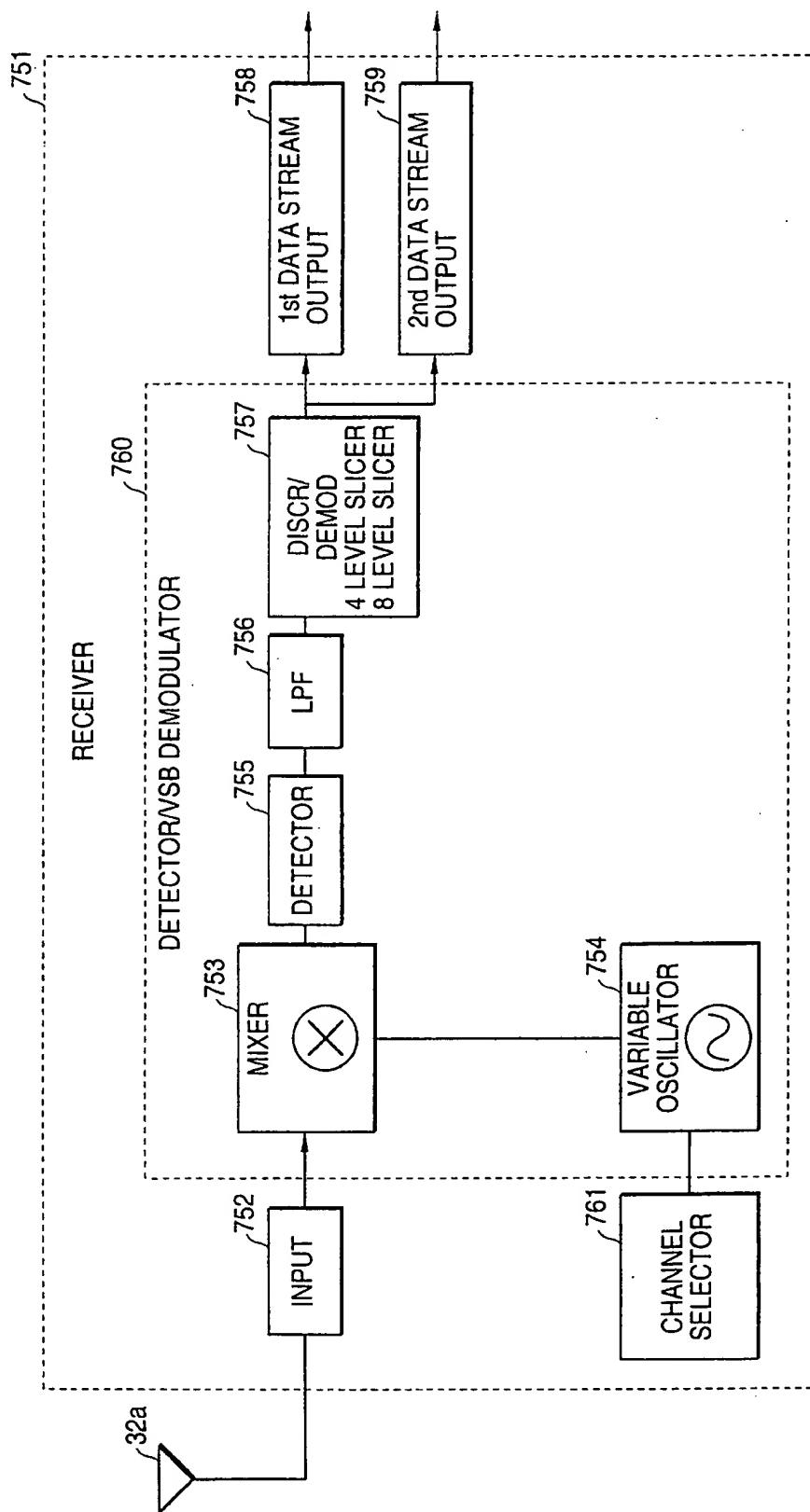


FIG. 64

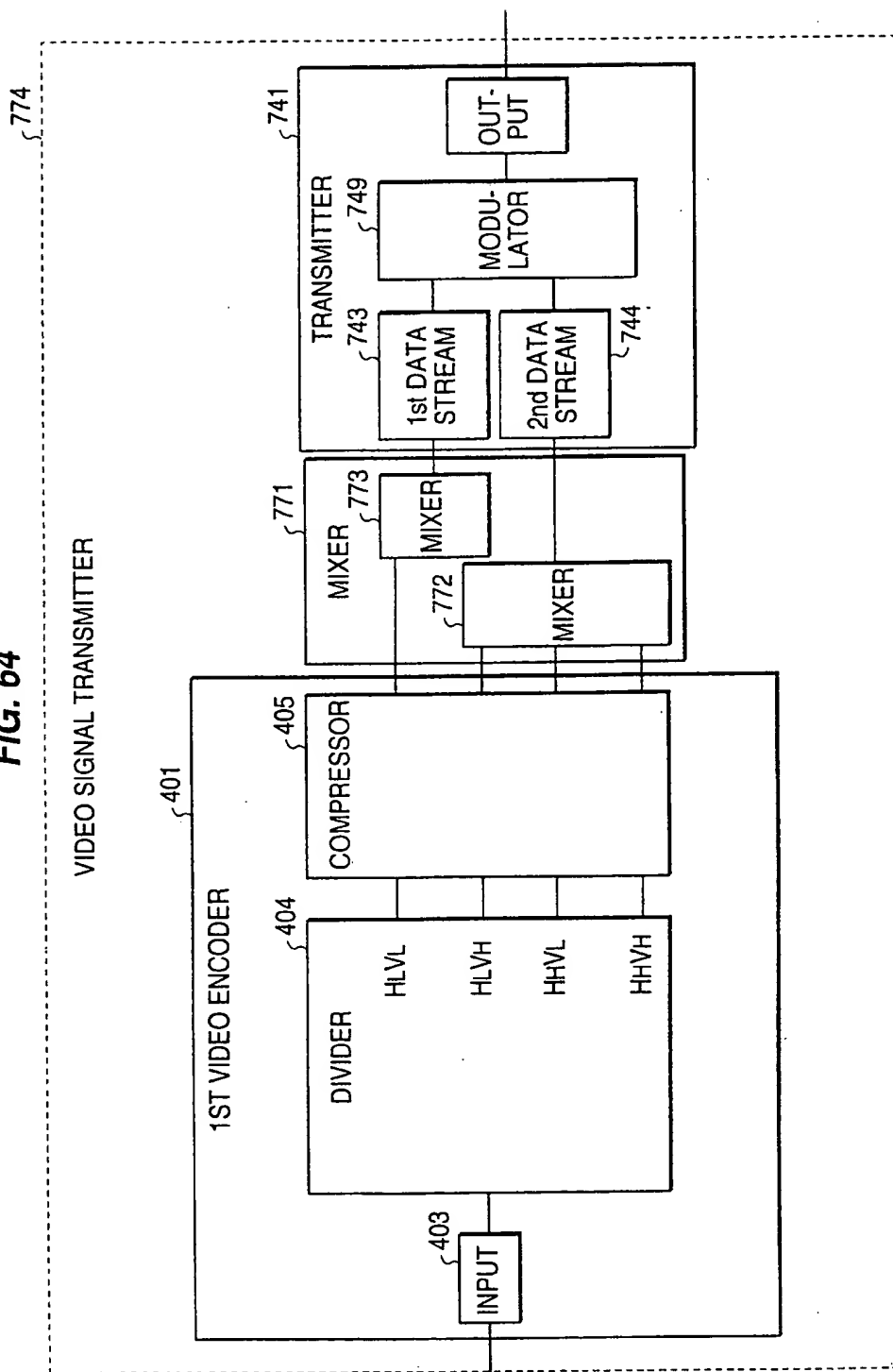


FIG. 65

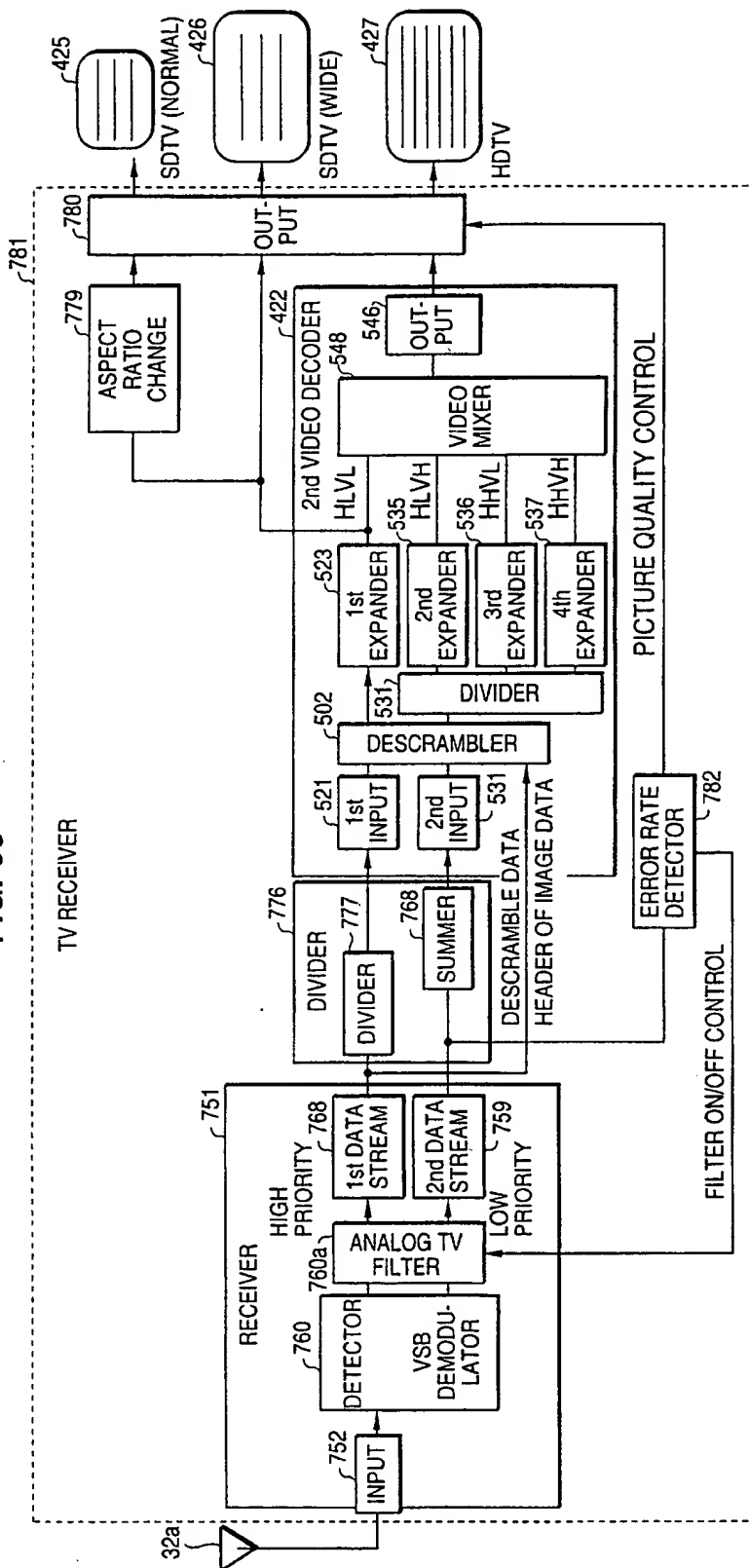
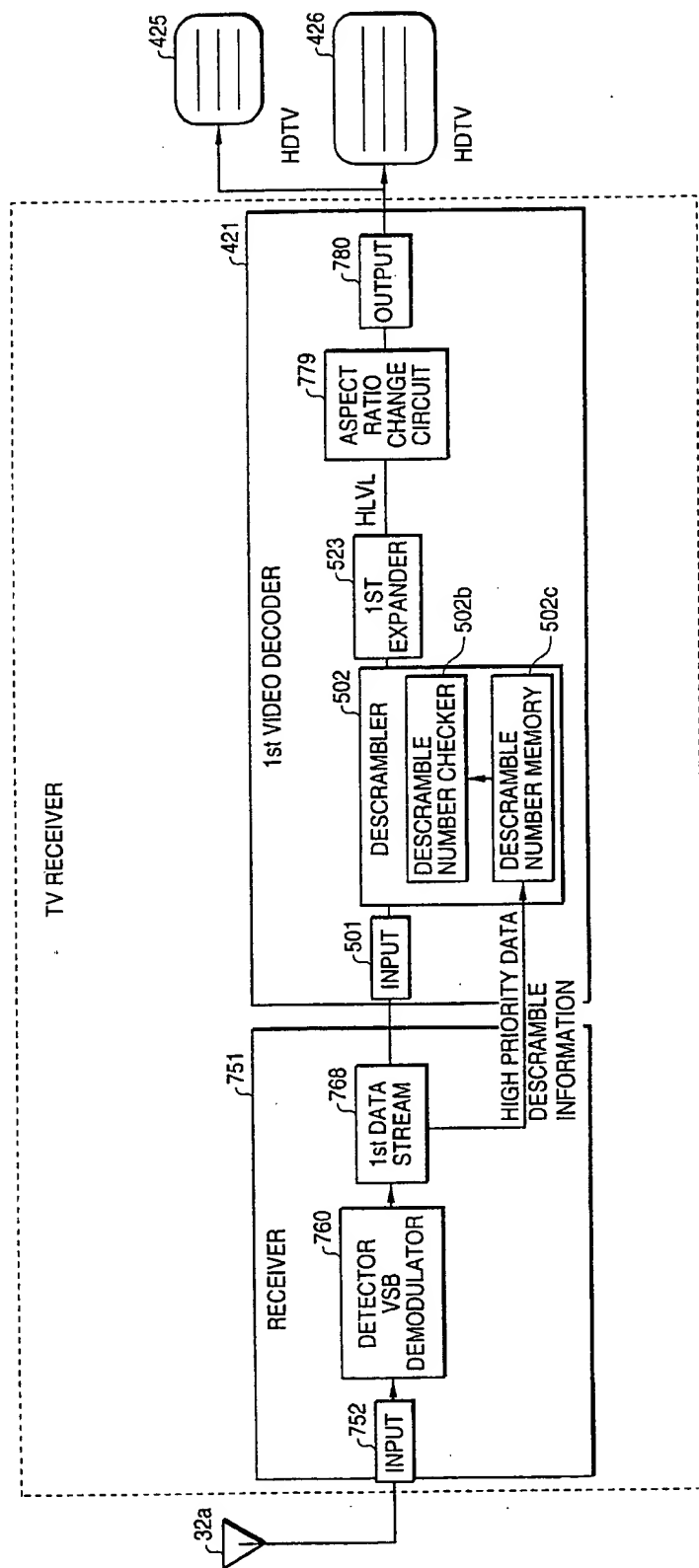
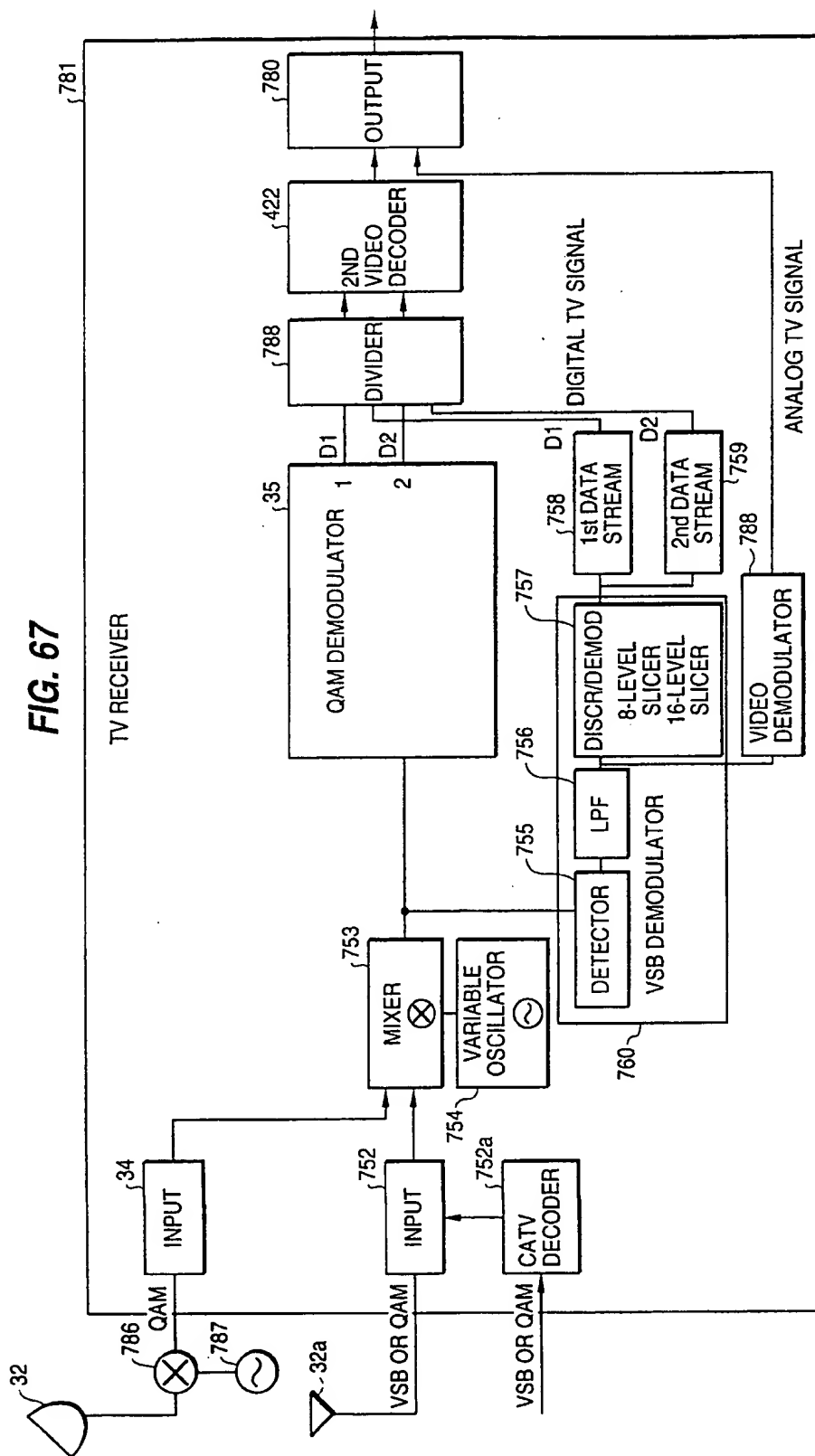


FIG. 66





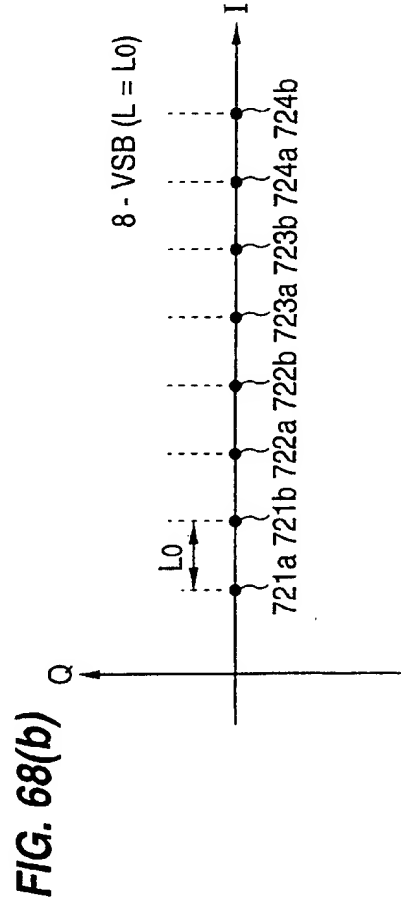
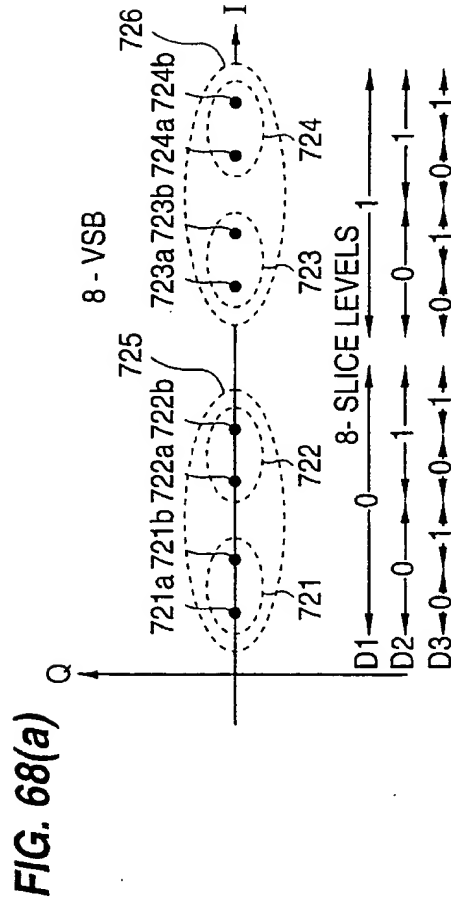


FIG. 68(c)

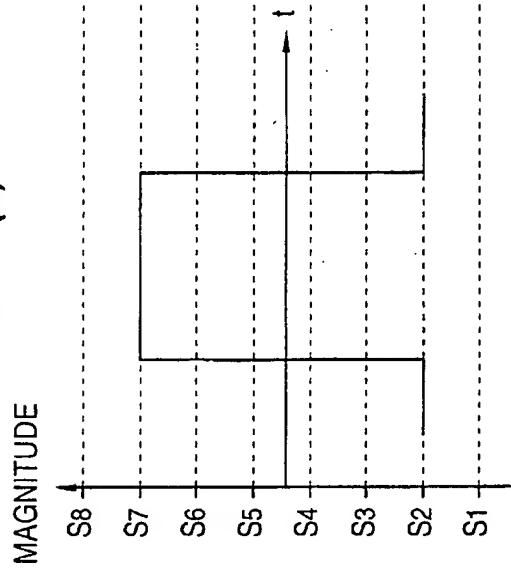


FIG. 69

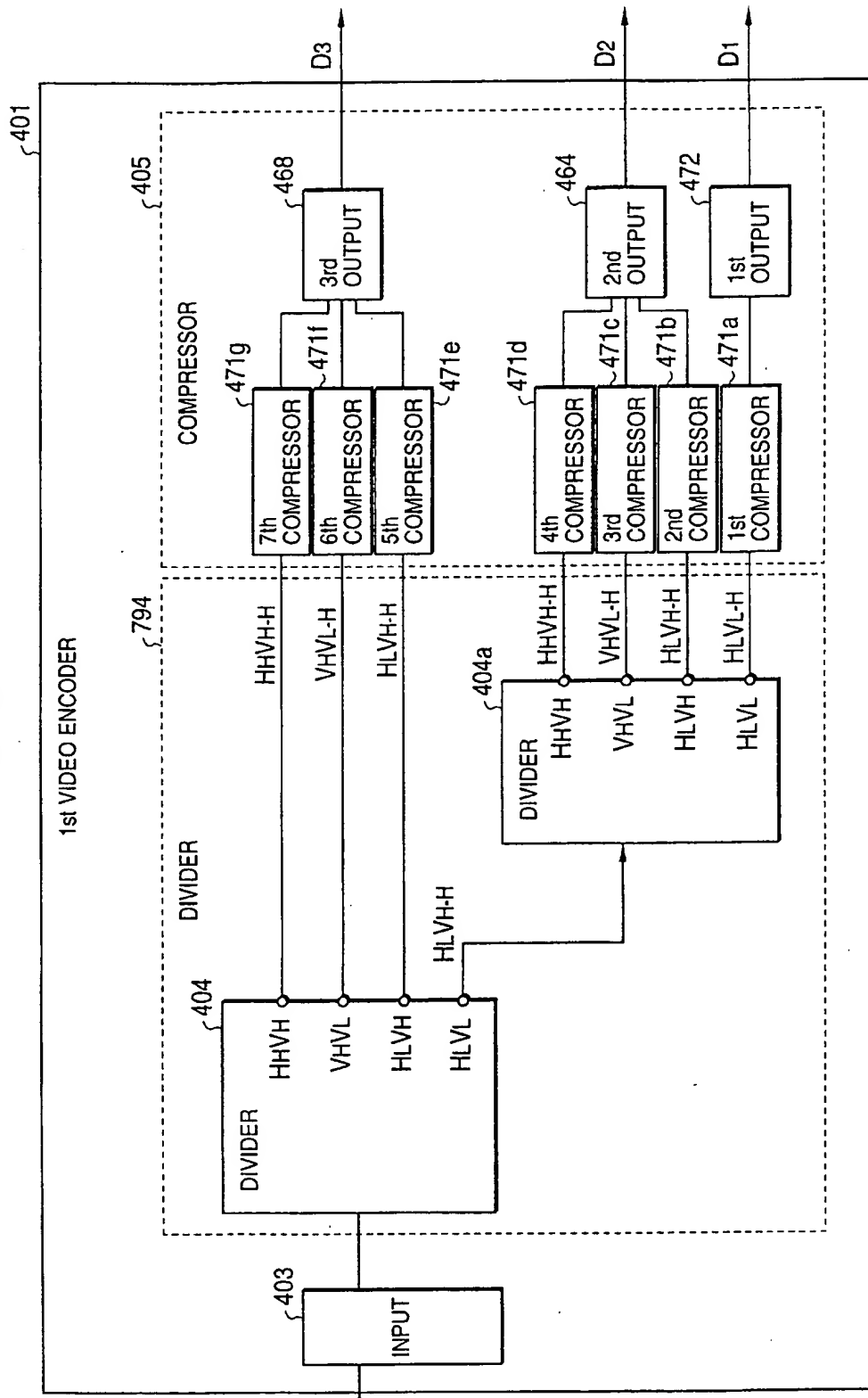


FIG. 70

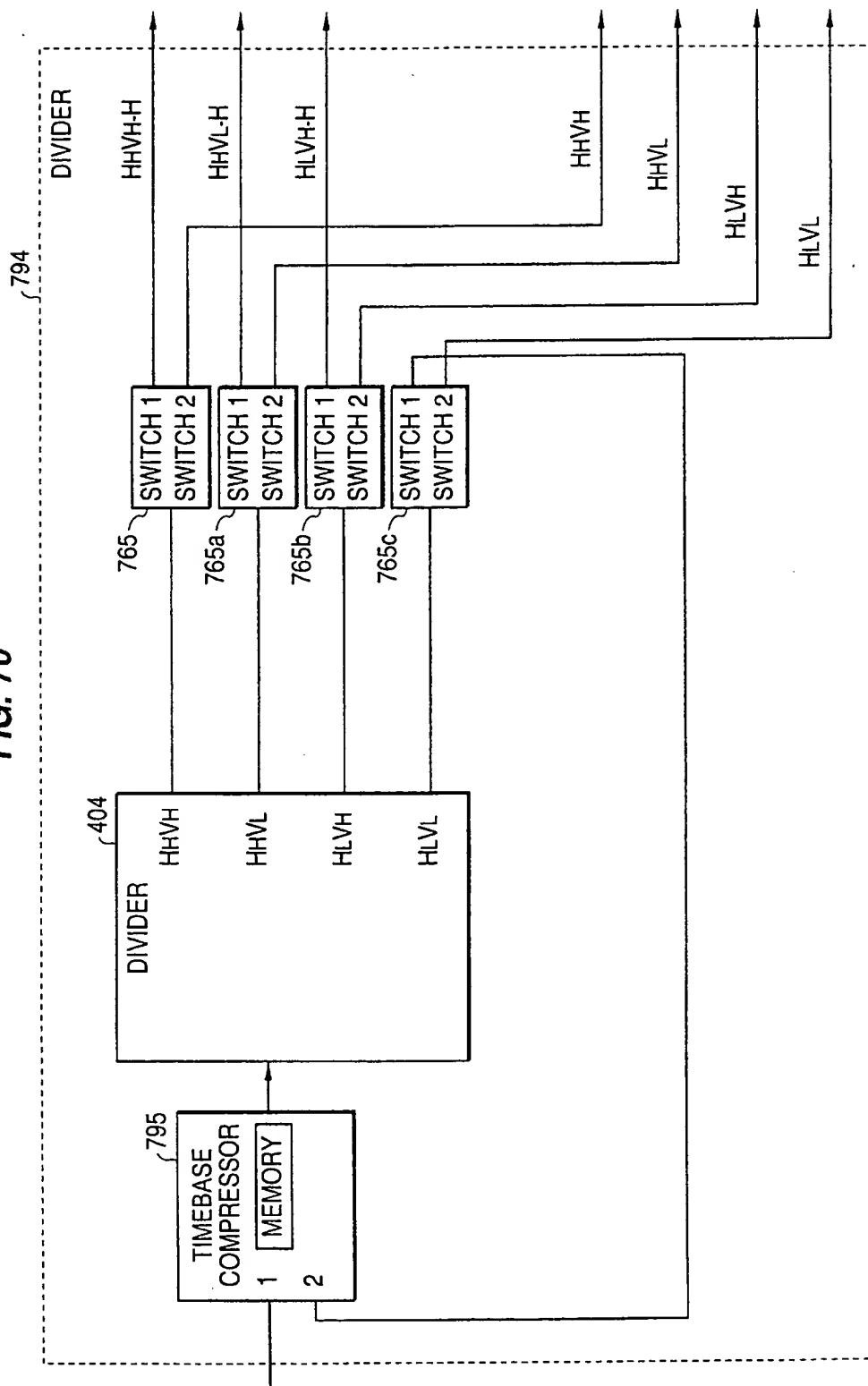


FIG. 71

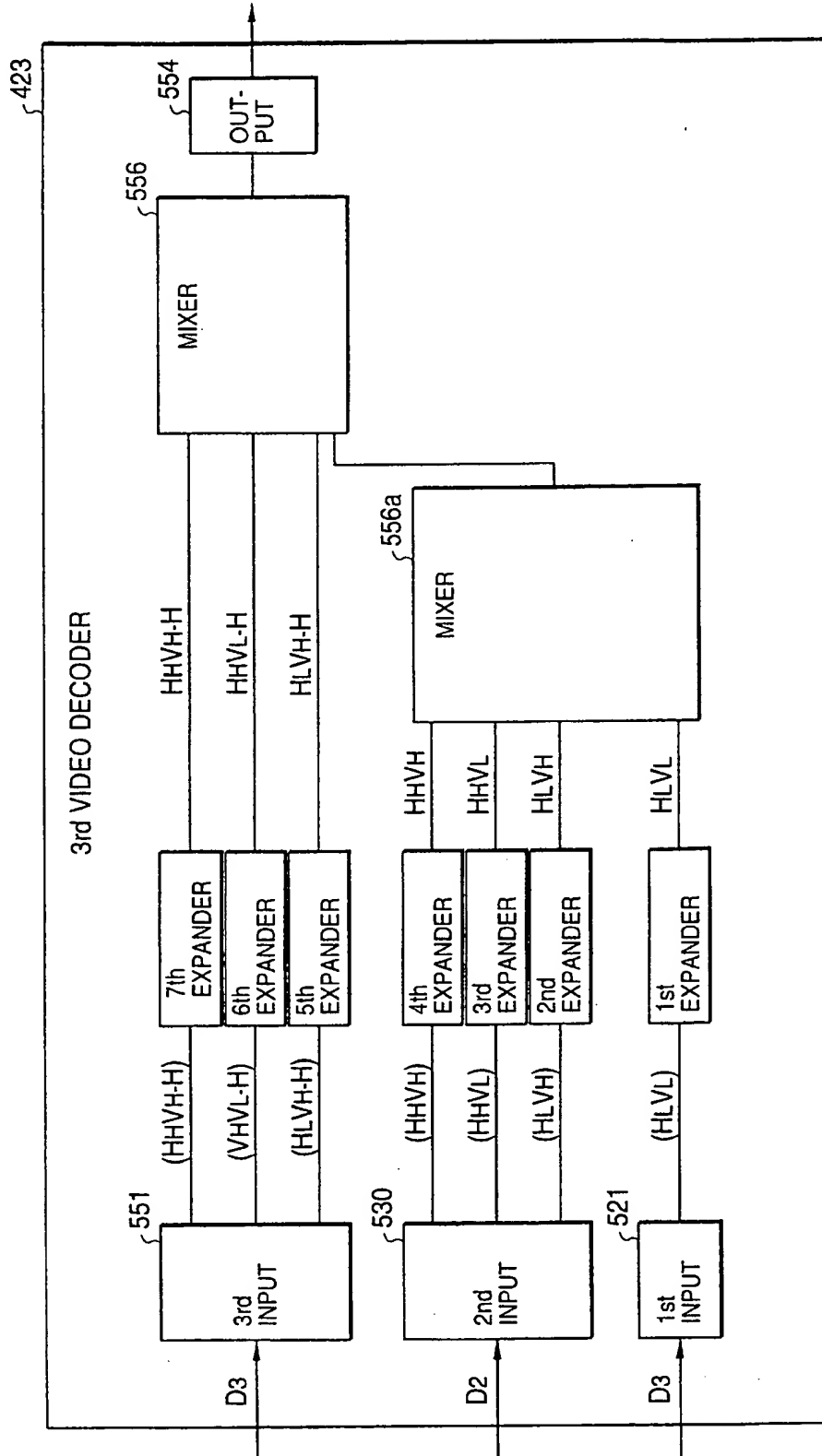


FIG. 72

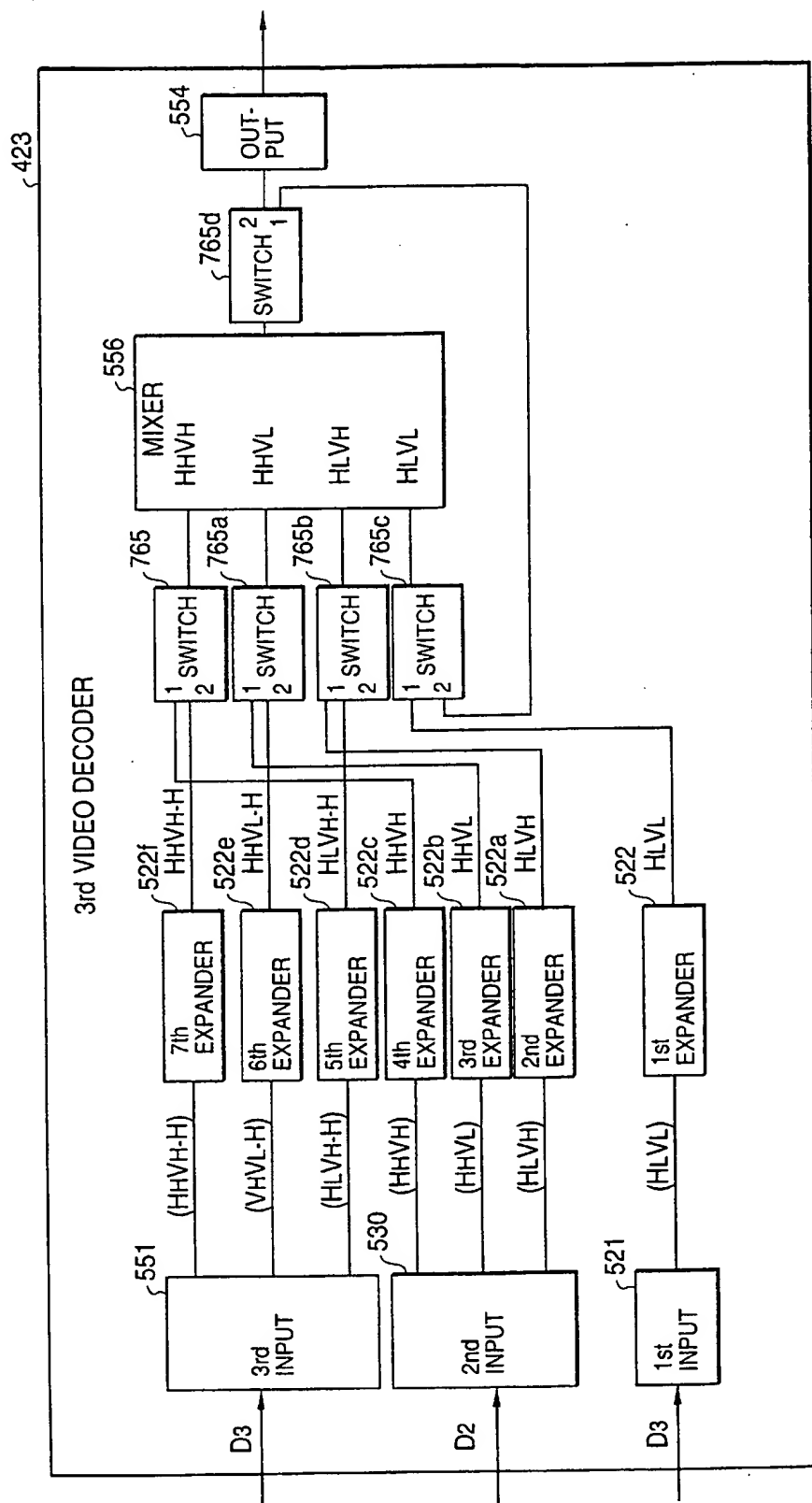


FIG. 73

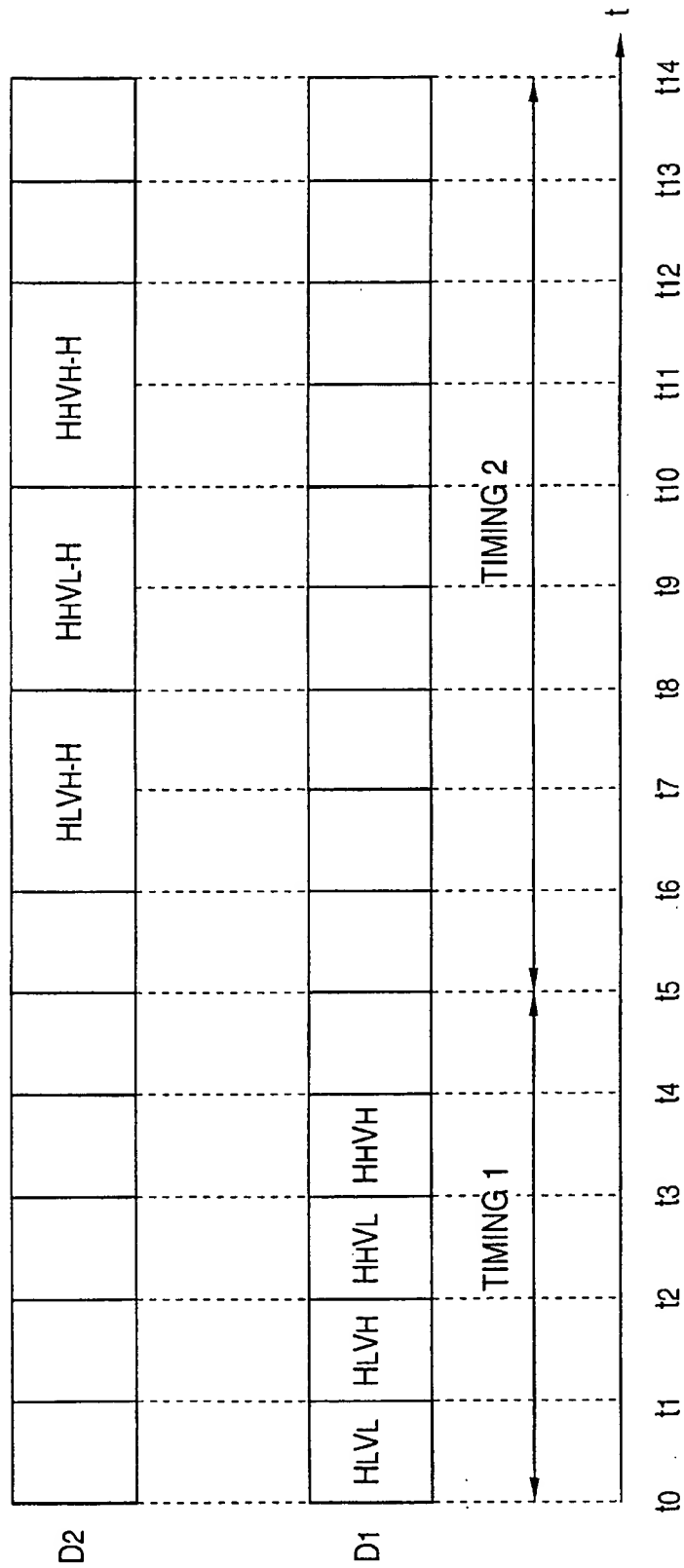


FIG. 74(a)

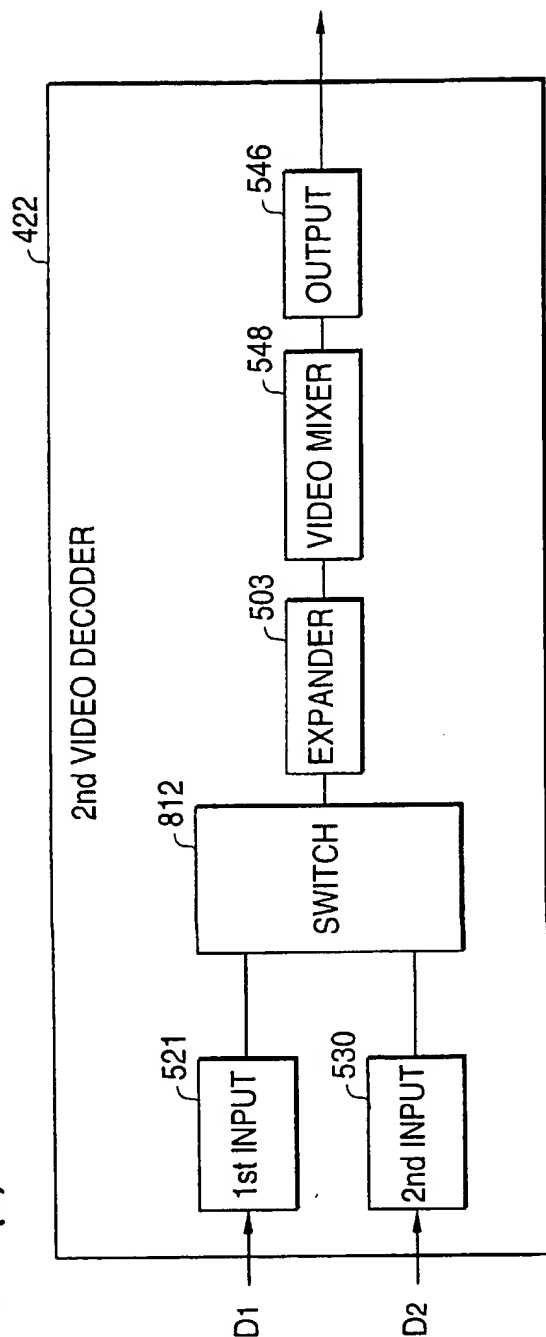


FIG. 74(b)

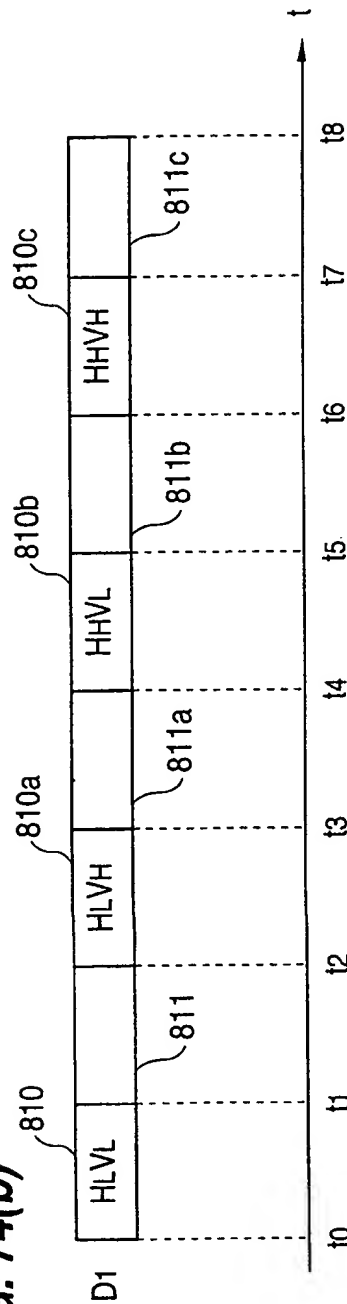


FIG. 75

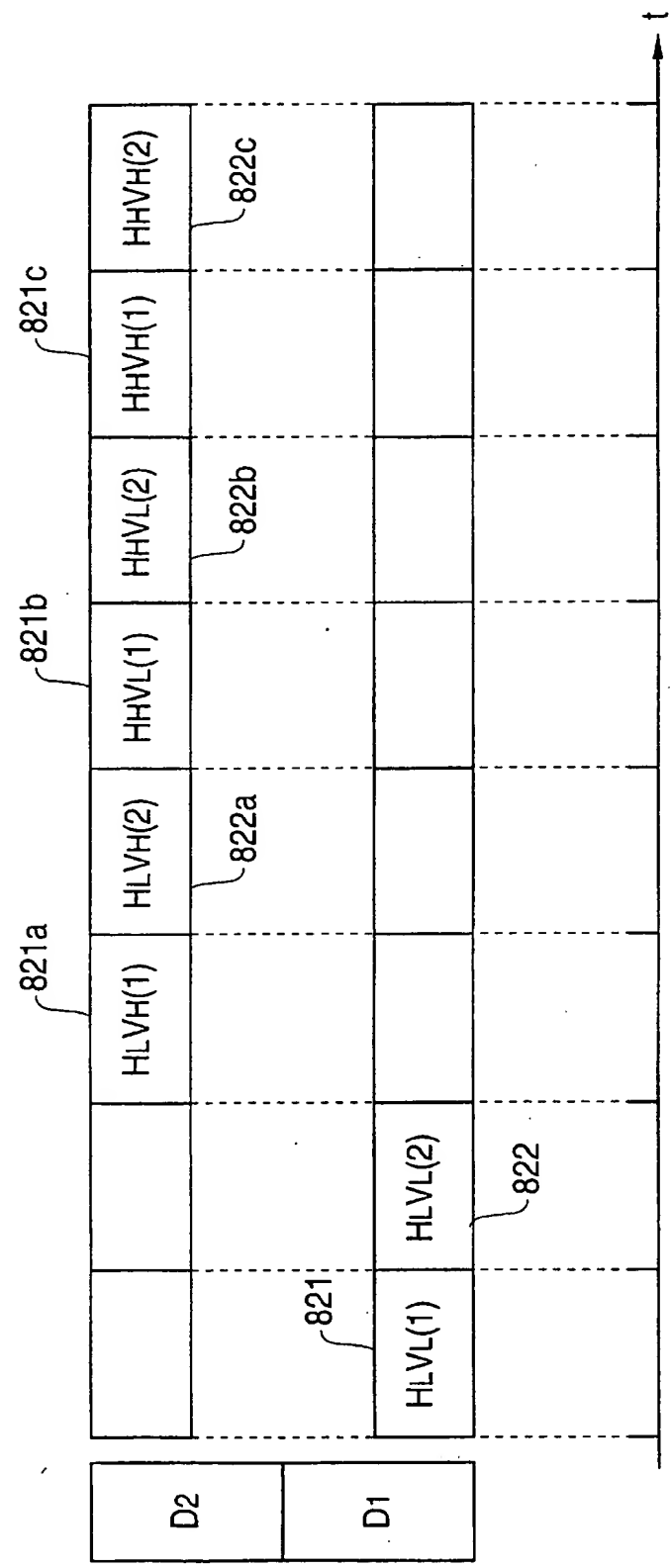
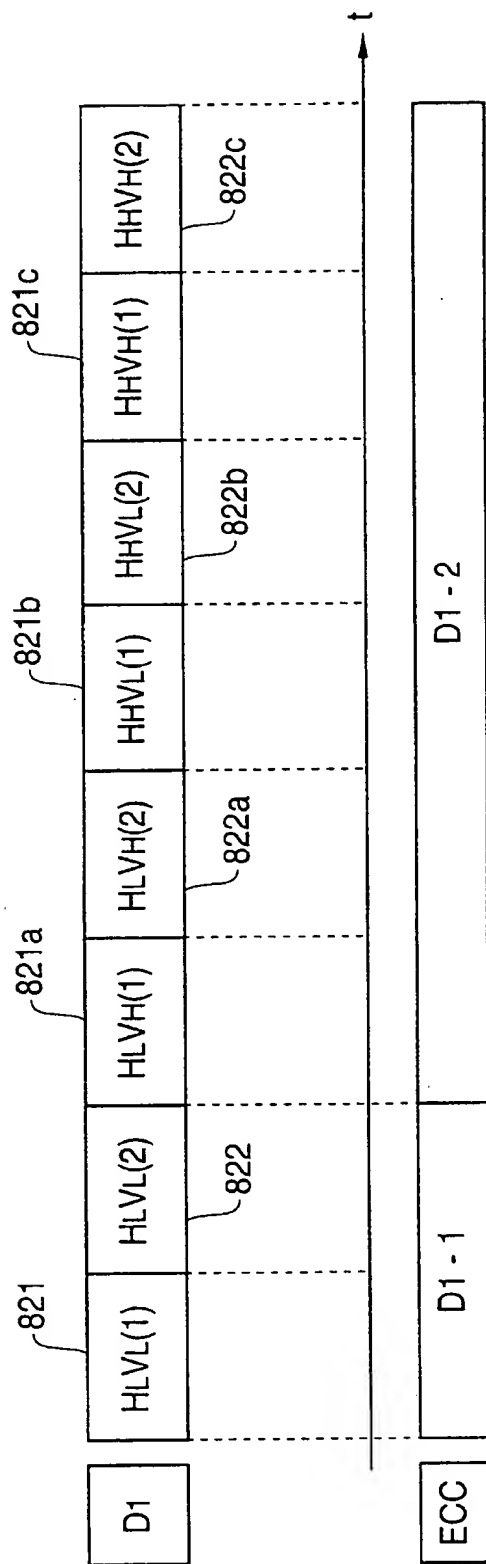


FIG. 76



The diagram illustrates the timing of a 16-bit bus system. The horizontal axis represents time t , with markers at $t_0, t_2, t_4, t_6, t_8, t_{10}, t_{12}, t_{14}, t_{16}, t_{18}, t_{20}, t_{22}, t_{24}, t_{26}, t_{28}, t_{30}, t_{32}, t_{34}$. The vertical axis represents data values. The data is organized into two main sections: D_2 (top) and D_1 (bottom). The data is divided into time intervals labeled 821, 822, 823, 824a, and 824b. The data values are represented by horizontal bars of varying lengths, indicating the duration of each data state. The data values are labeled as follows: $HLVL$ (1), $HLVL$ (2), $HLVL-H$ (1), $HLVL-H$ (2), $HLVL-H-H$ (1), $HLVL-H-H$ (2), $HLVL-H-H-H$ (1), $HLVL-H-H-H$ (2). The data is organized into two main sections: D_2 (top) and D_1 (bottom). The data is divided into time intervals labeled 821, 822, 823, 824a, and 824b. The data values are represented by horizontal bars of varying lengths, indicating the duration of each data state. The data values are labeled as follows: $HLVL$ (1), $HLVL$ (2), $HLVL-H$ (1), $HLVL-H$ (2), $HLVL-H-H$ (1), $HLVL-H-H$ (2), $HLVL-H-H-H$ (1), $HLVL-H-H-H$ (2).

FIG. 78

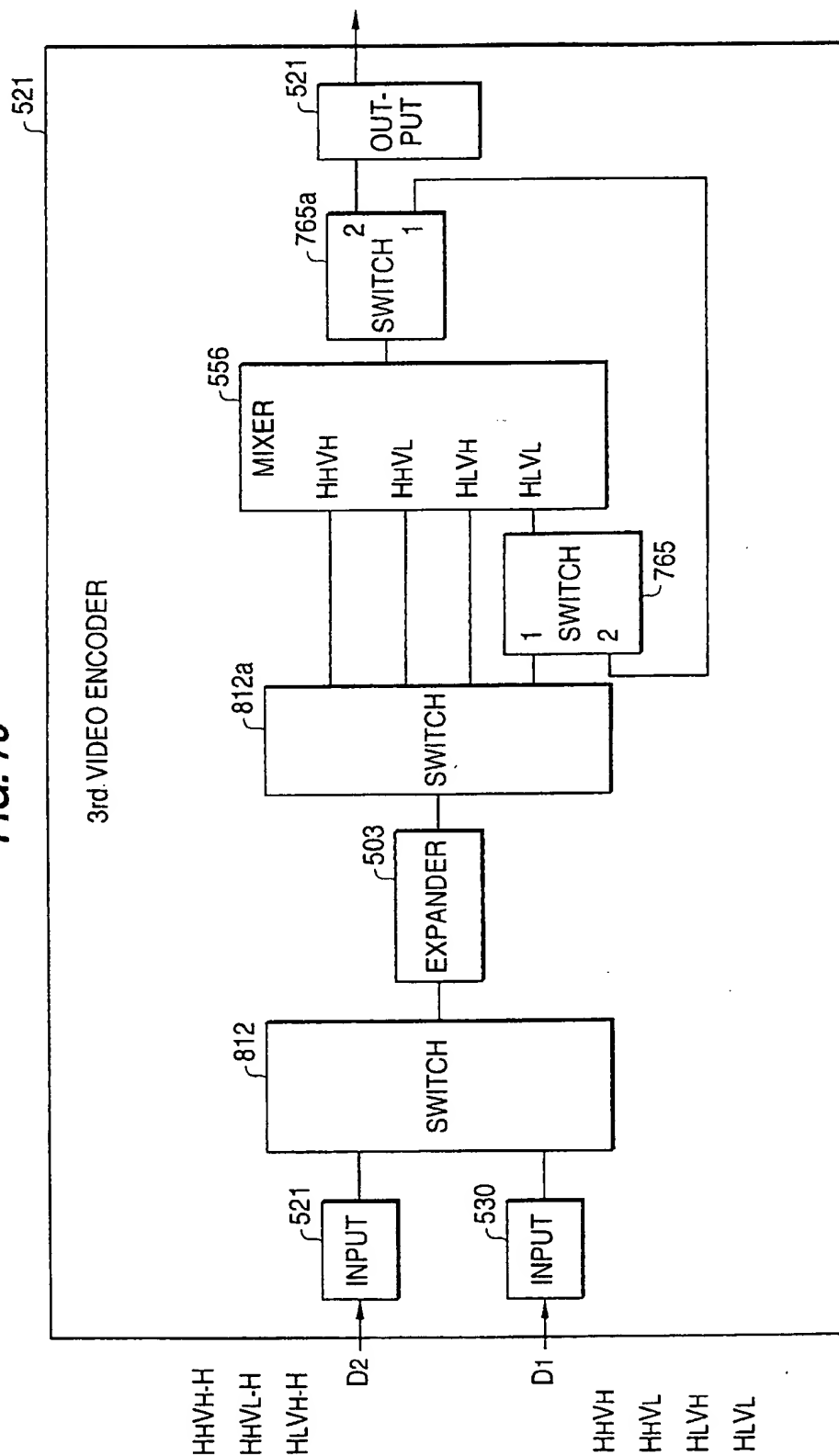


FIG. 79

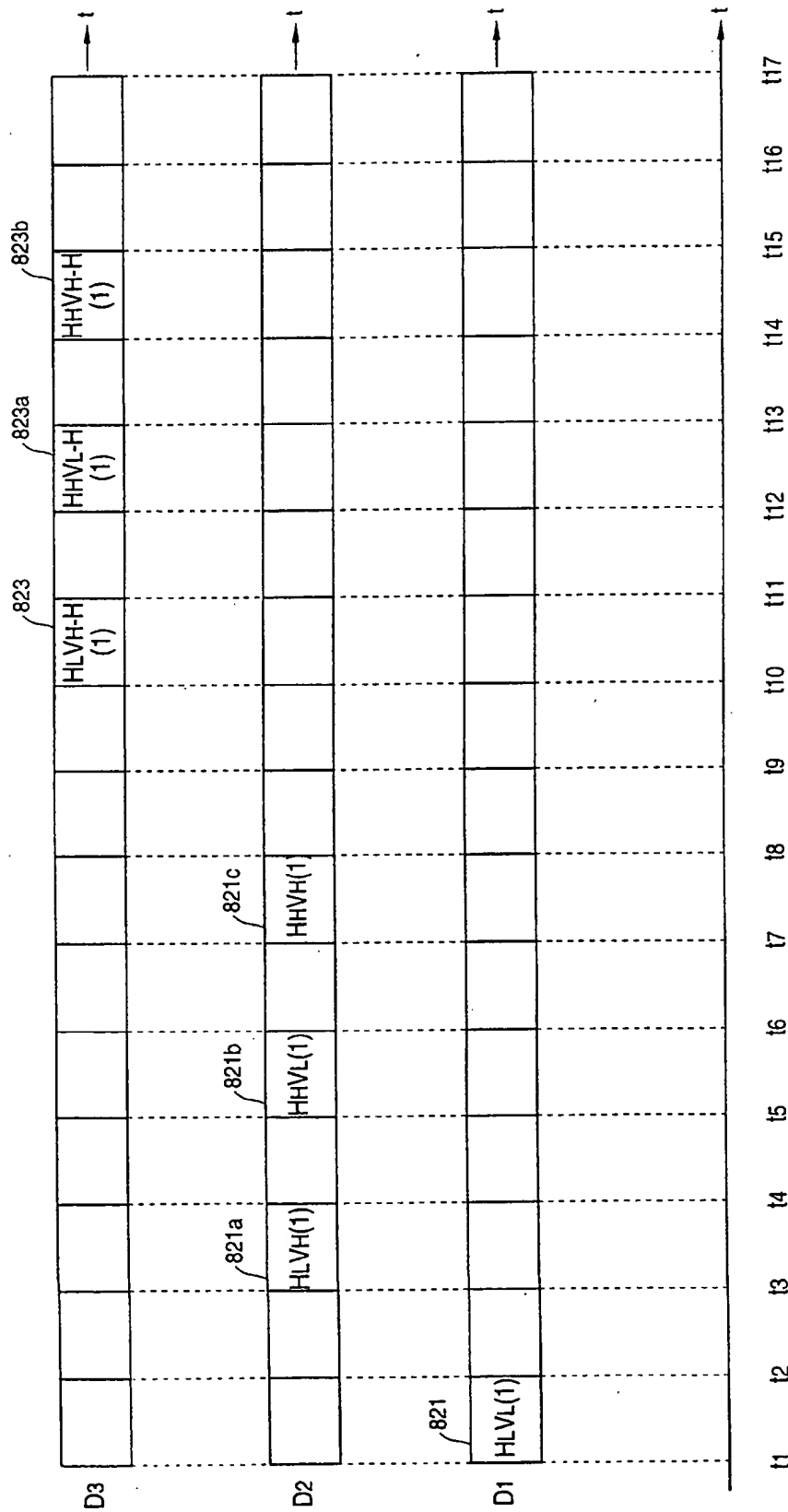


FIG. 80

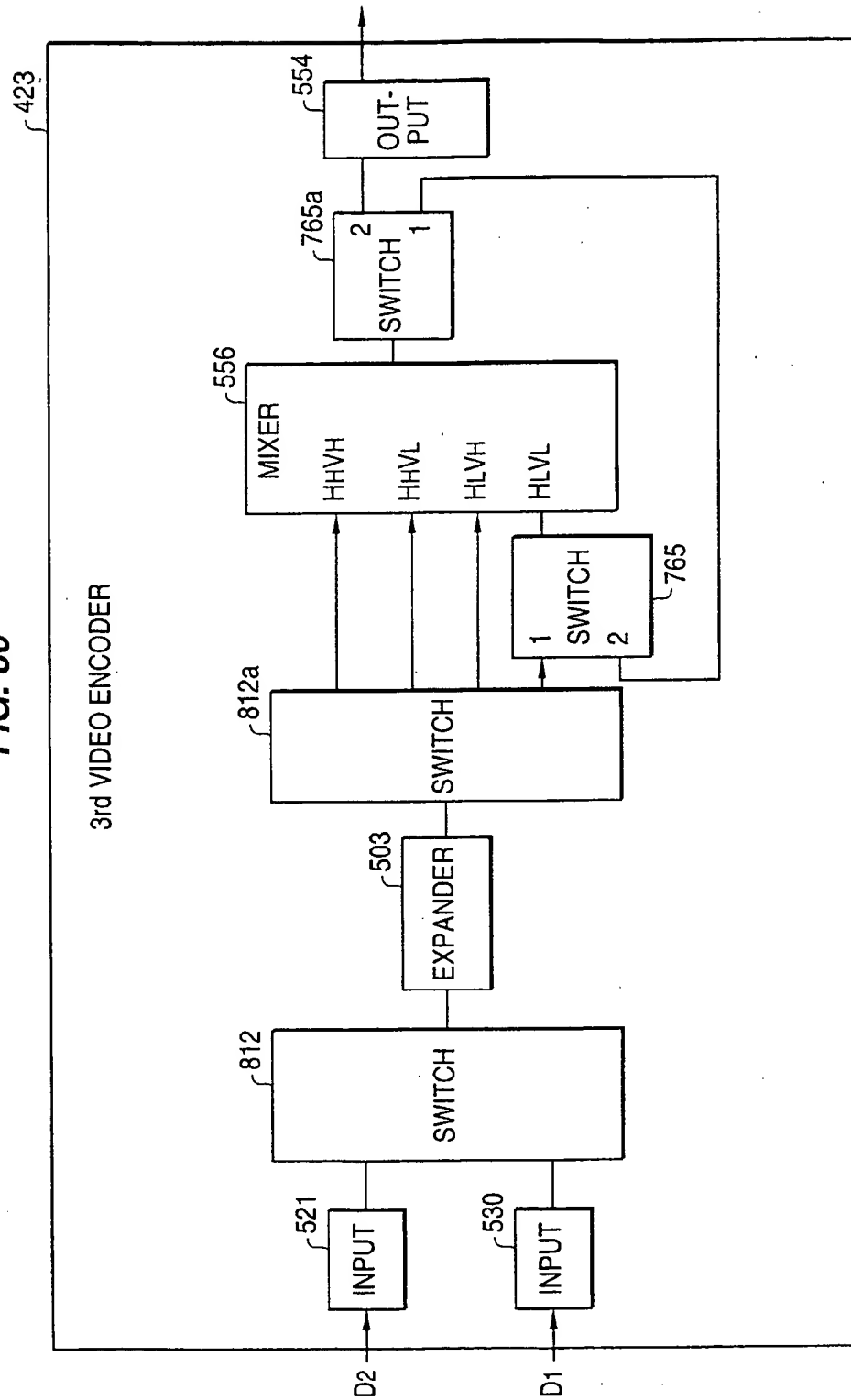


FIG. 81

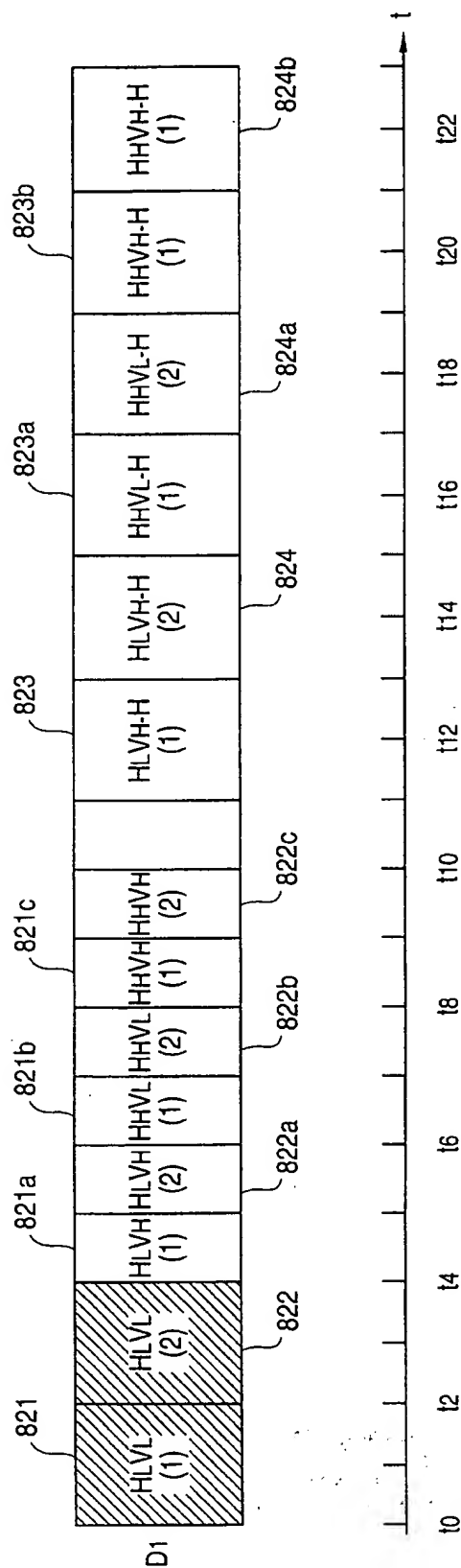


FIG. 82

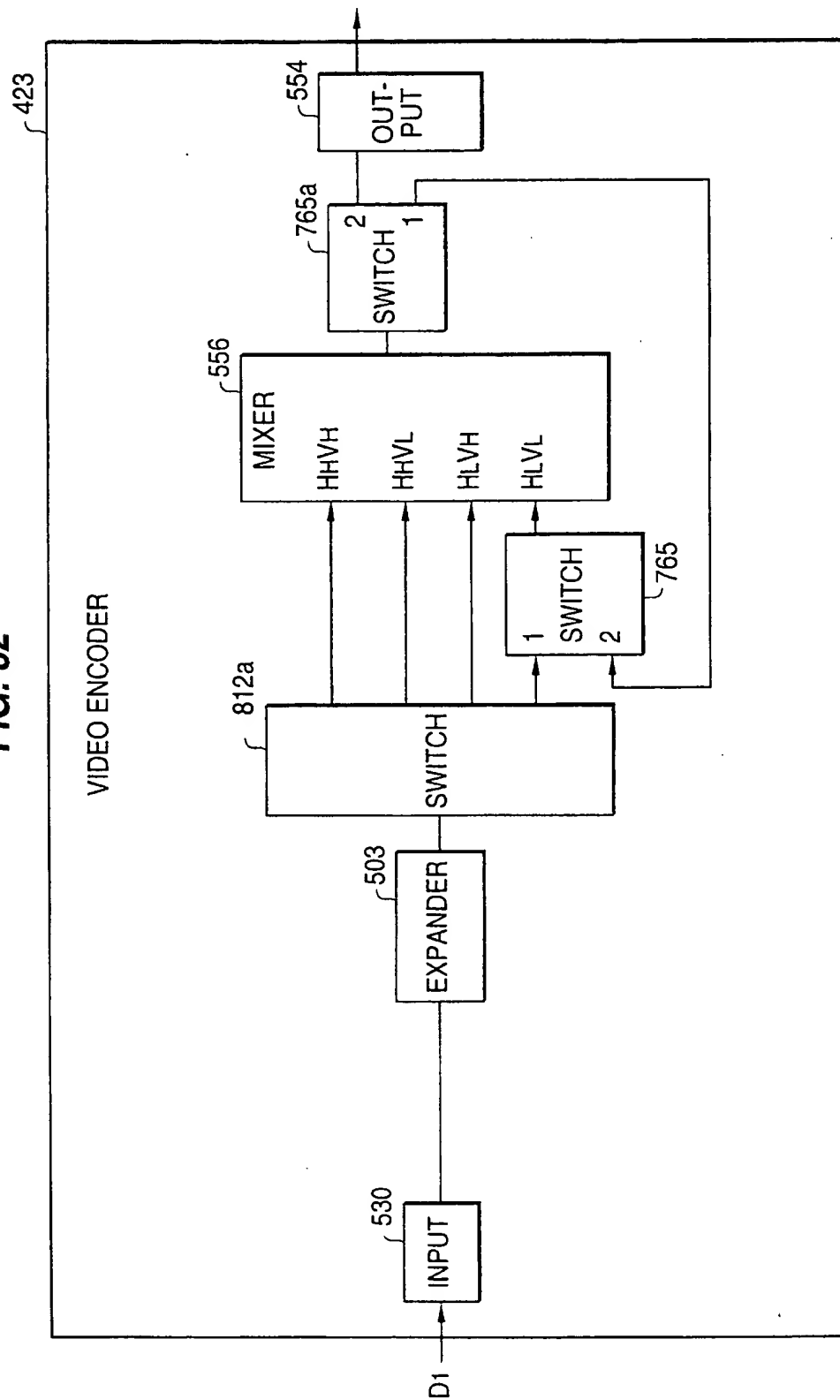


FIG. 83

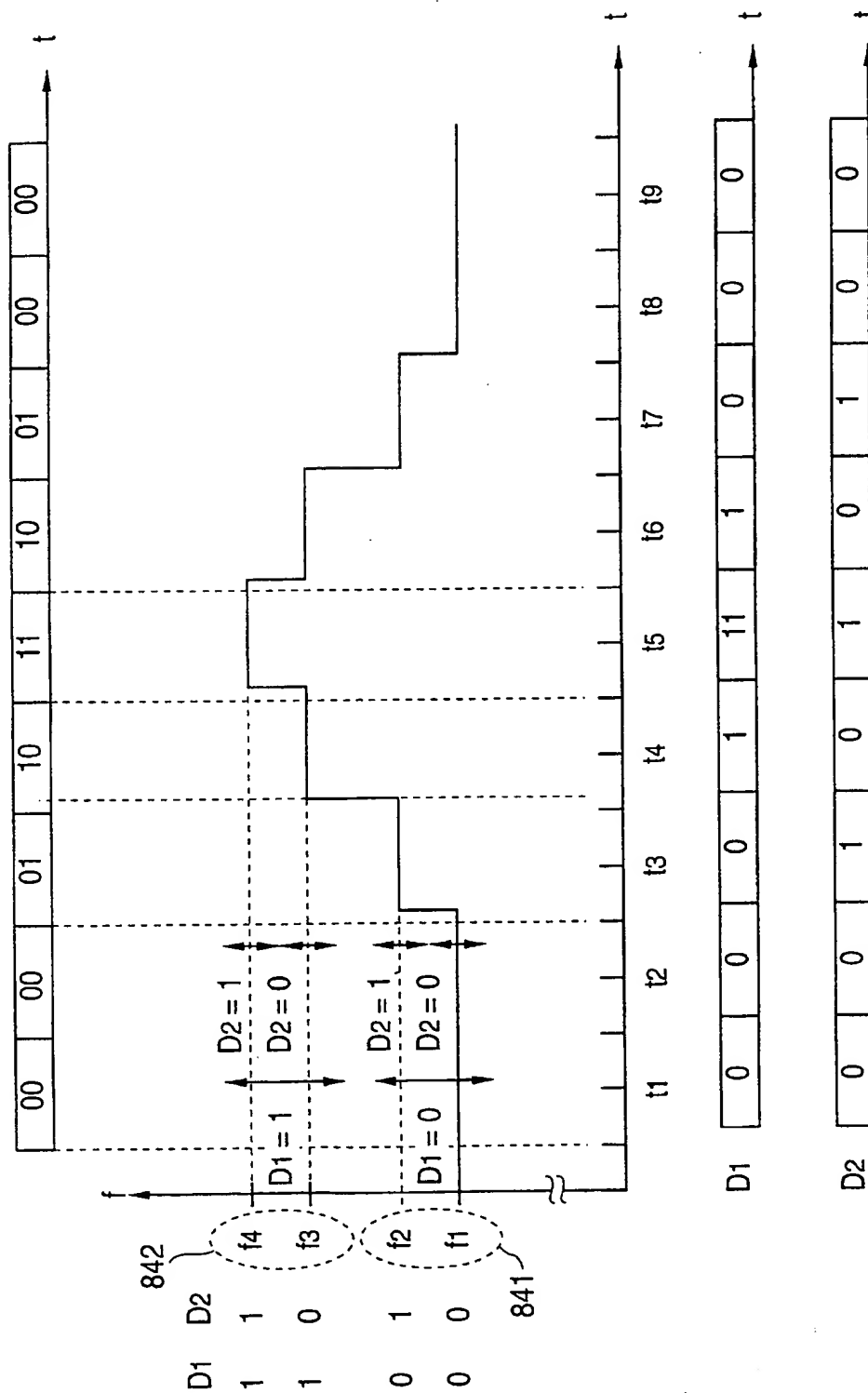


FIG. 84

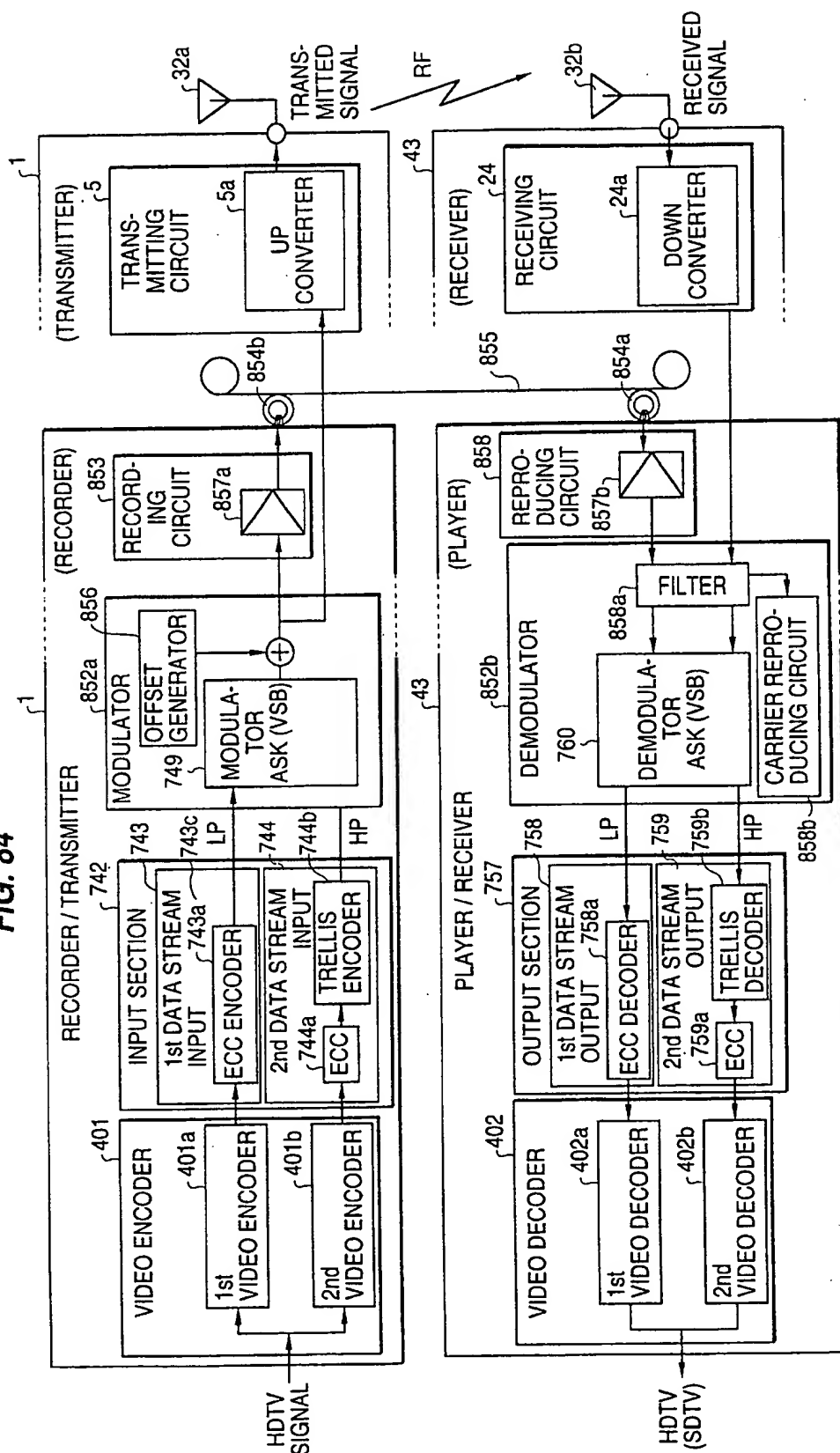


FIG. 85

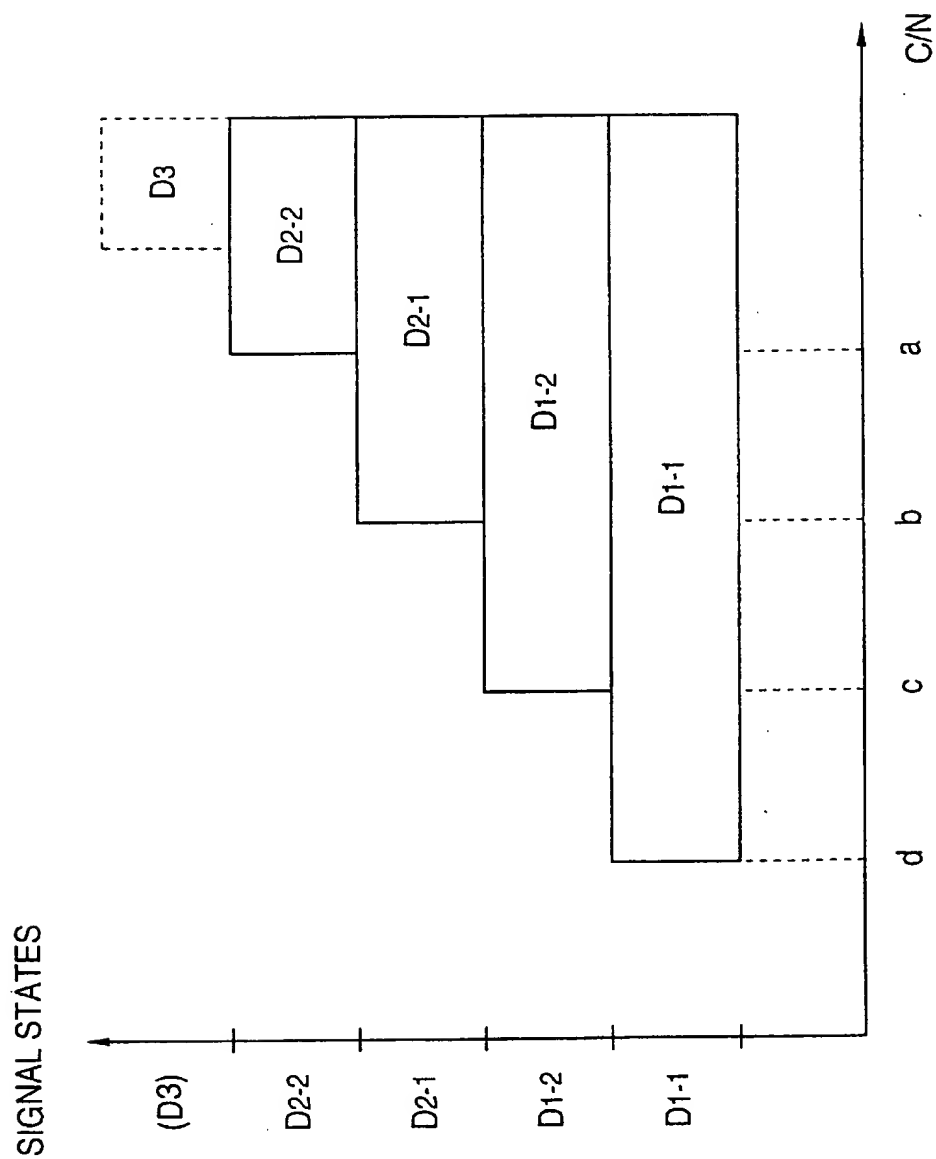


FIG. 86

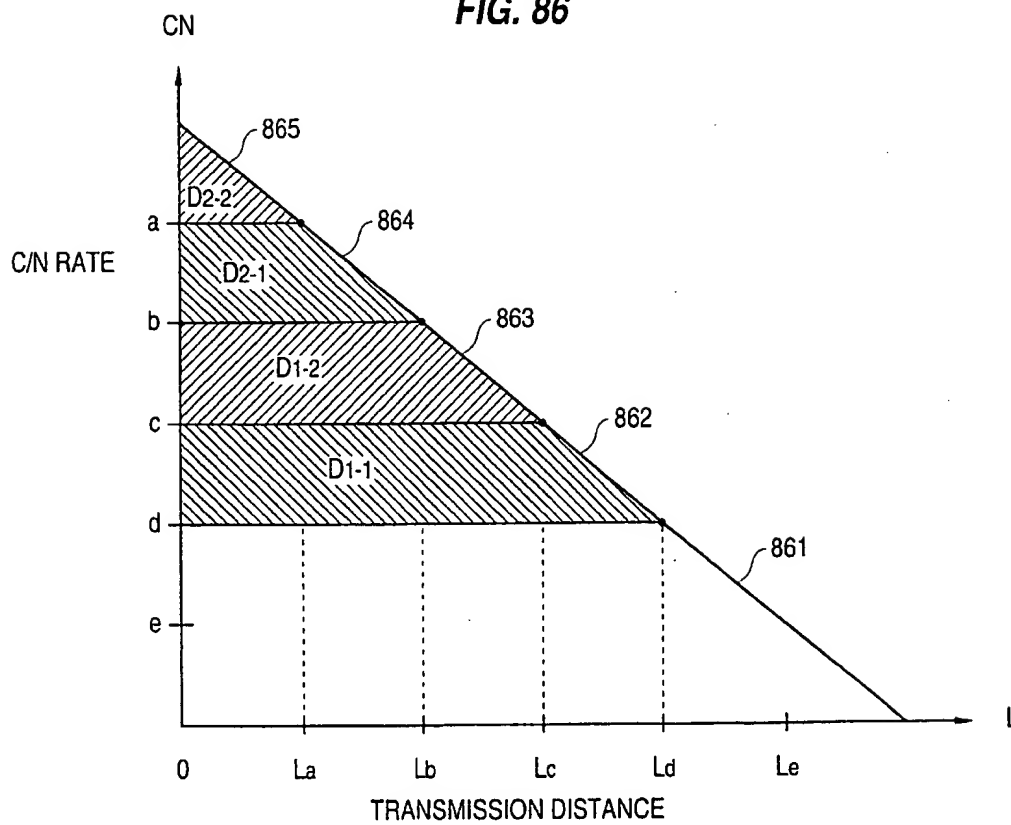


FIG. 87

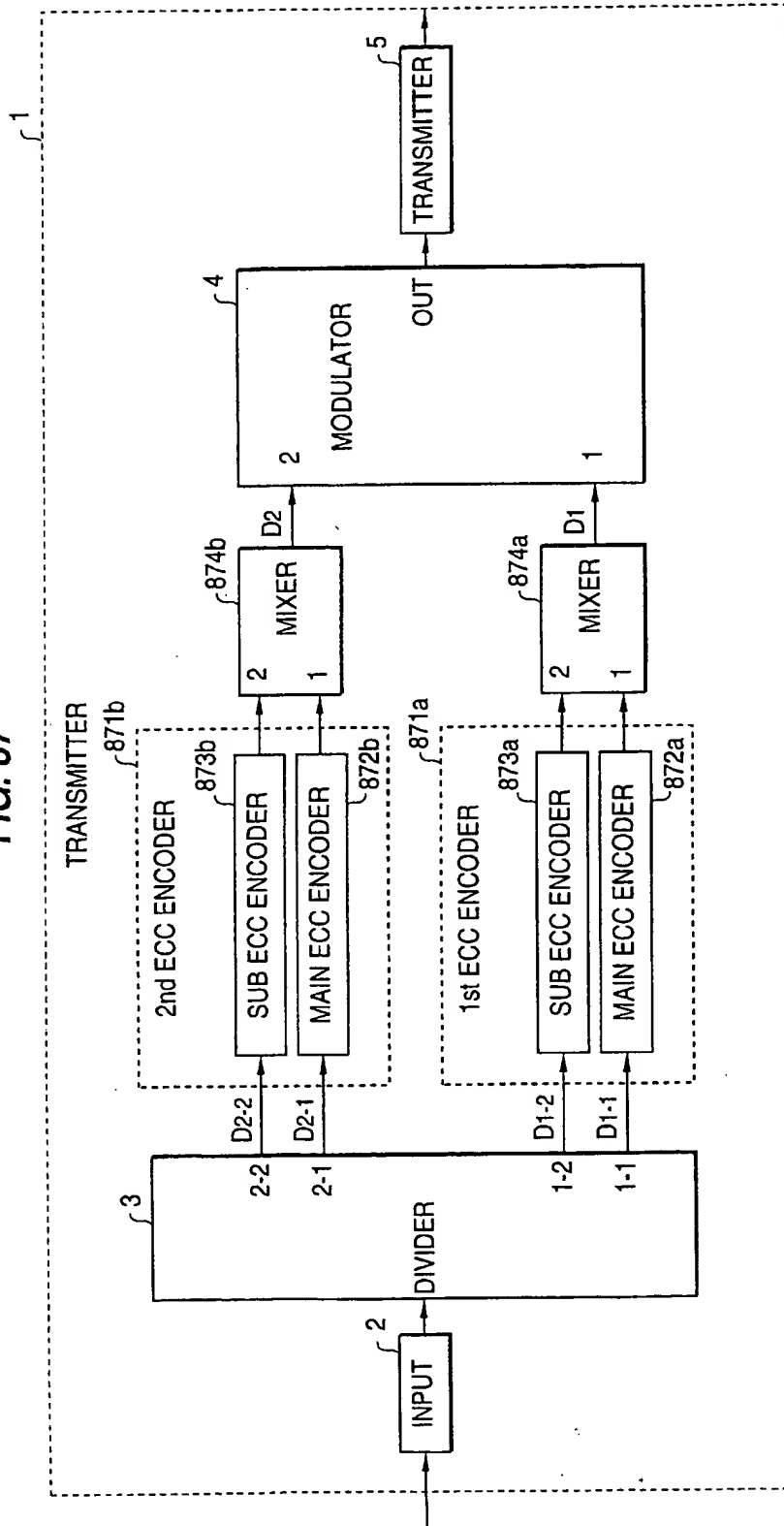


FIG. 88

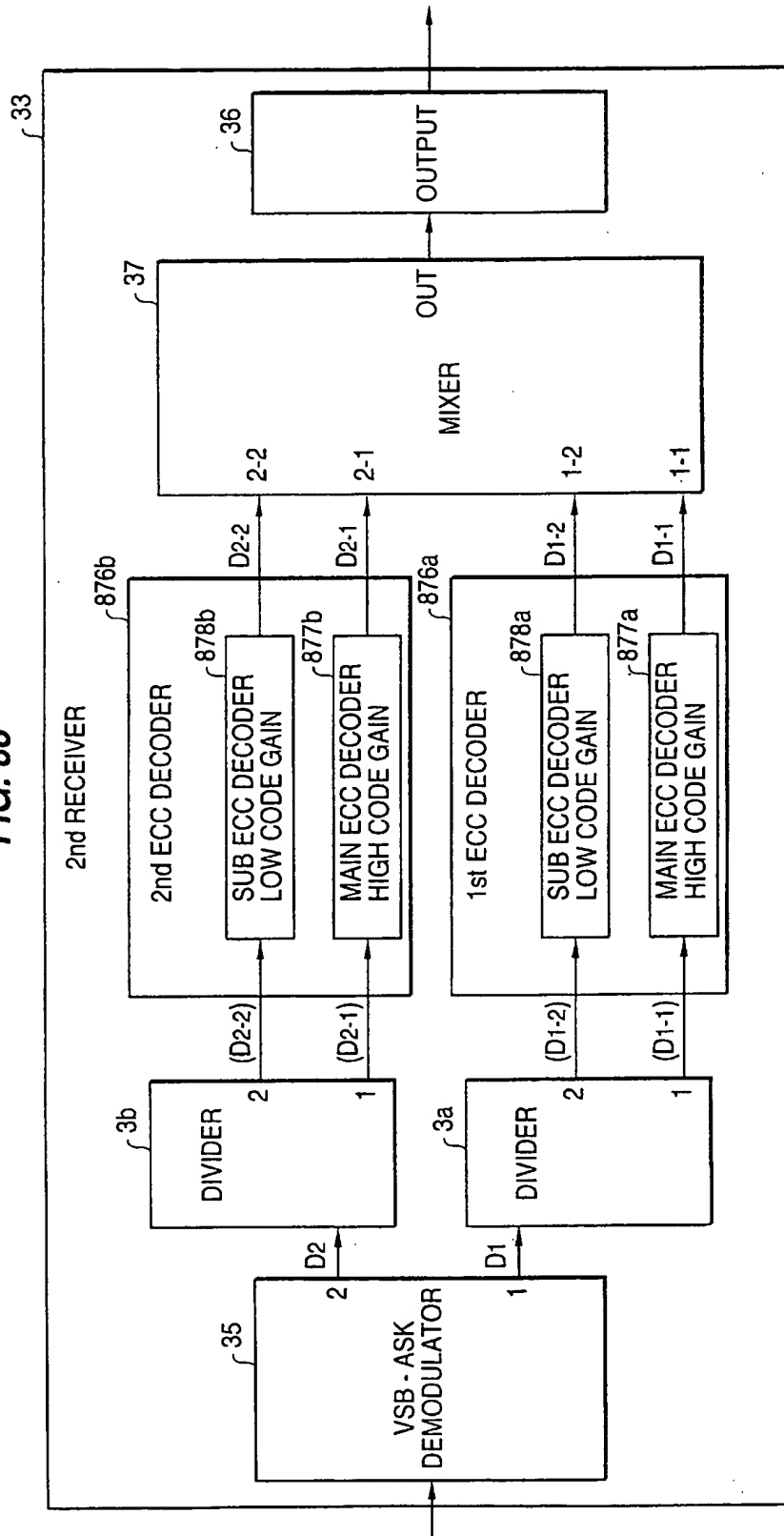


FIG. 89

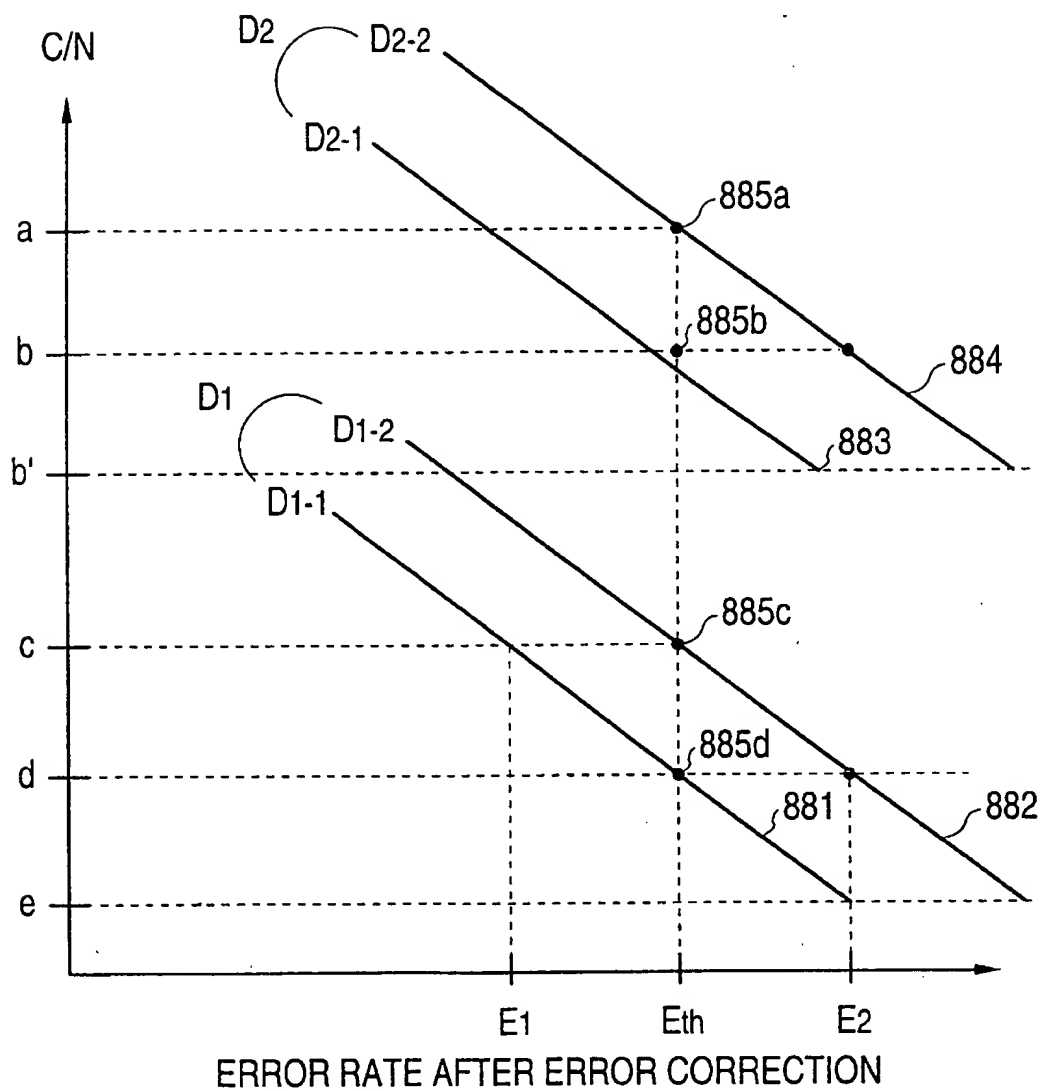
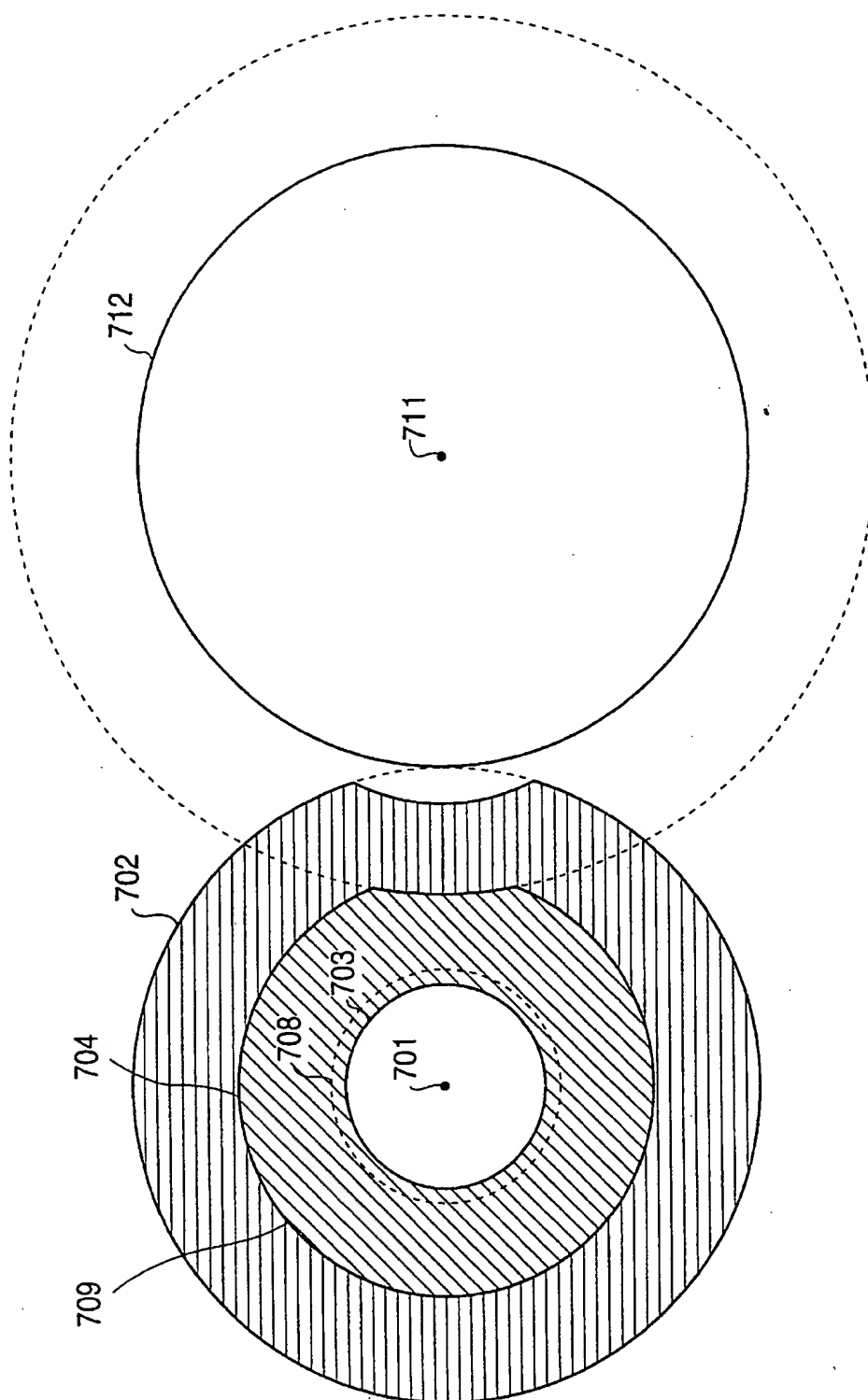
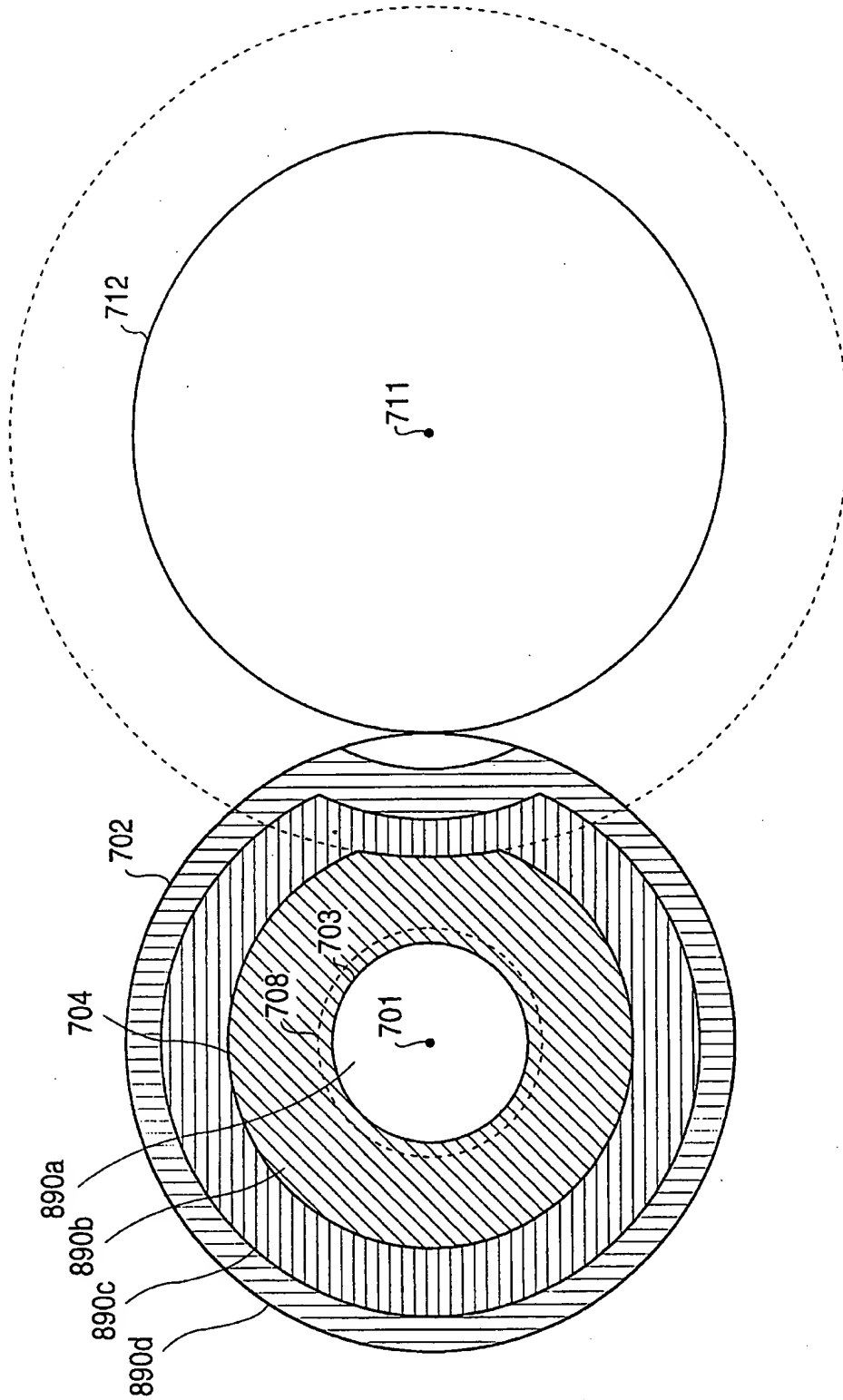


FIG. 90



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FIG. 91



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FIG. 92

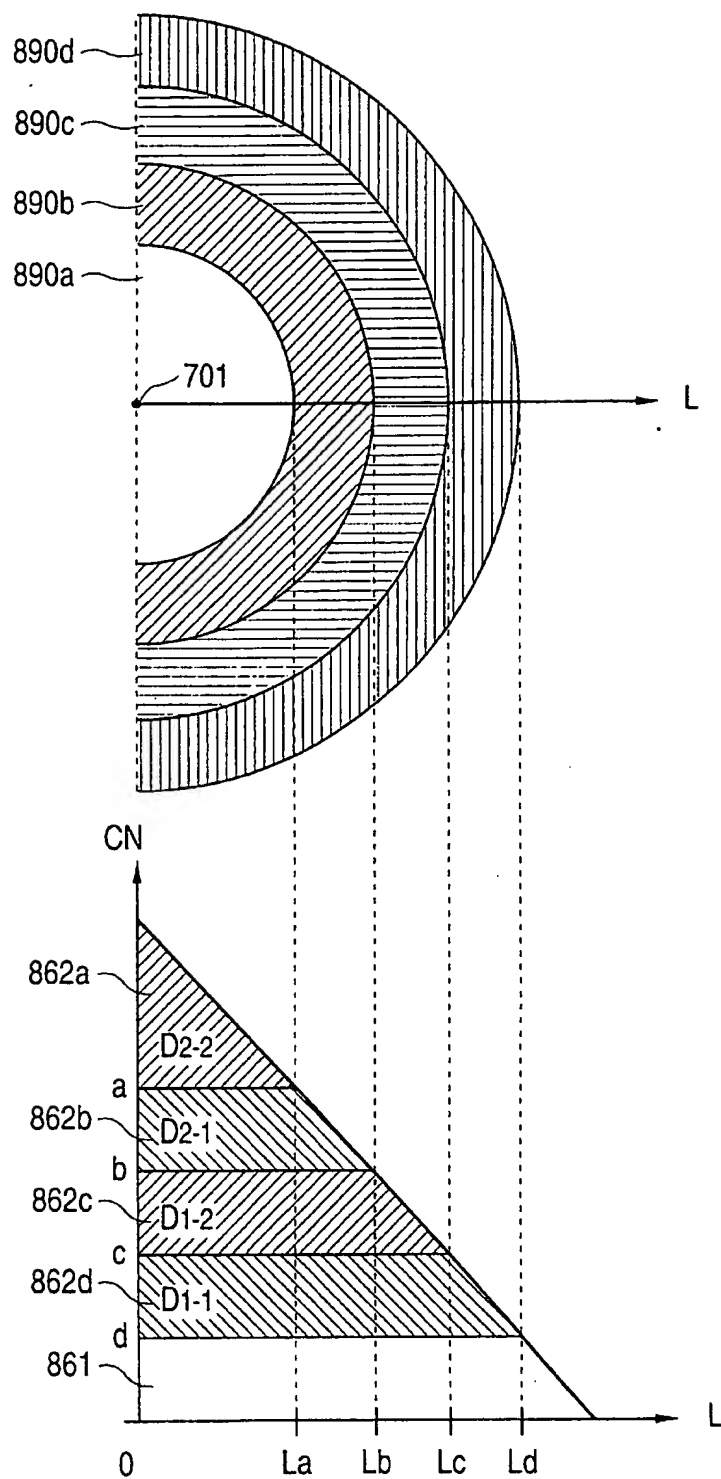


FIG. 93

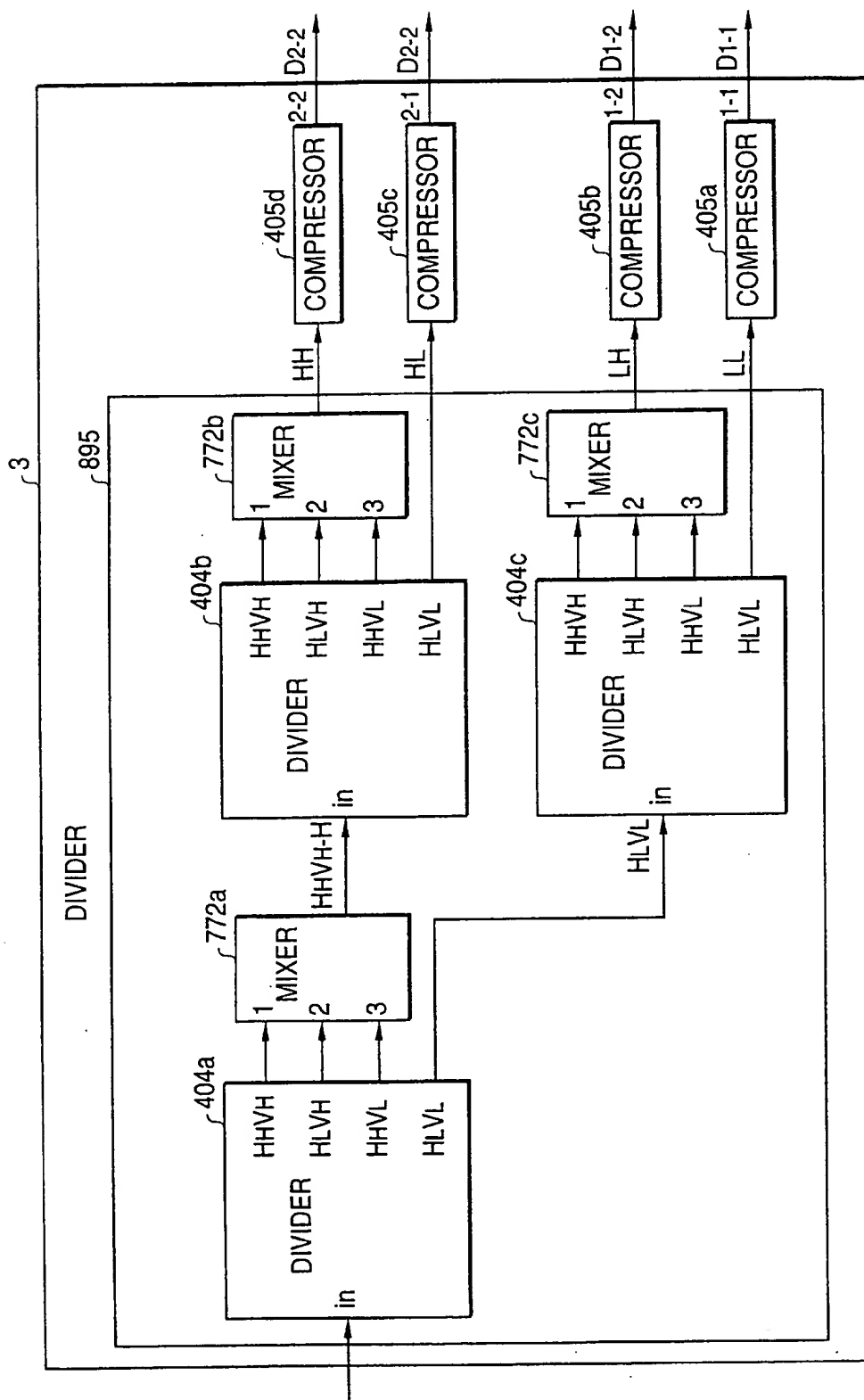


FIG. 94

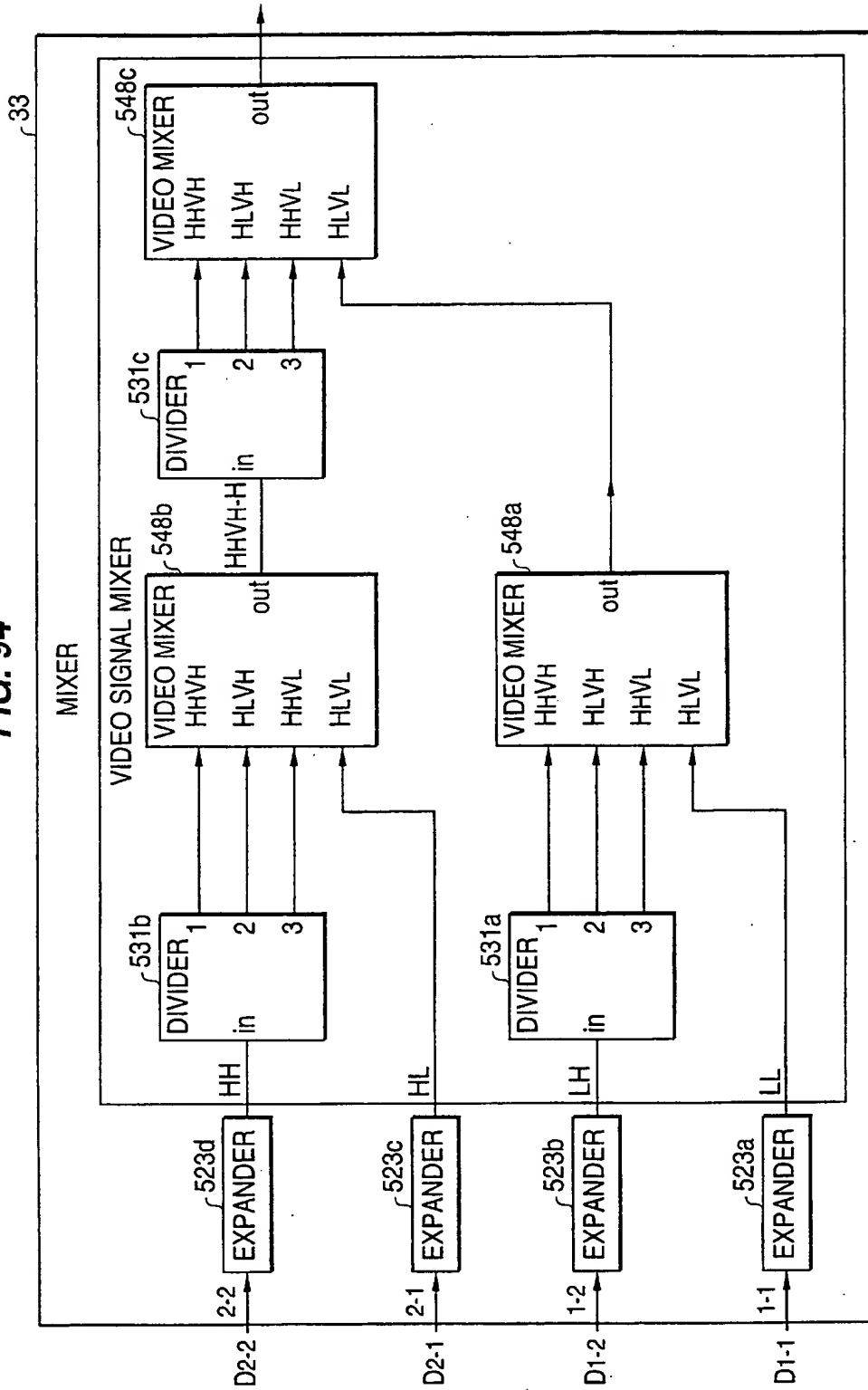


FIG. 95

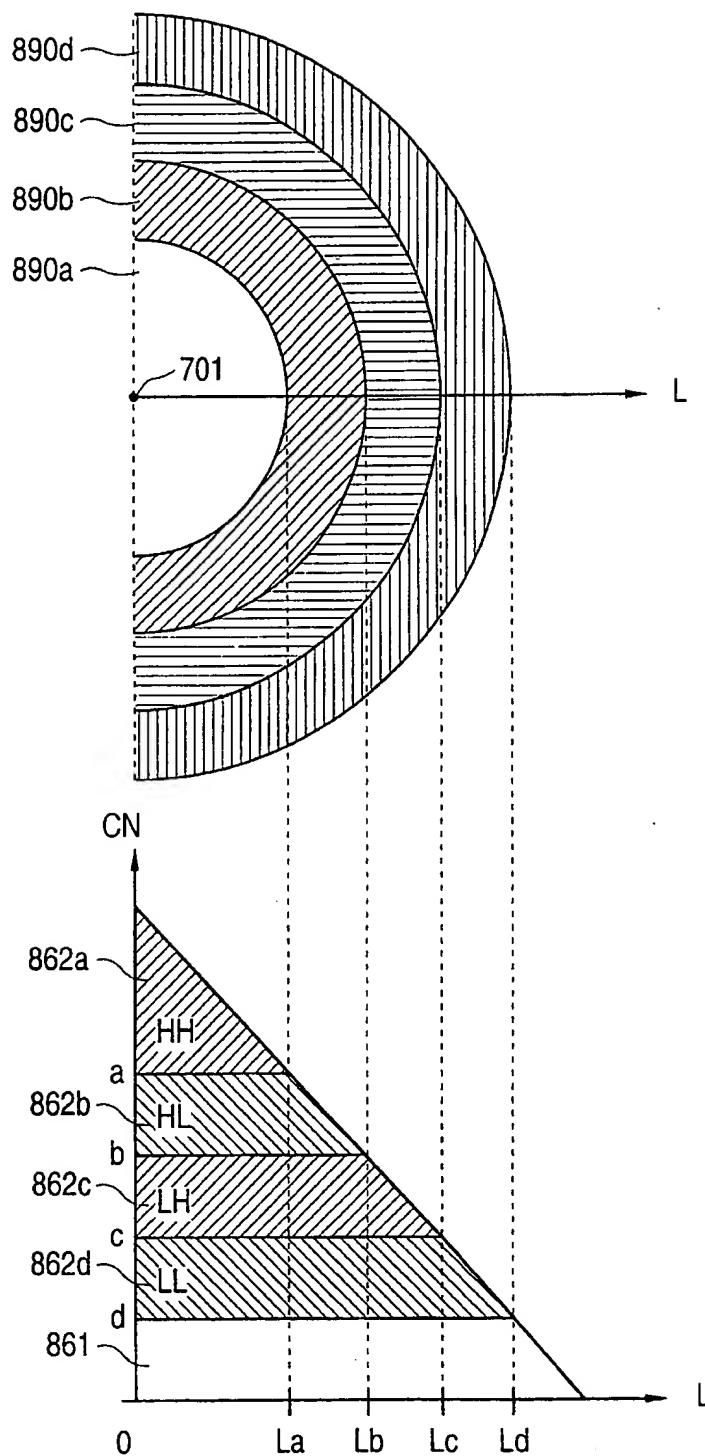


FIG. 96

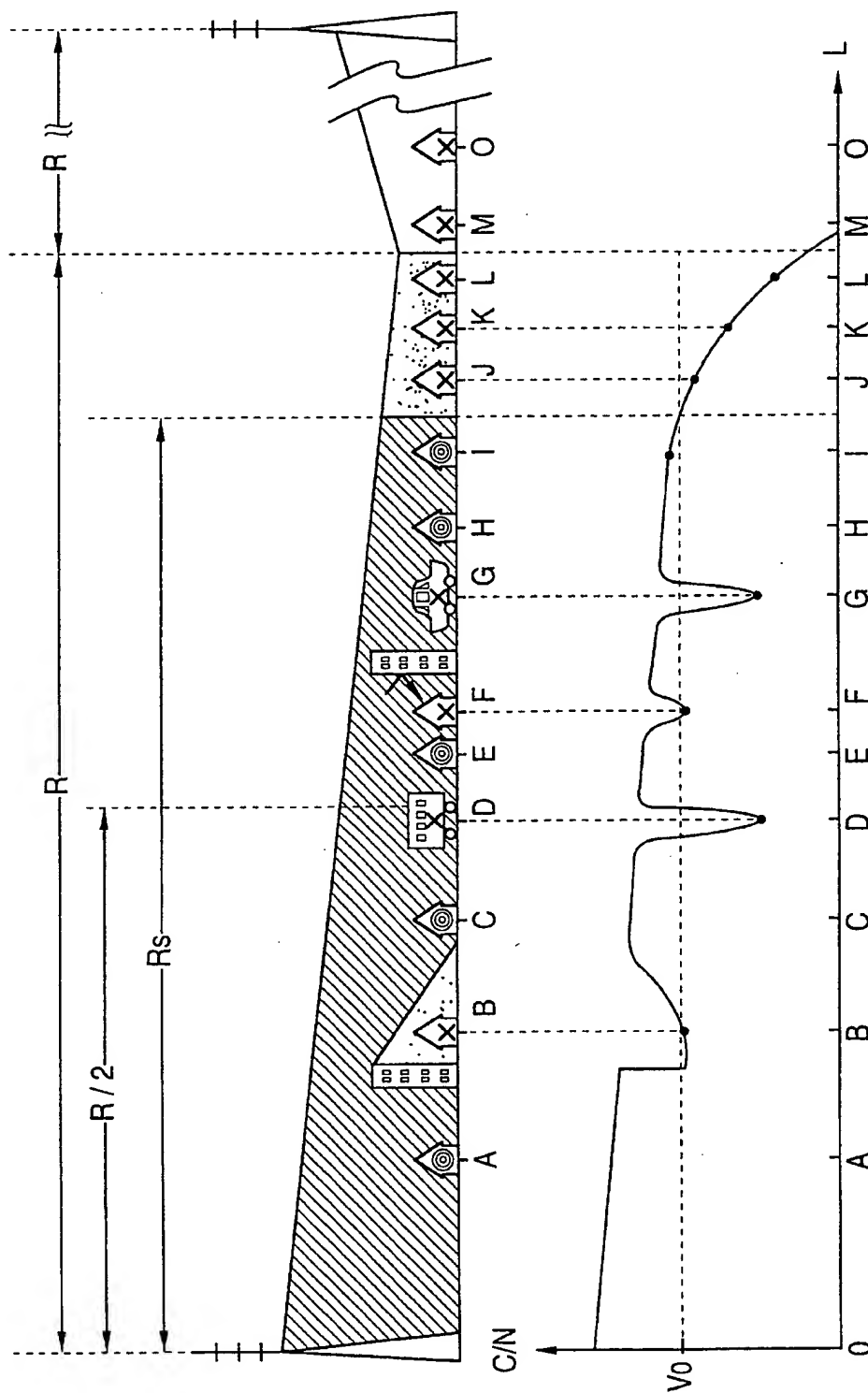


FIG. 97

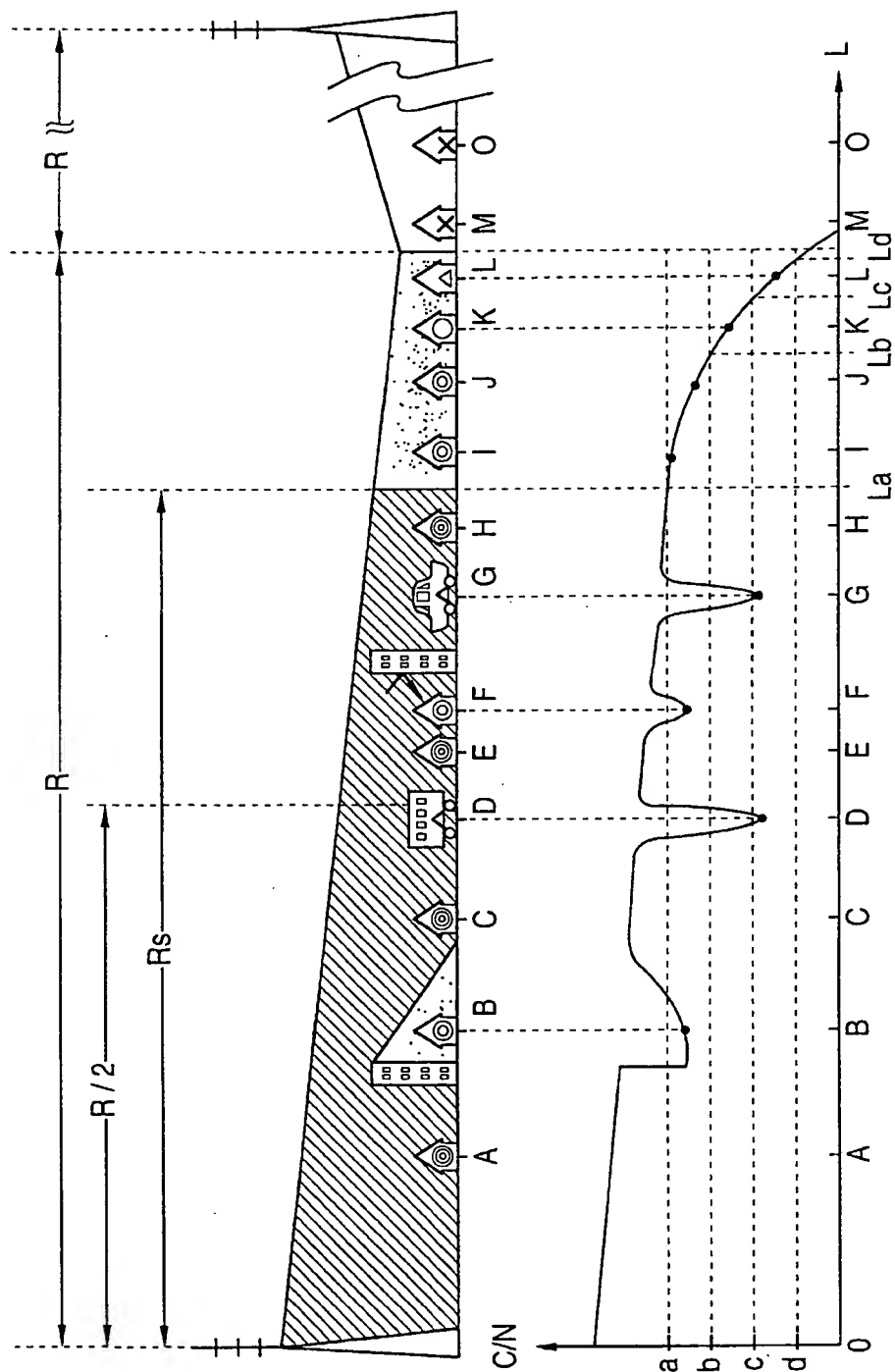
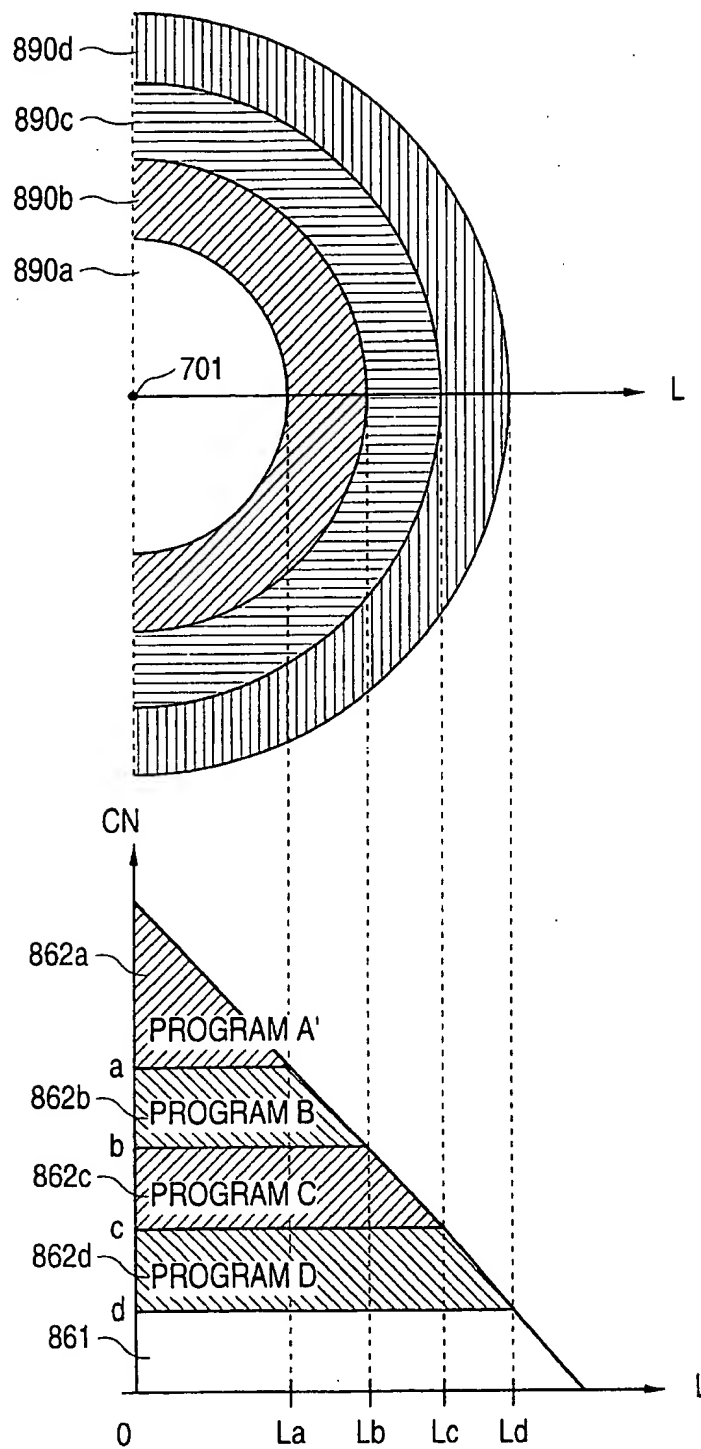
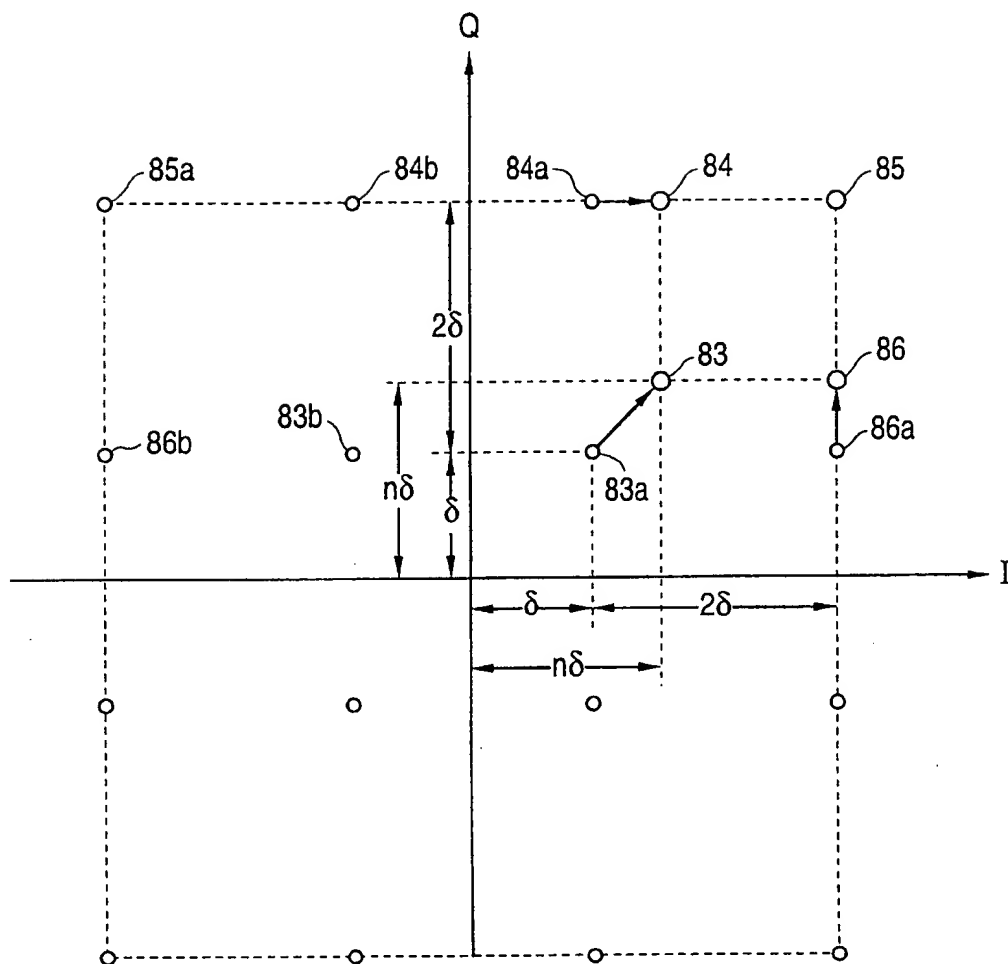


FIG. 98



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FIG. 99



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FIG. 100

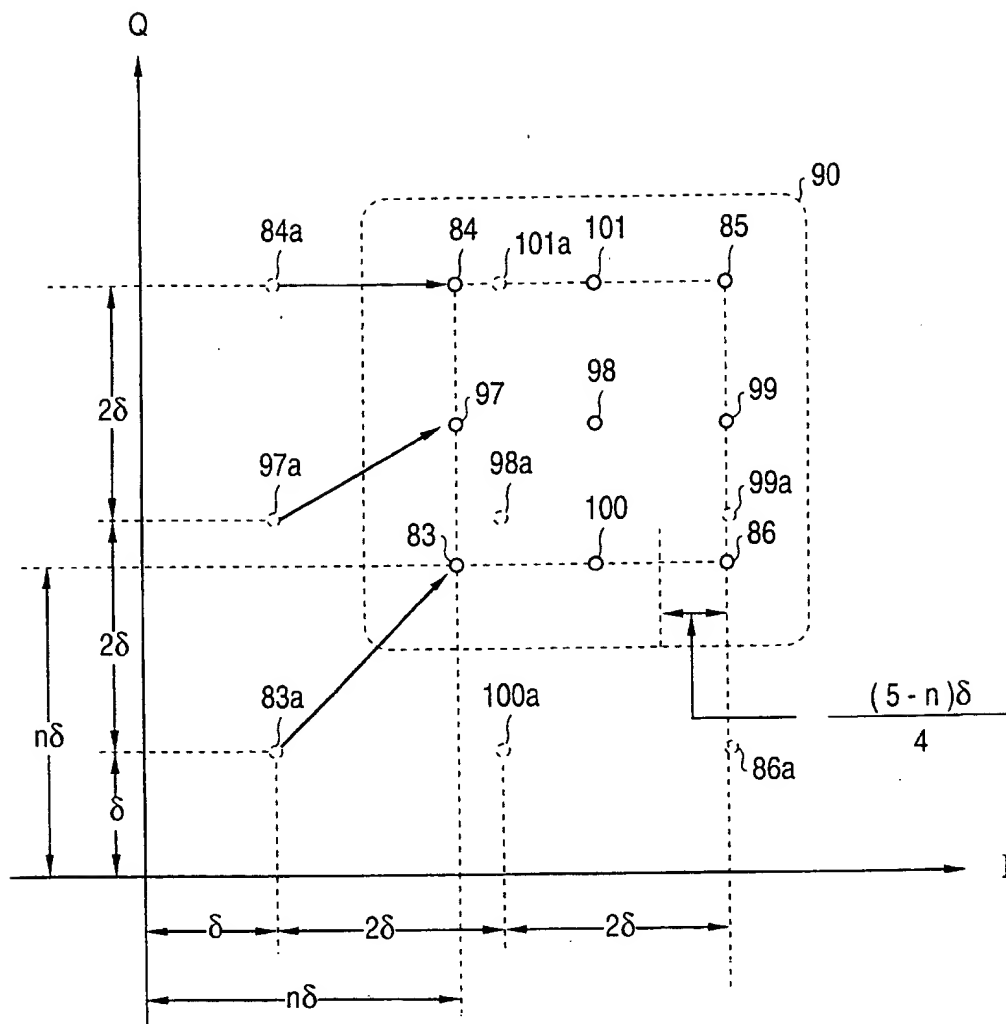


FIG. 101

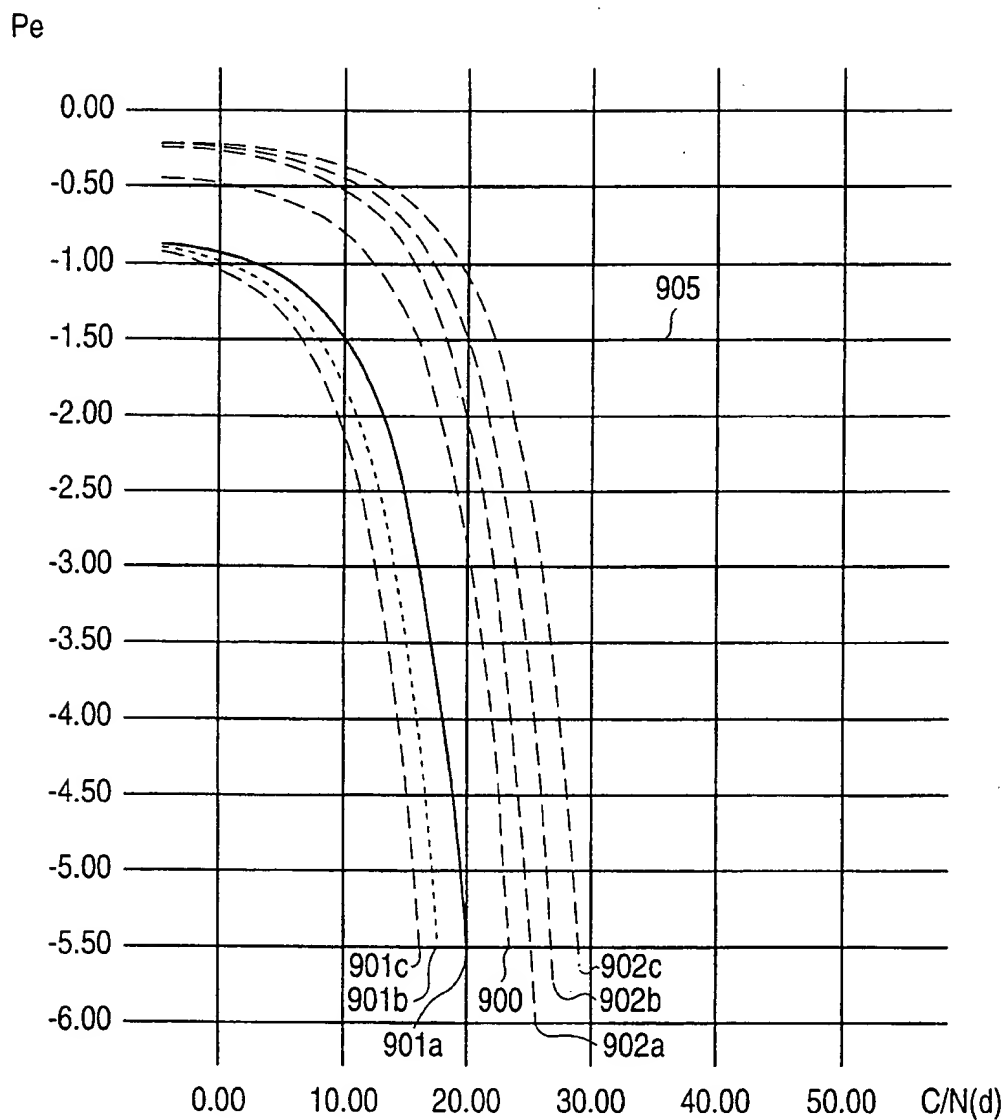


FIG. 102

Pe

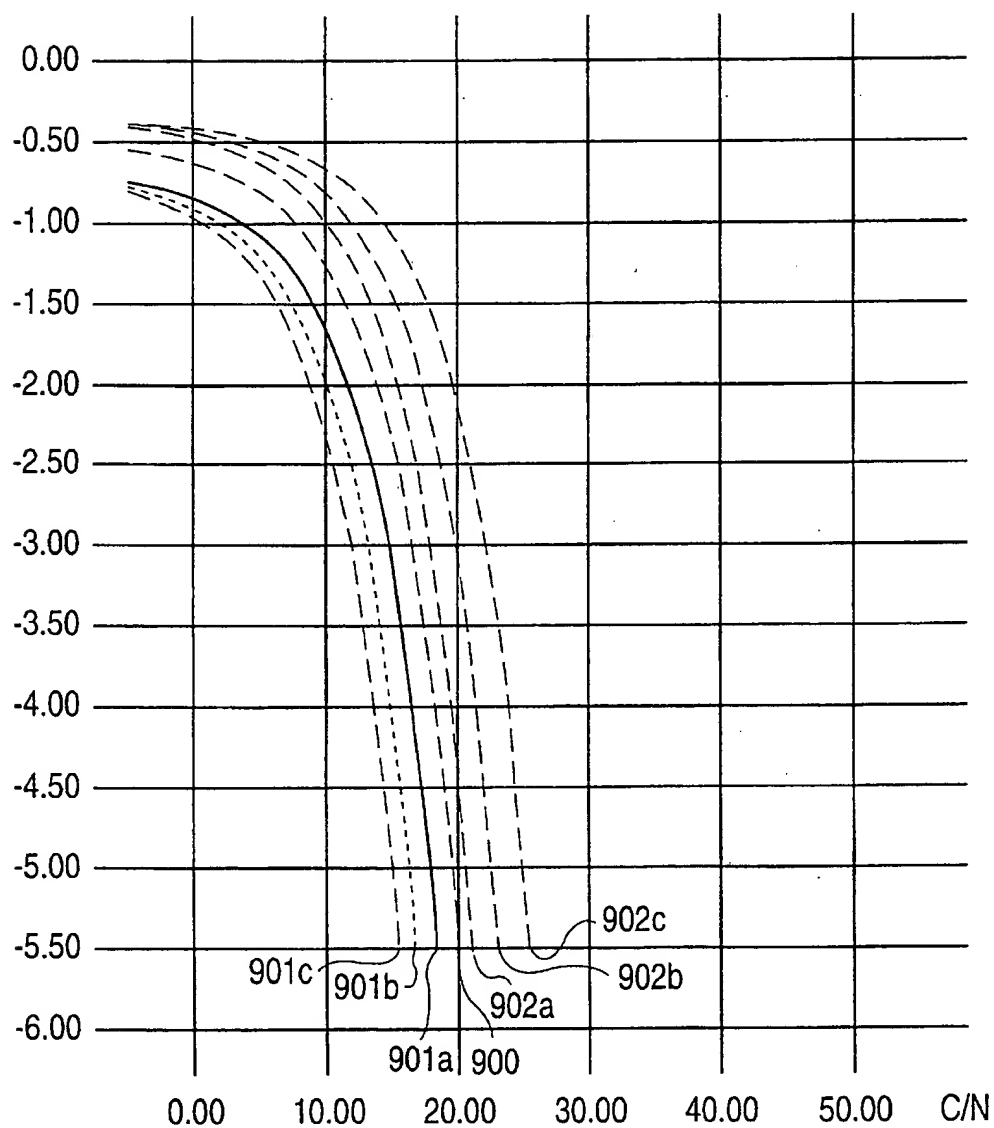


FIG. 103

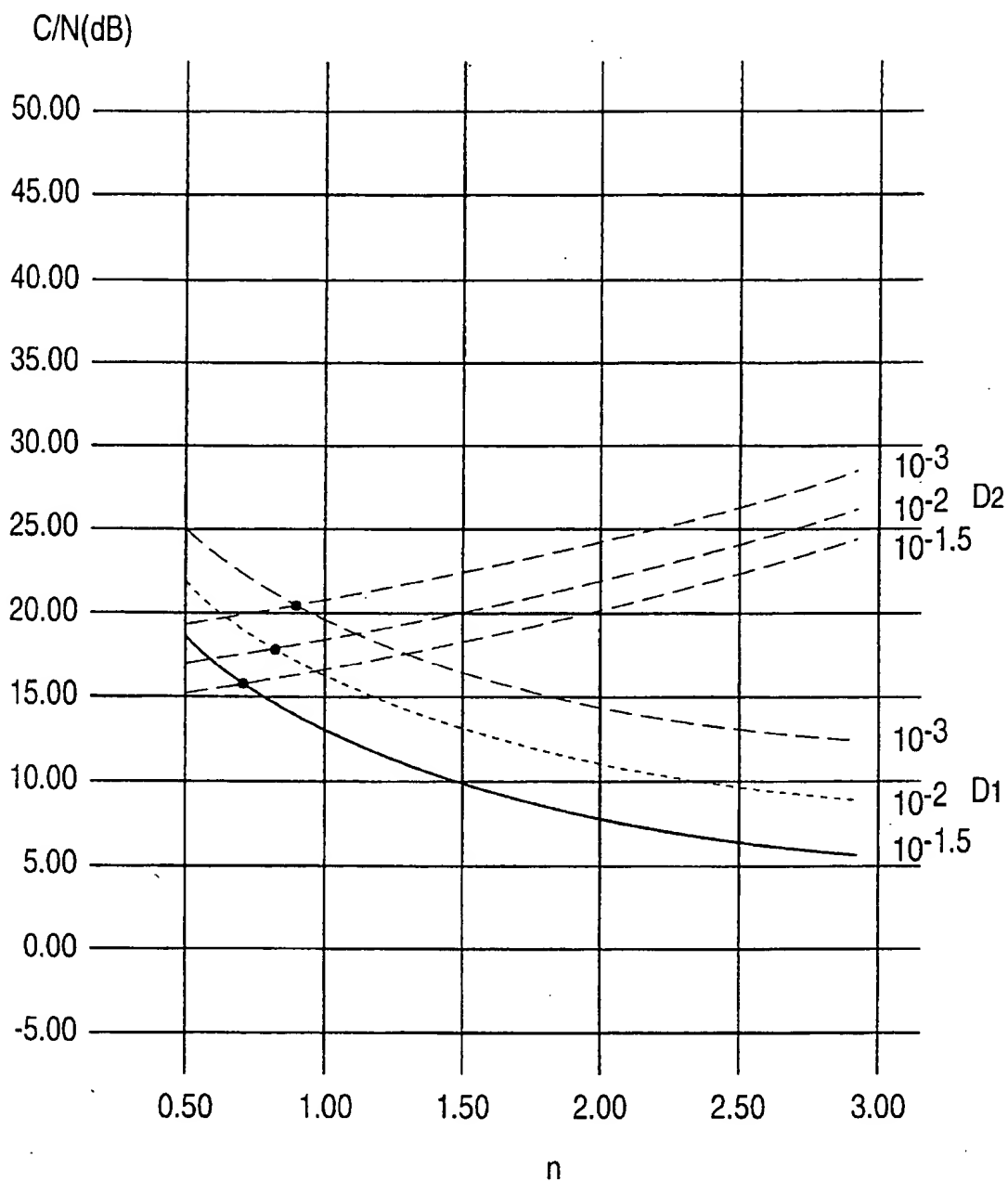


FIG. 104

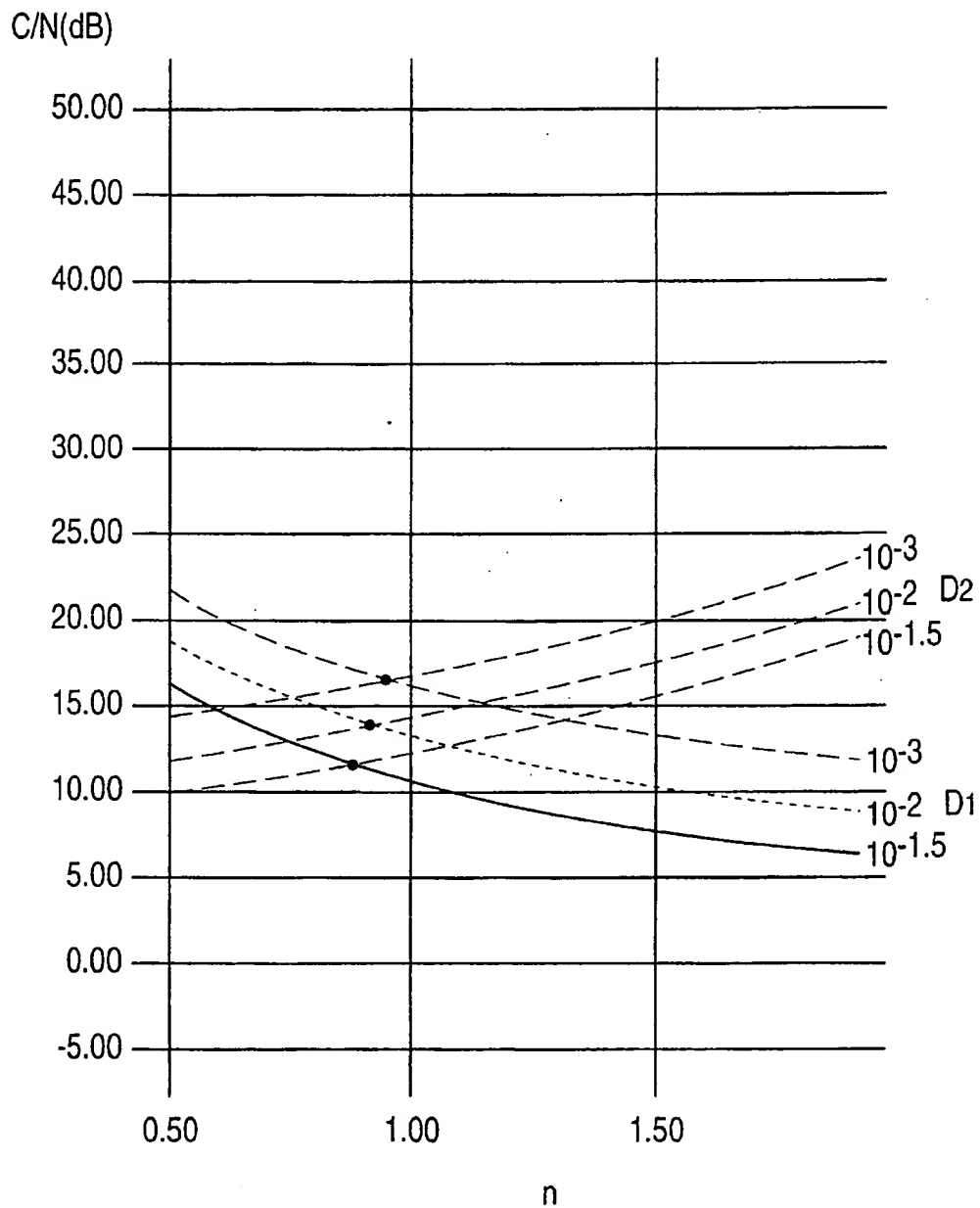


FIG. 105

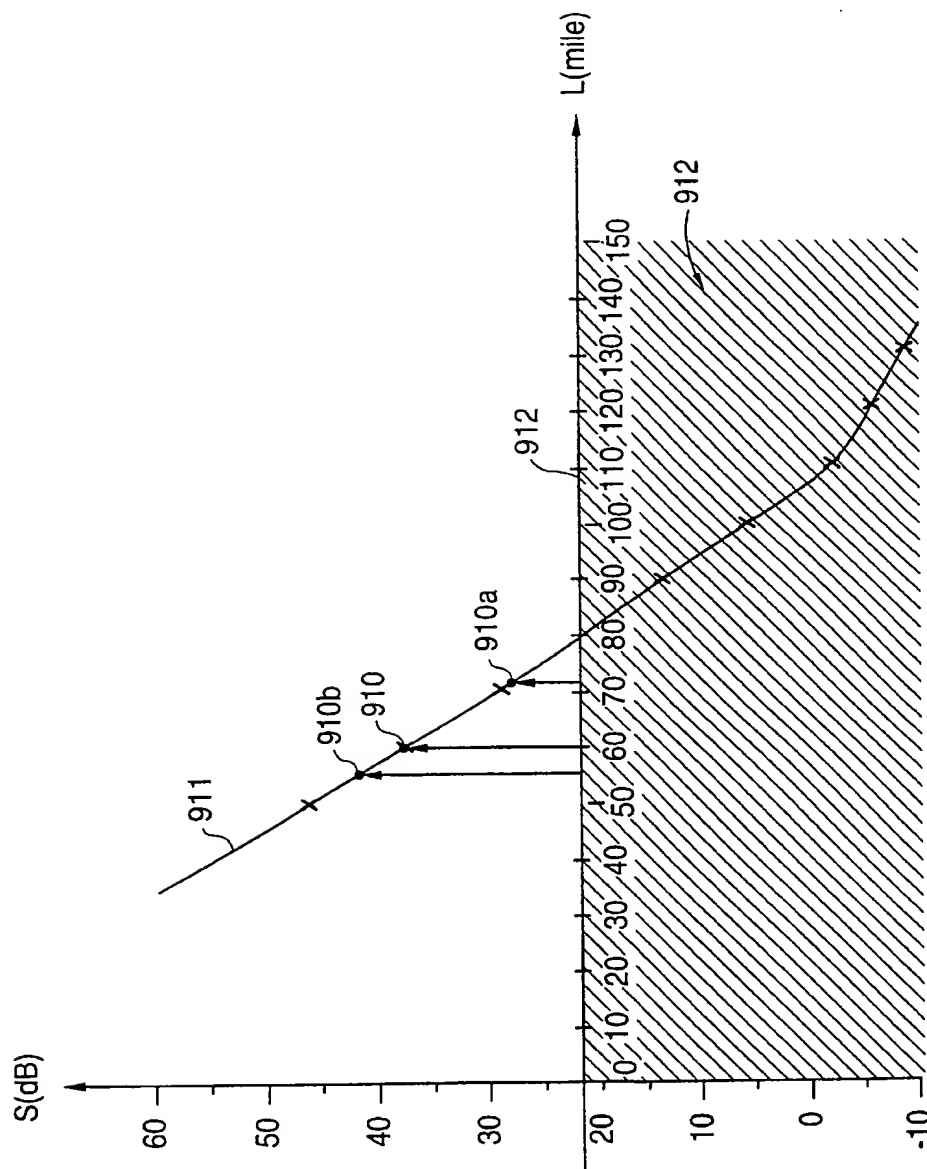


FIG. 106

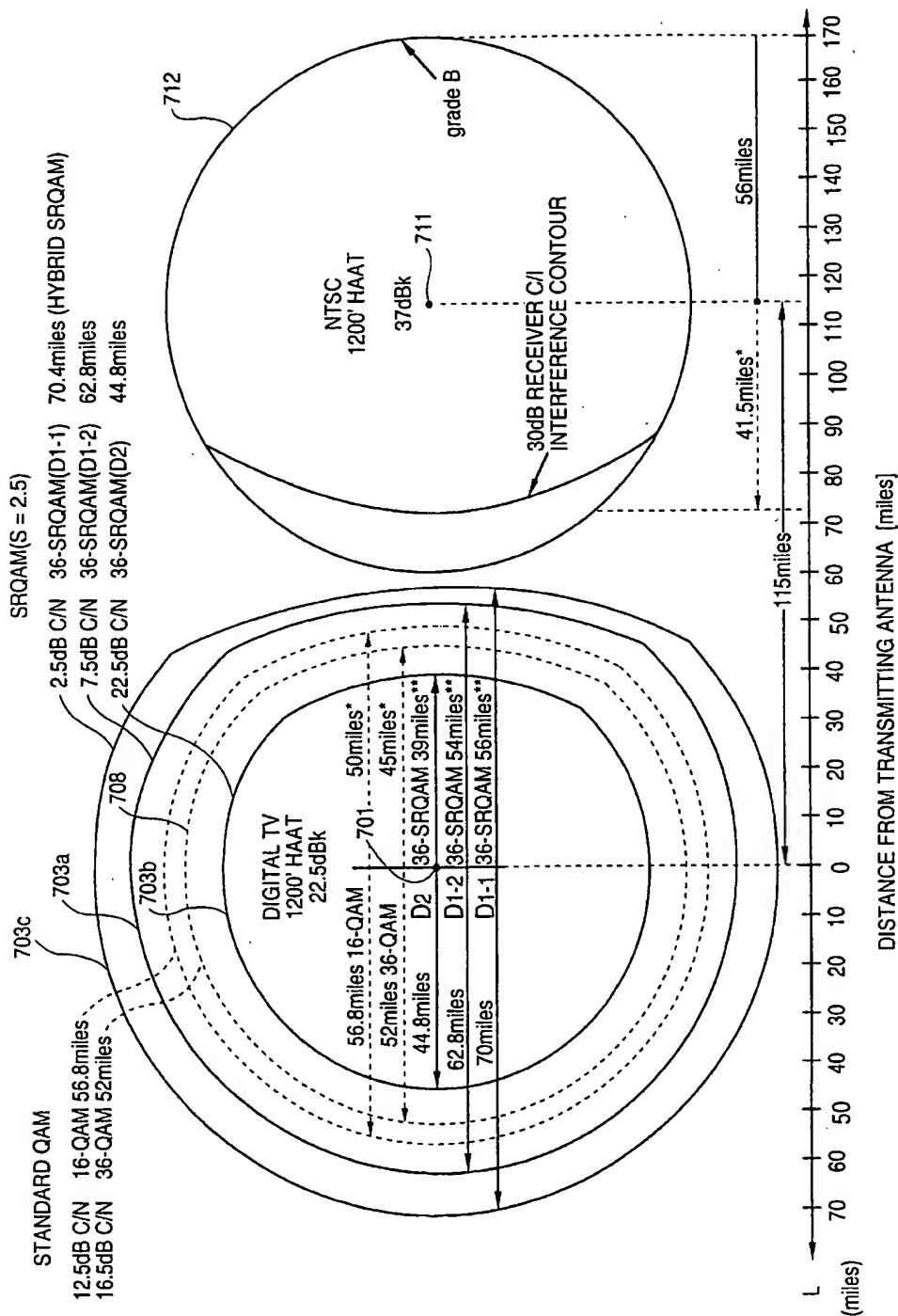


FIG. 107

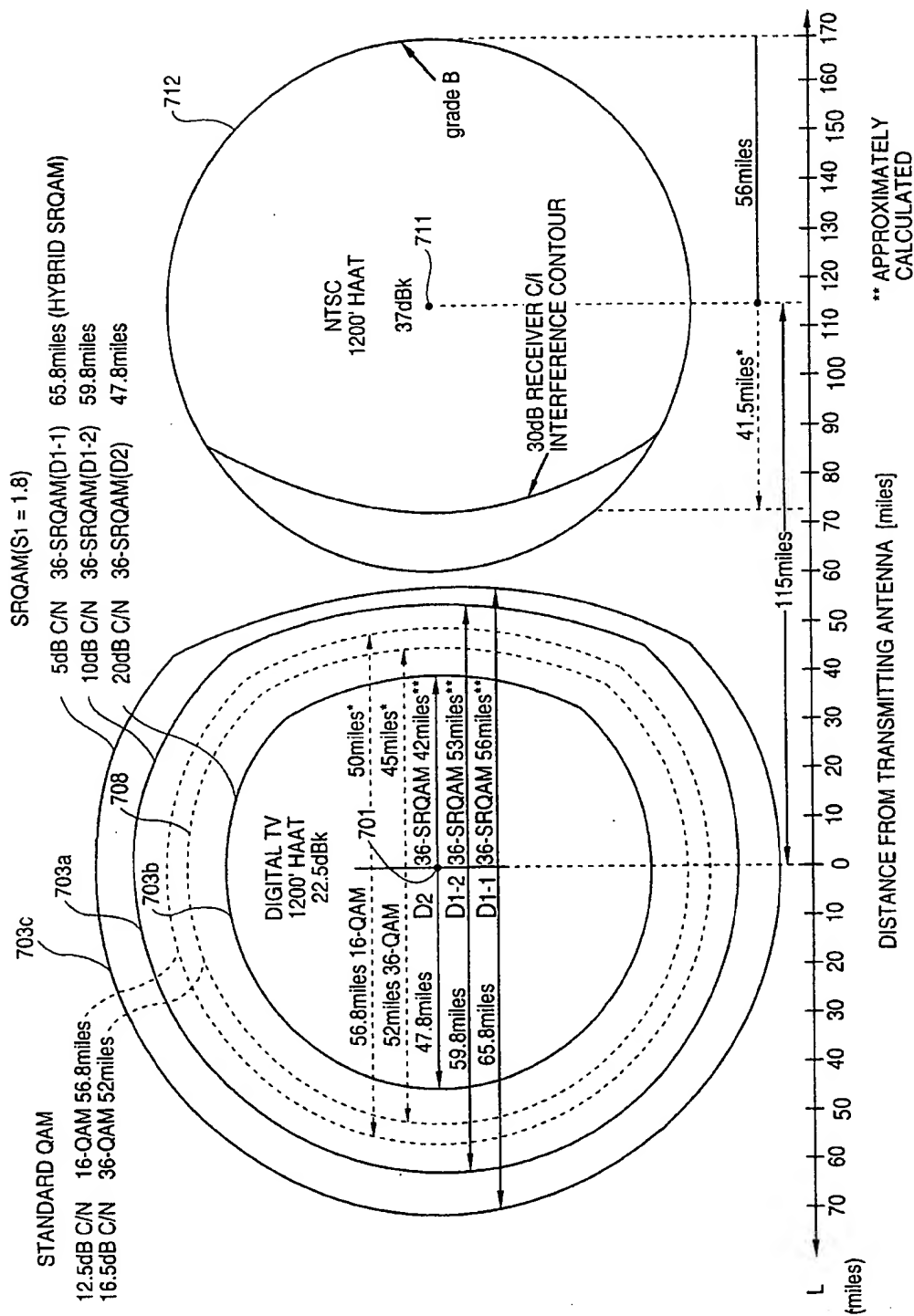


FIG. 108(b)

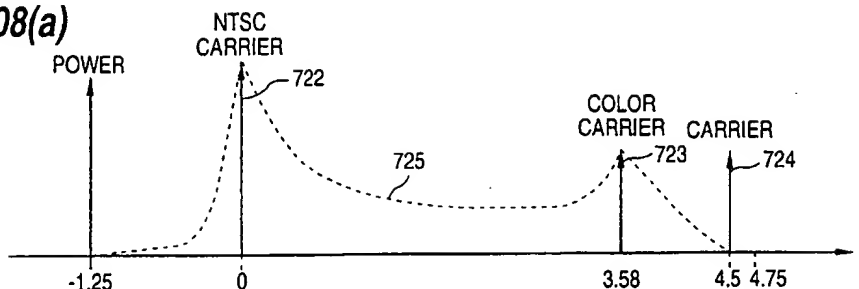


FIG. 108(b)

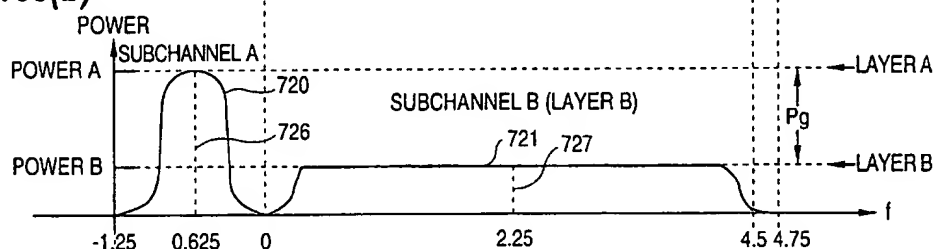


FIG. 108(c)

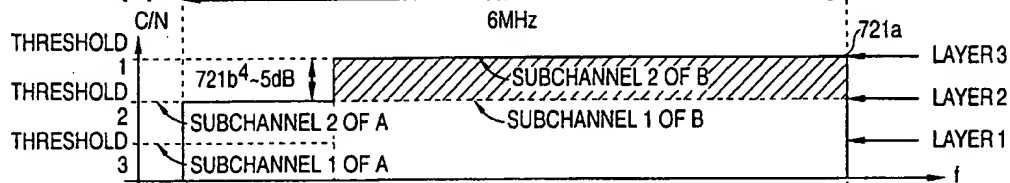


FIG. 108(d)

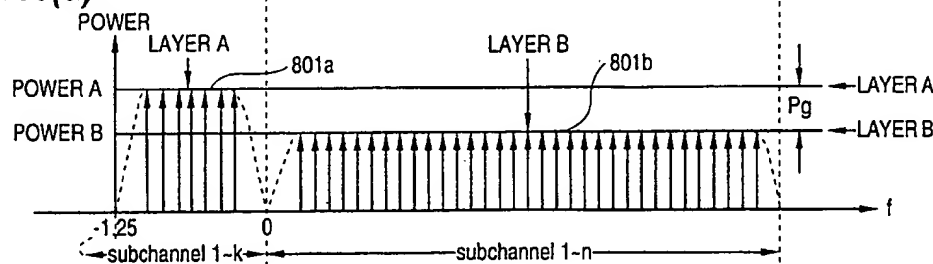
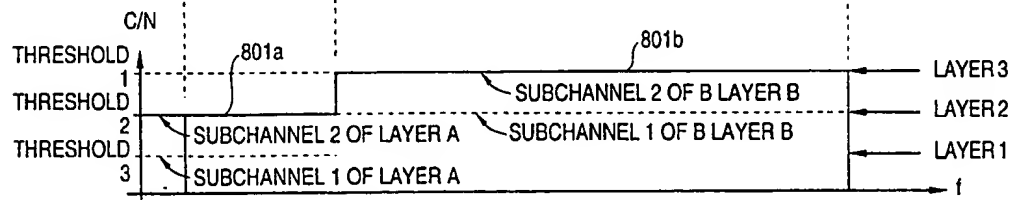


FIG. 108(e)



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FIG. 109

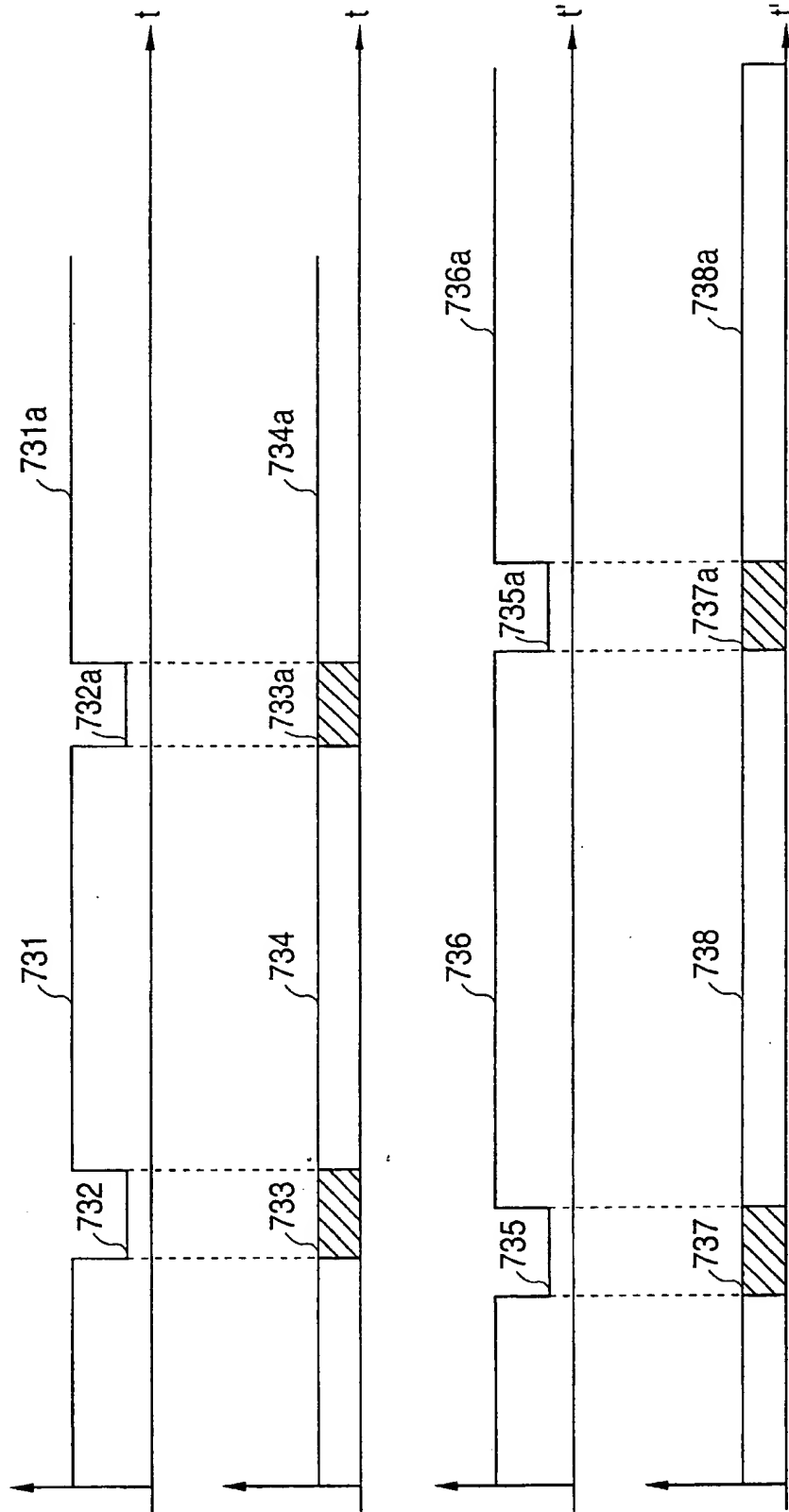


FIG. 110

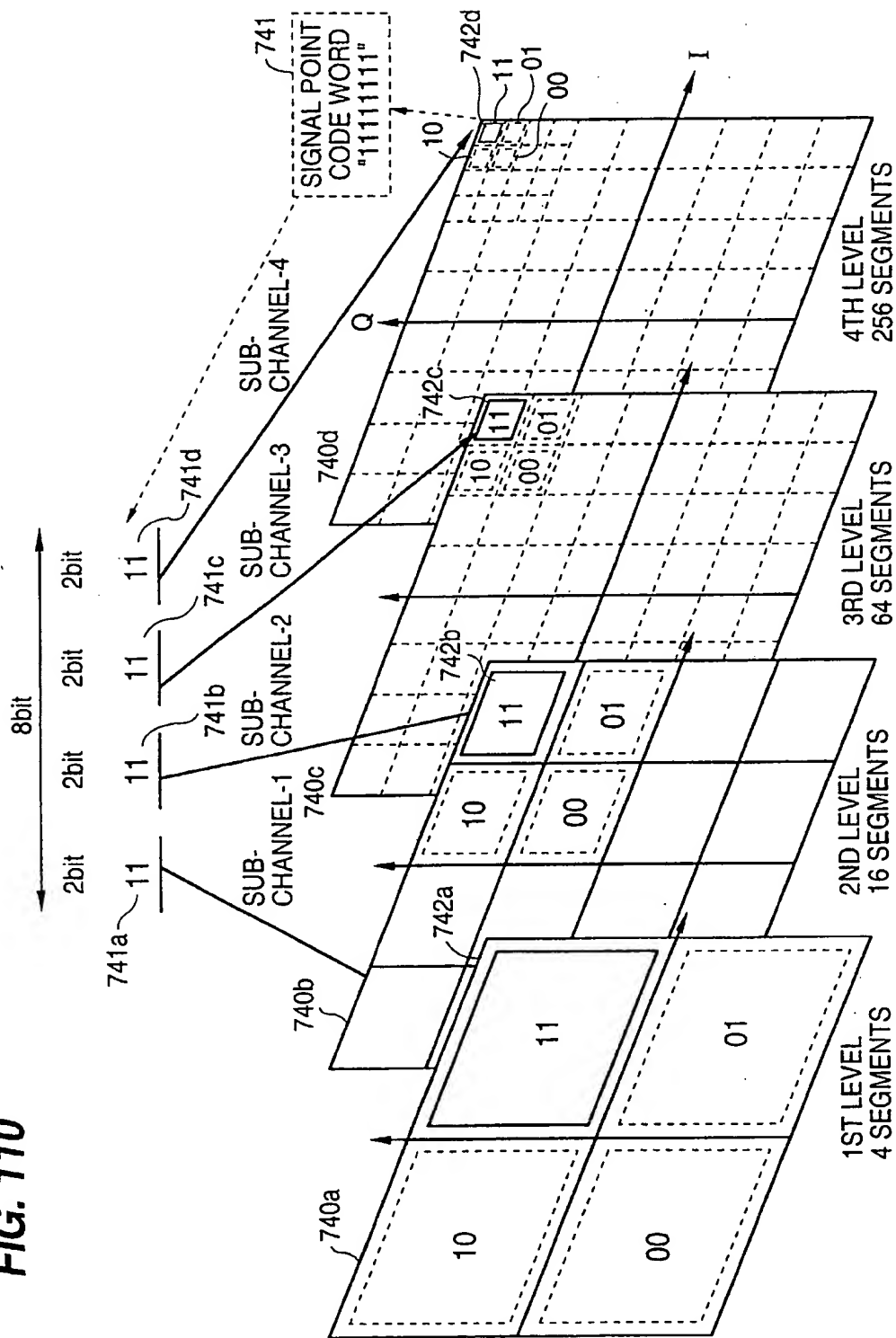
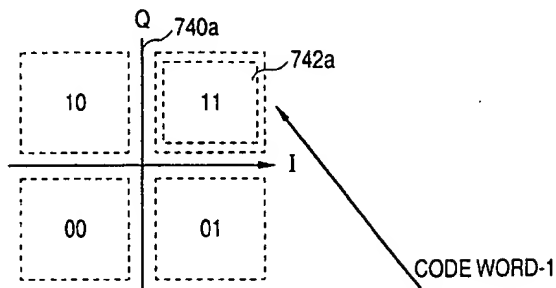
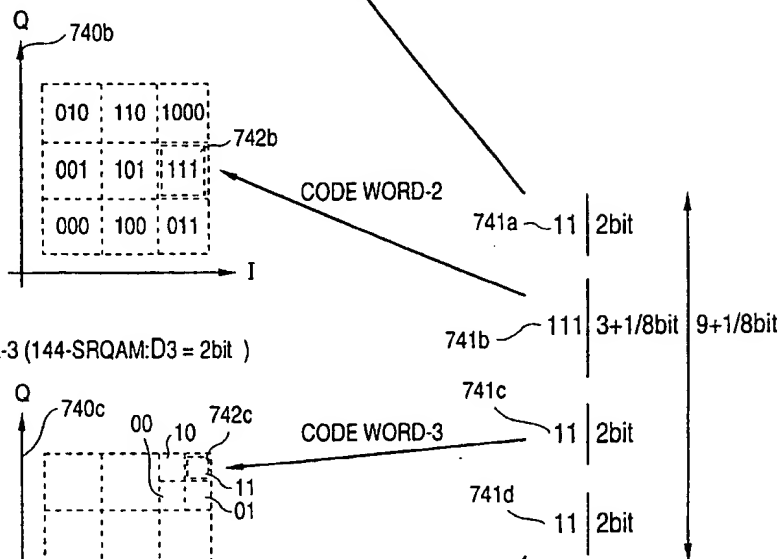


FIG. 111

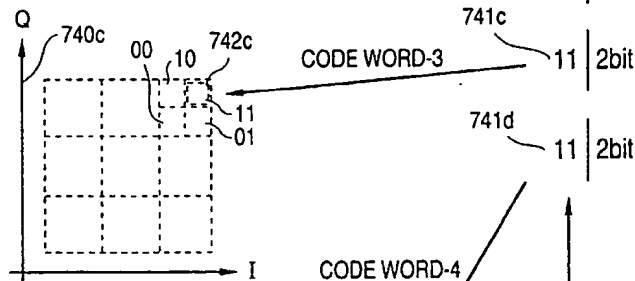
SUBCHANNEL-1 (SRQAM:D1 = 2bit)



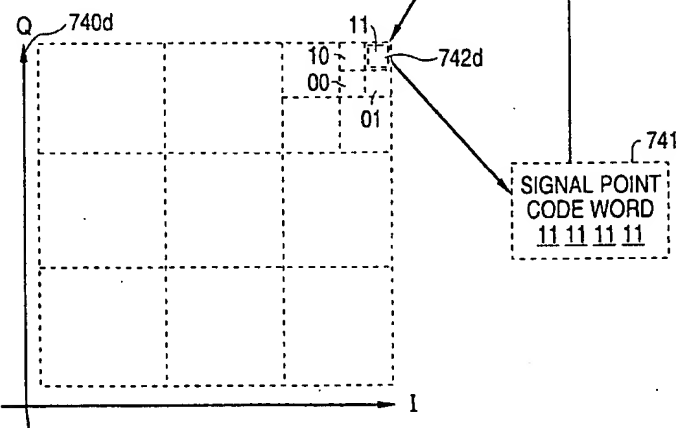
SUBCHANNEL-2 (36-SRQAM:D2 = 3bit + 1/8bit)



SUBCHANNEL-3 (144-SRQAM:D3 = 2bit)



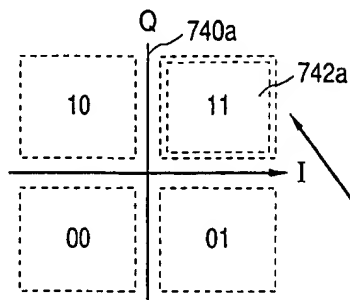
SUBCHANNEL-4 (576-SRQAM:D4 = 2bit)



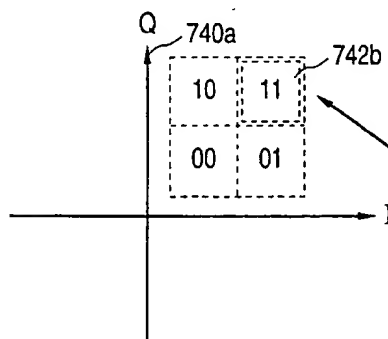
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FIG. 112

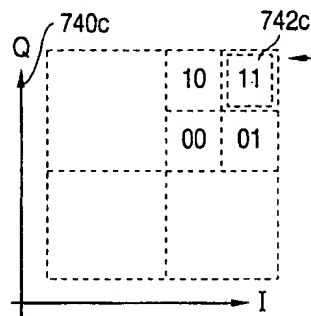
SUBCHANNEL-1 (SRQAM:D1 = 2bit)



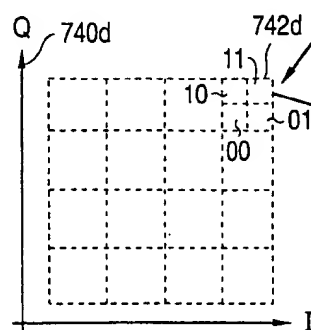
SUBCHANNEL-2 (16-SRQAM:D2 = 2bit)



SUBCHANNEL-3 (64-SRQAM:D3 = 2bit)



SUBCHANNEL-4 (256-SRQAM:D4 = 2bit)

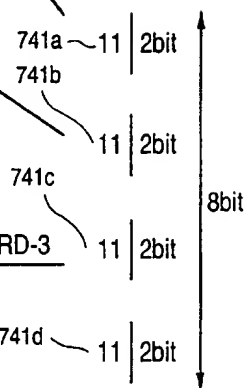


CODE WORD-1

CODE WORD-2

CODE WORD-3

CODE WORD-4



SIGNAL POINT
CODE WORD
11 11 11 11

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FIG. 113

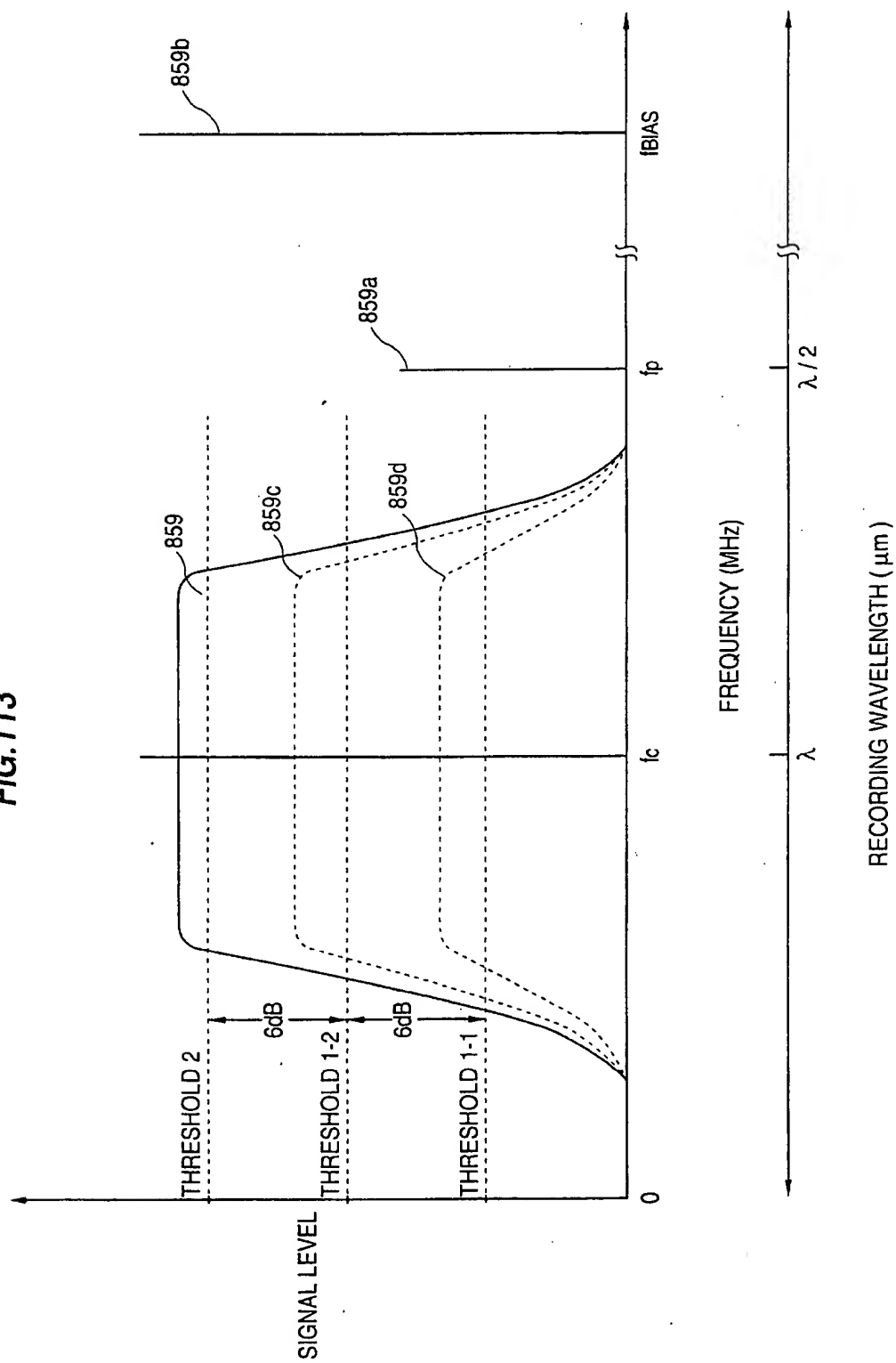


FIG. 114

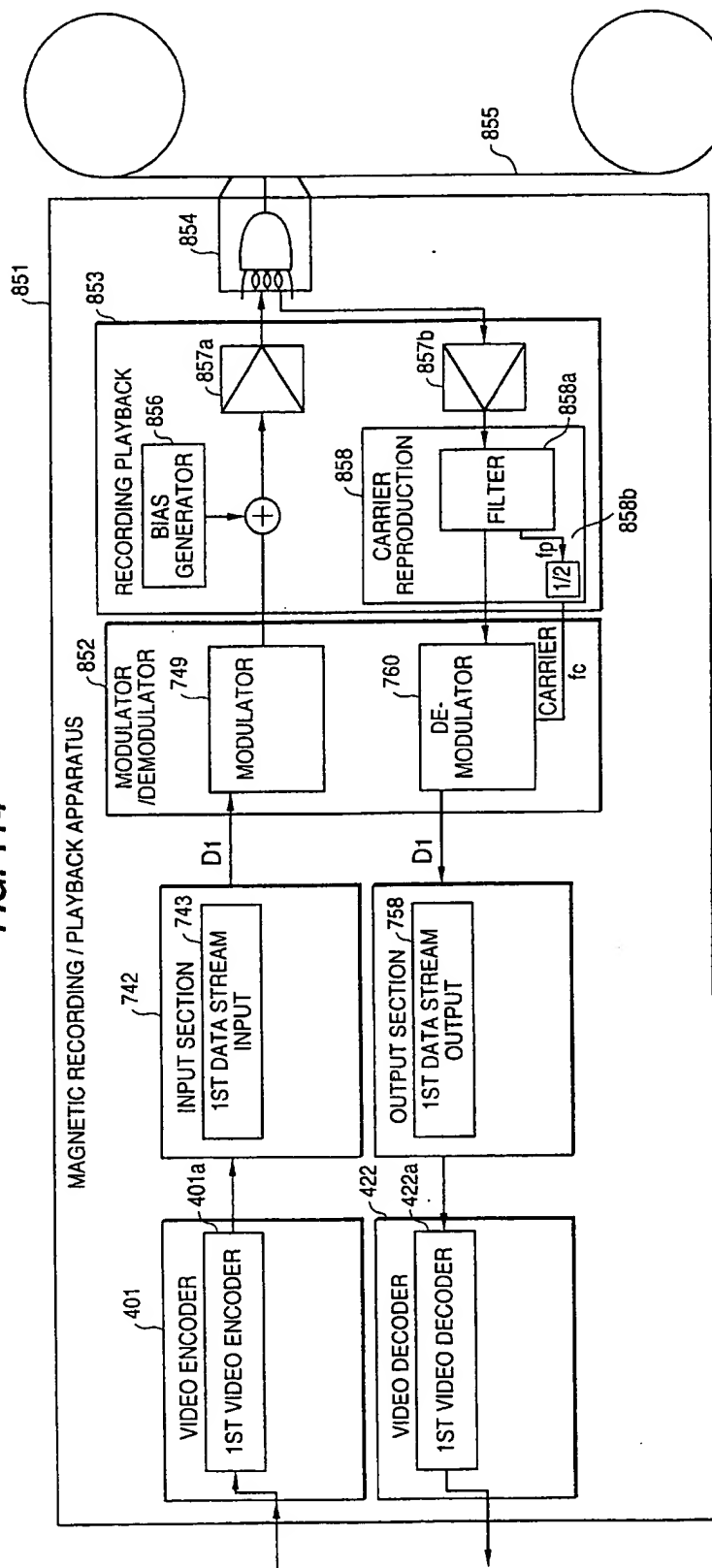


FIG. 115

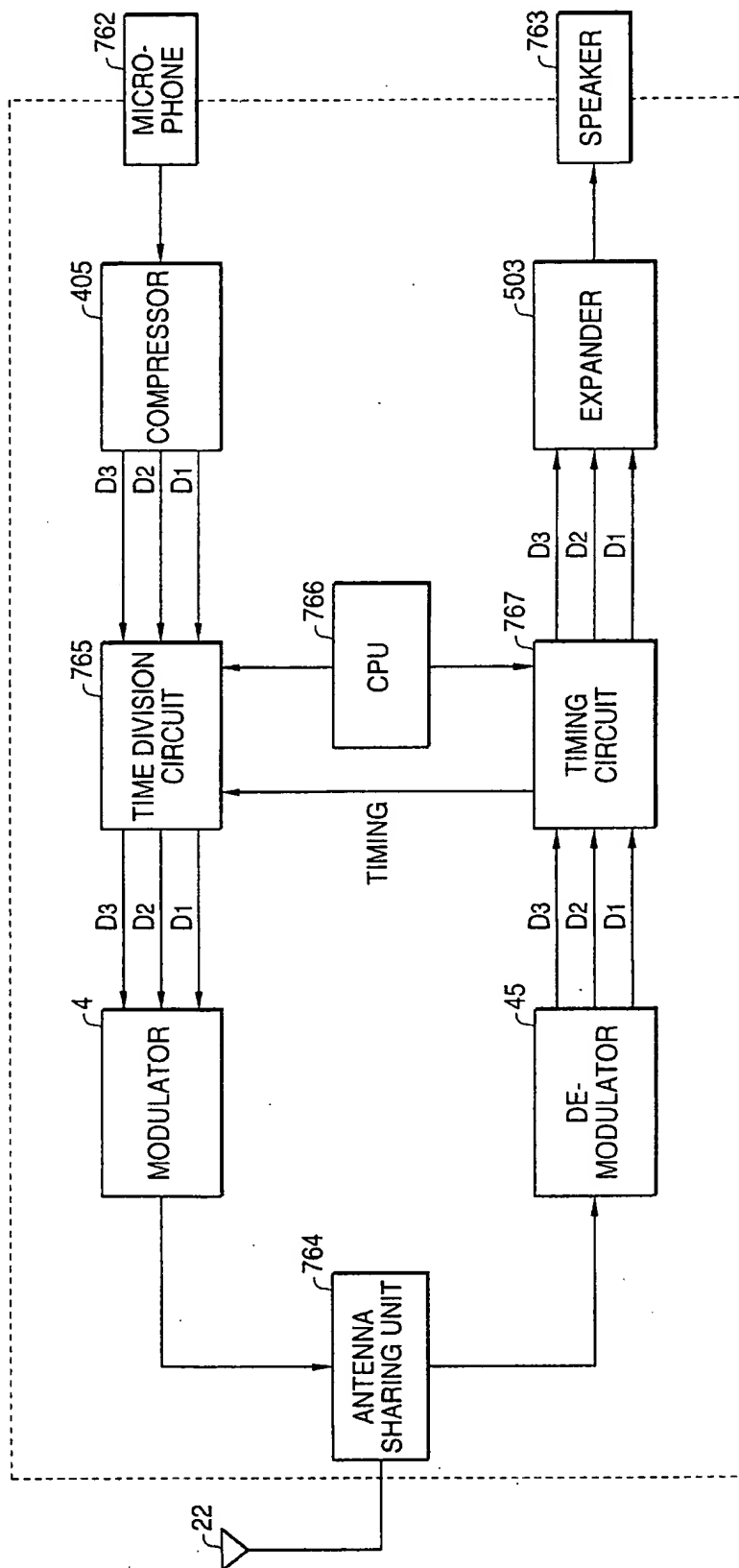


FIG. 116

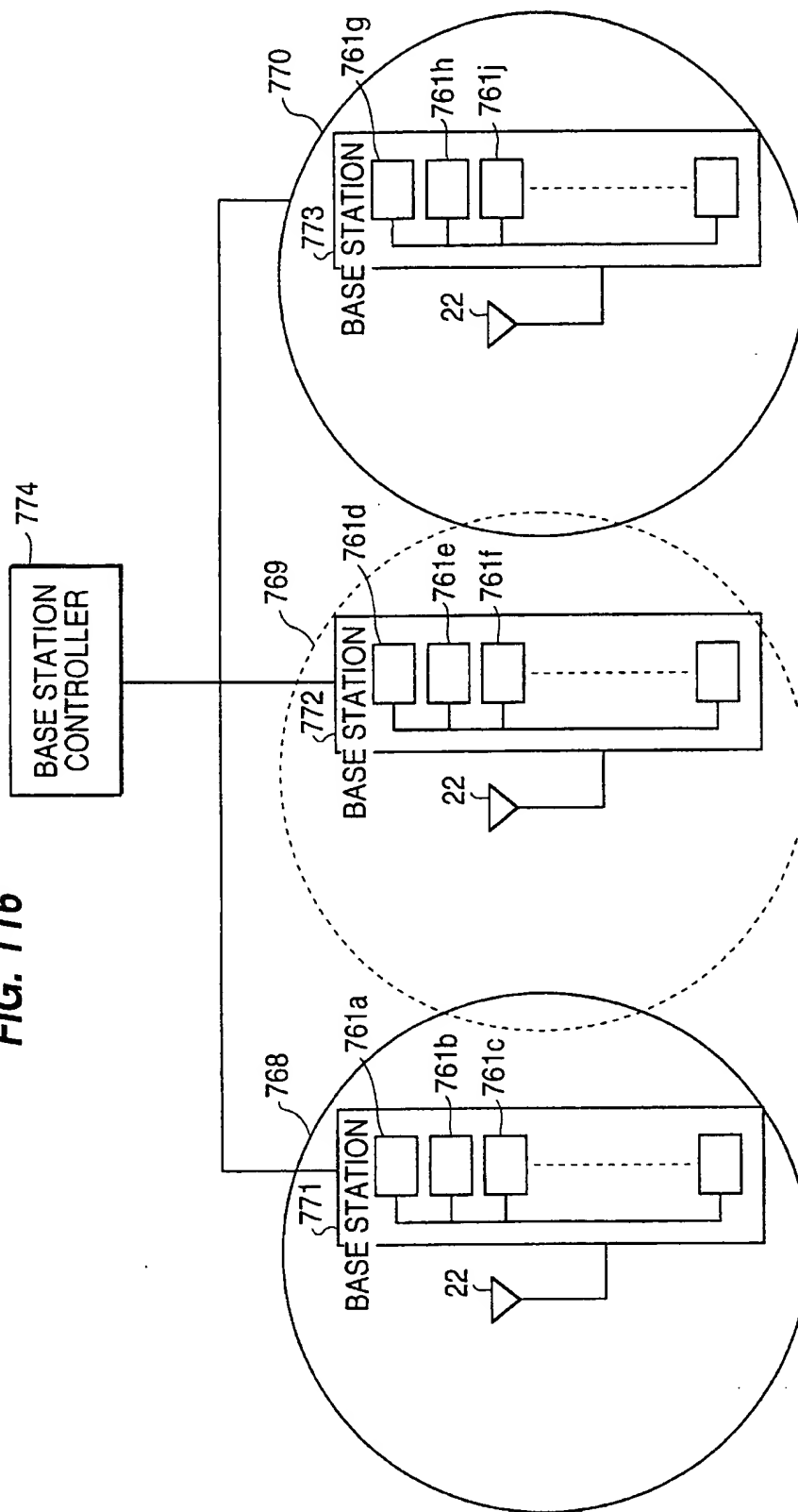
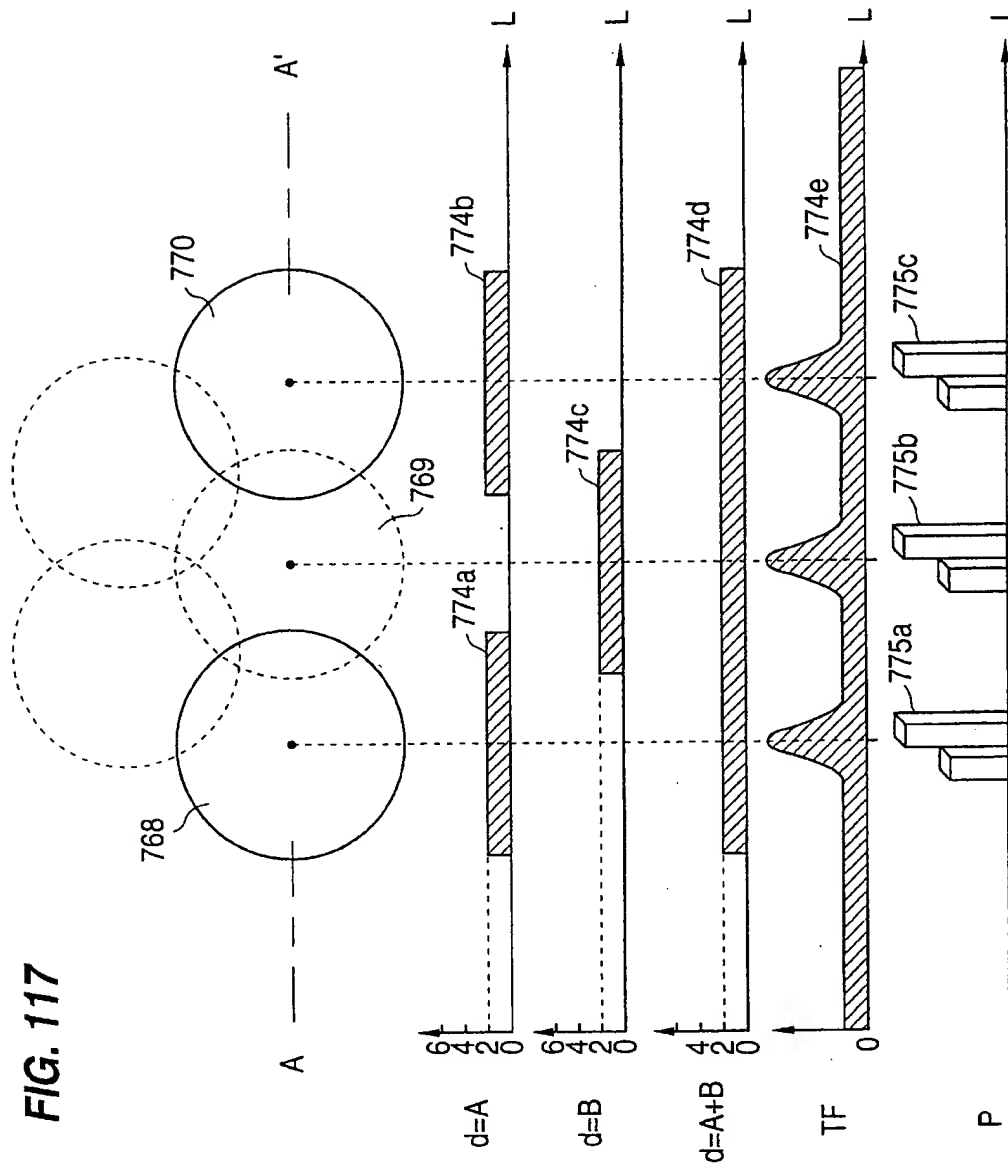
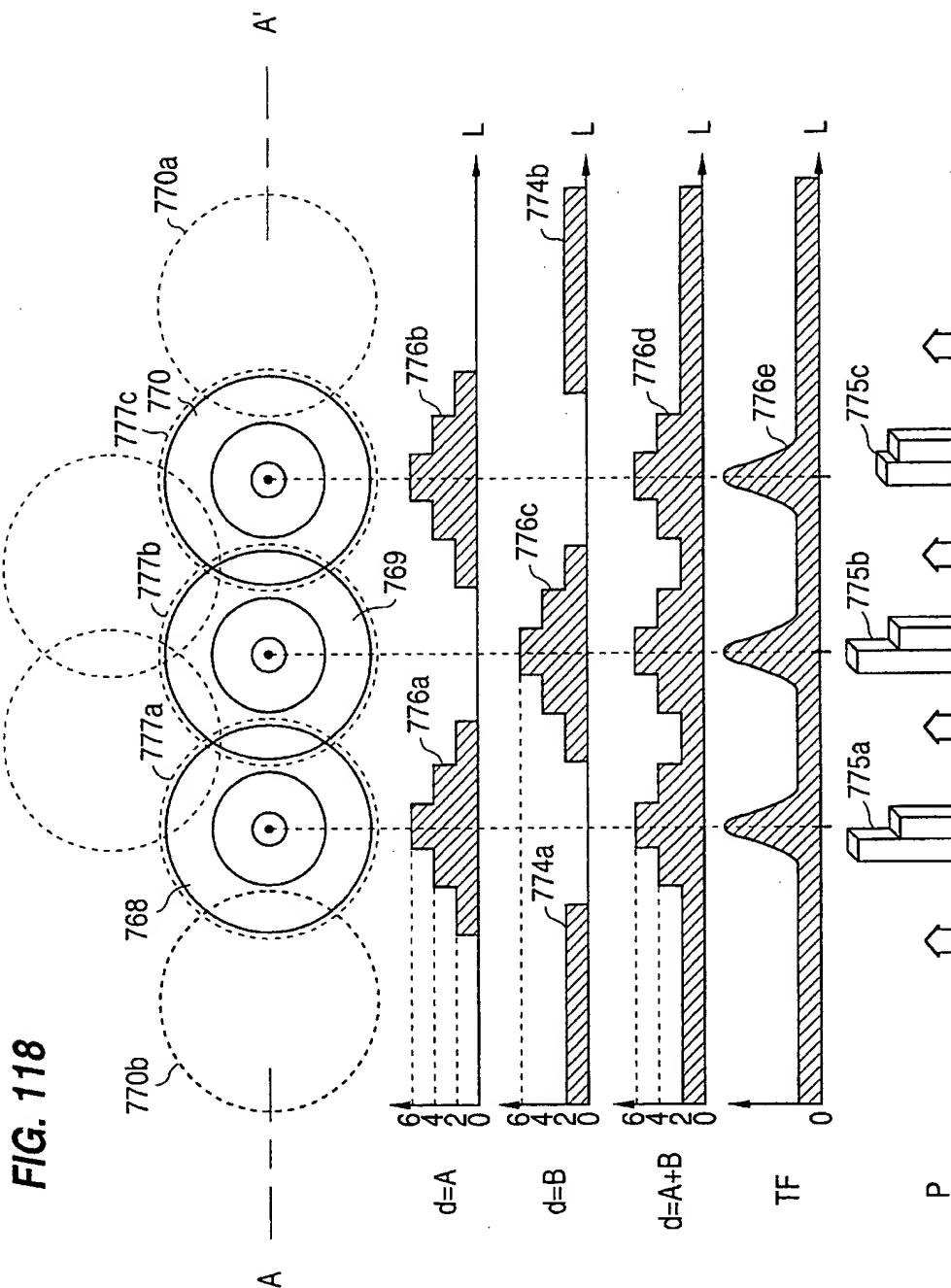


FIG. 117





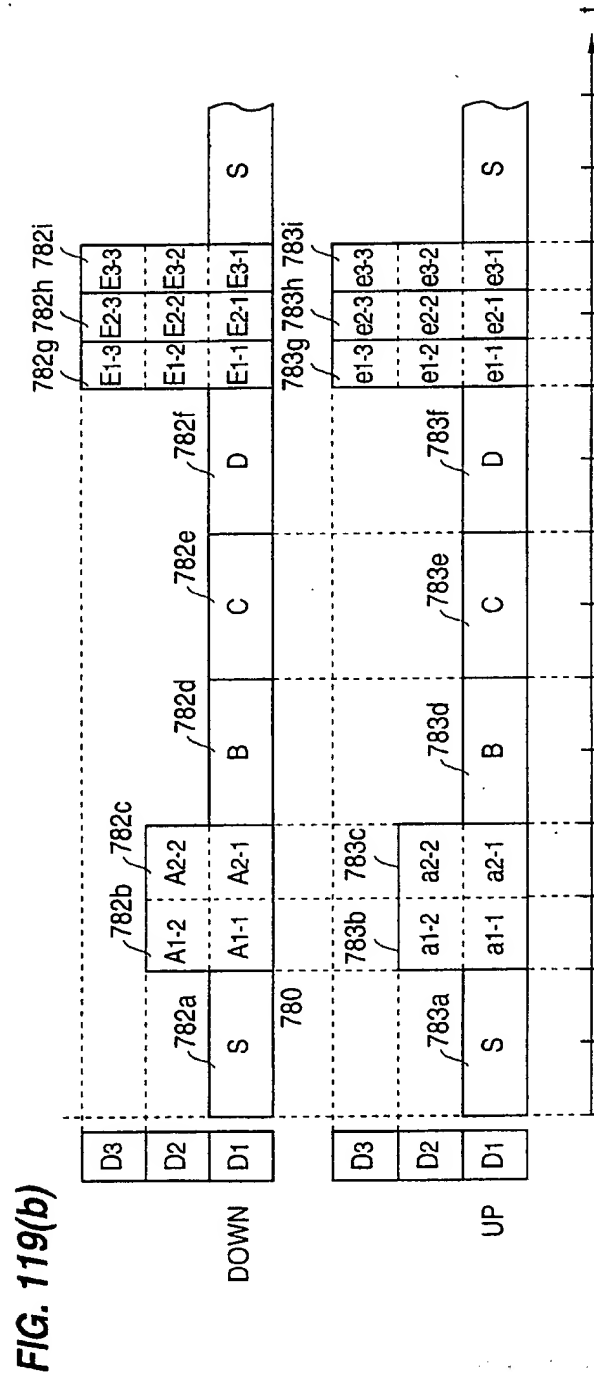
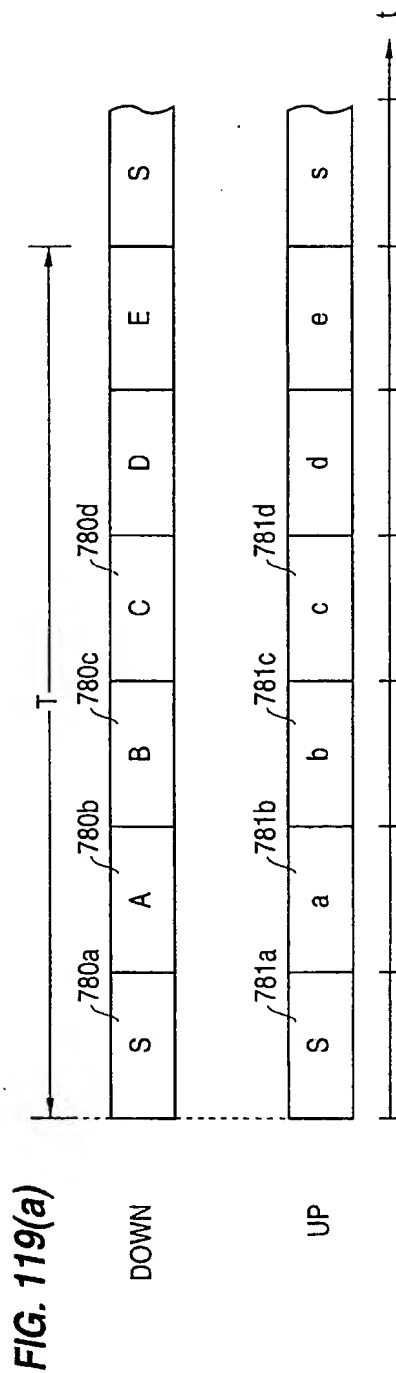


FIG. 120(a)

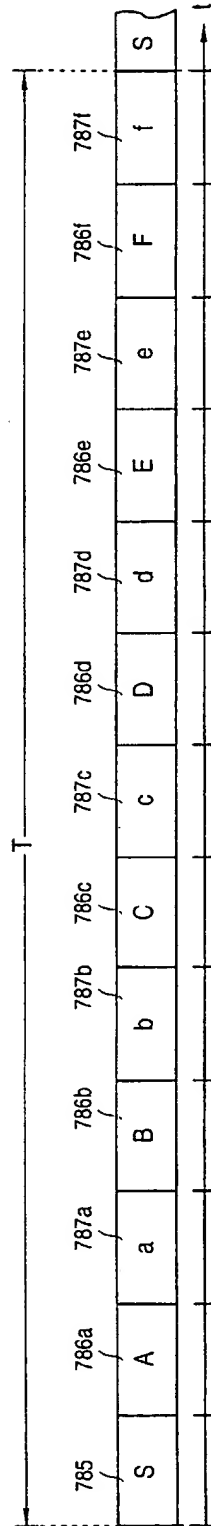


FIG. 120(b)

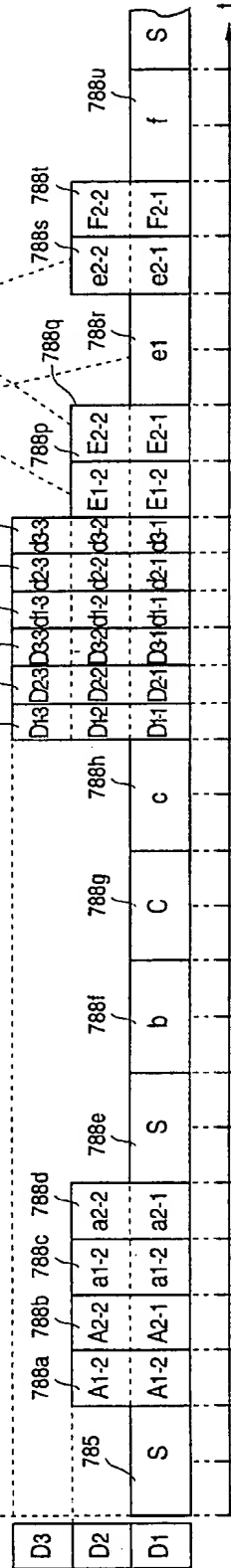


FIG. 121

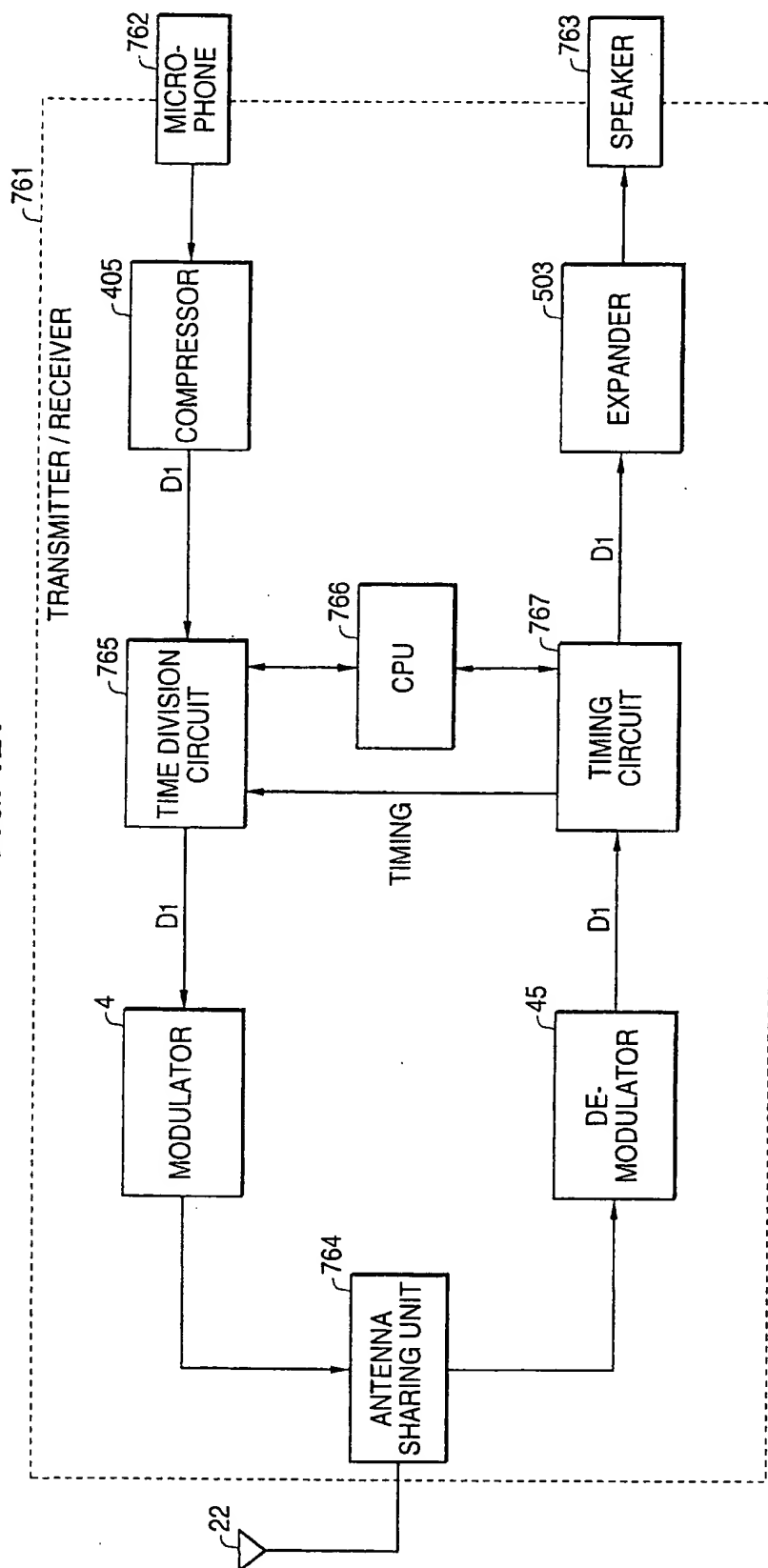
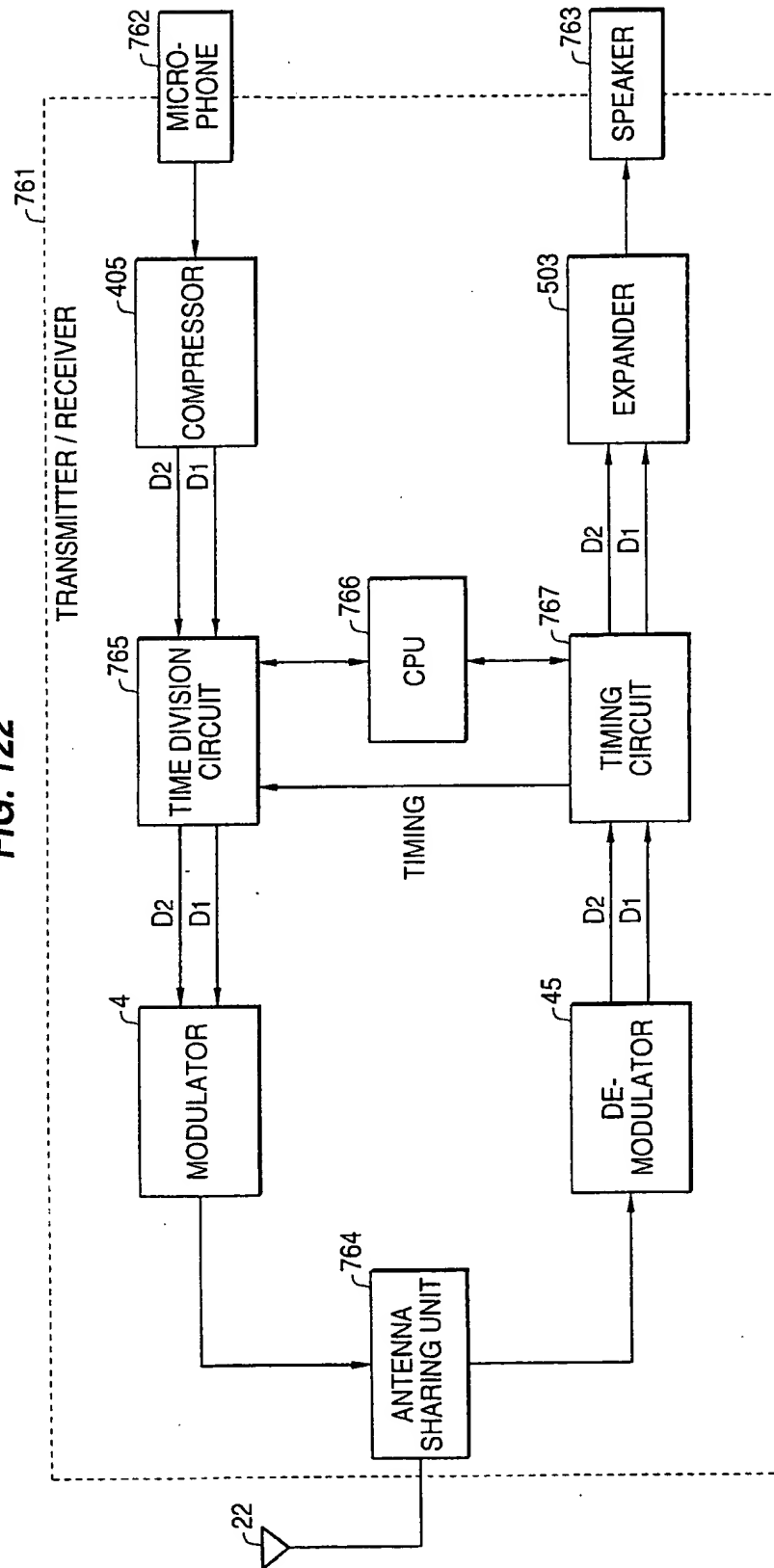
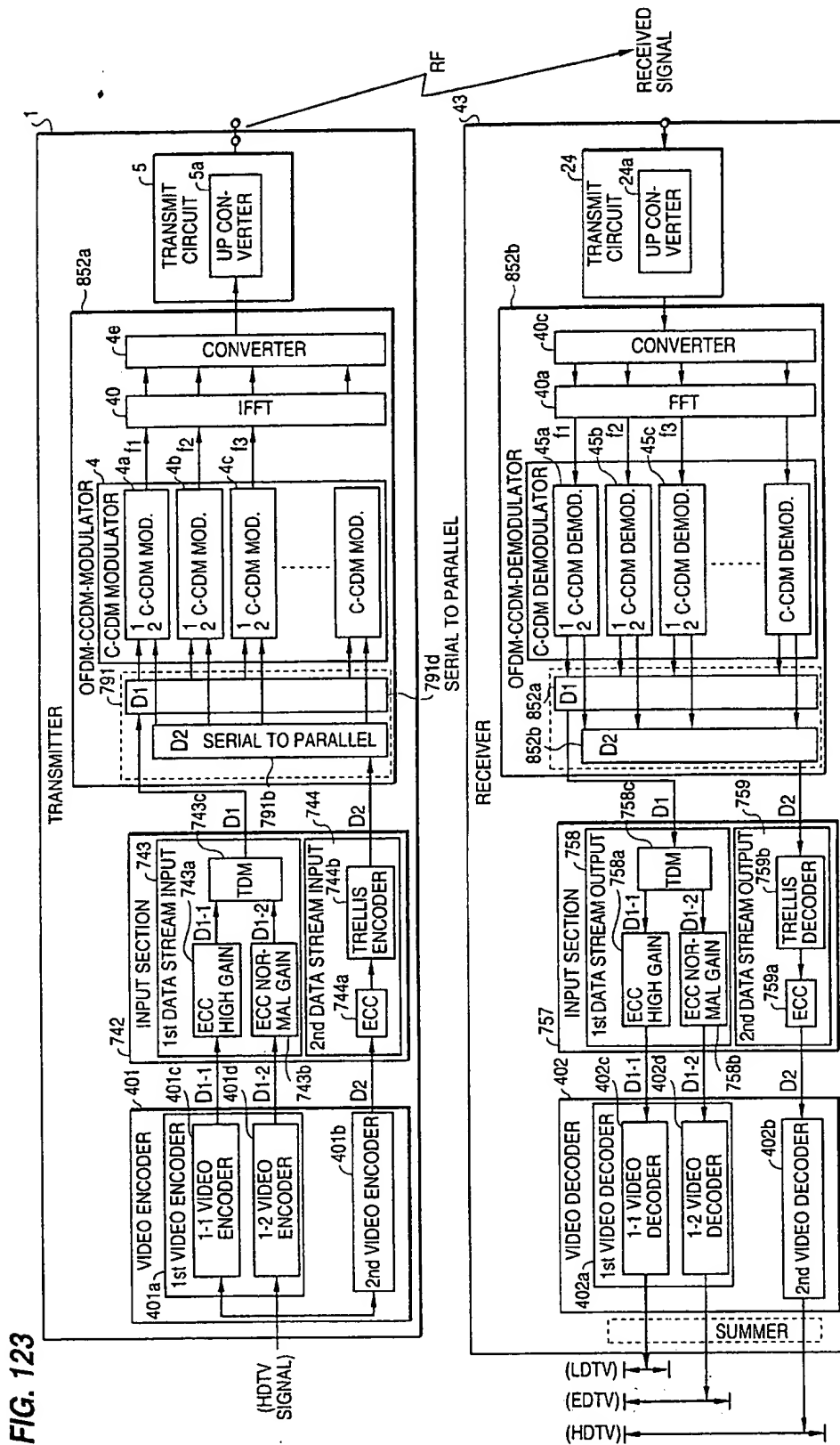


FIG. 122





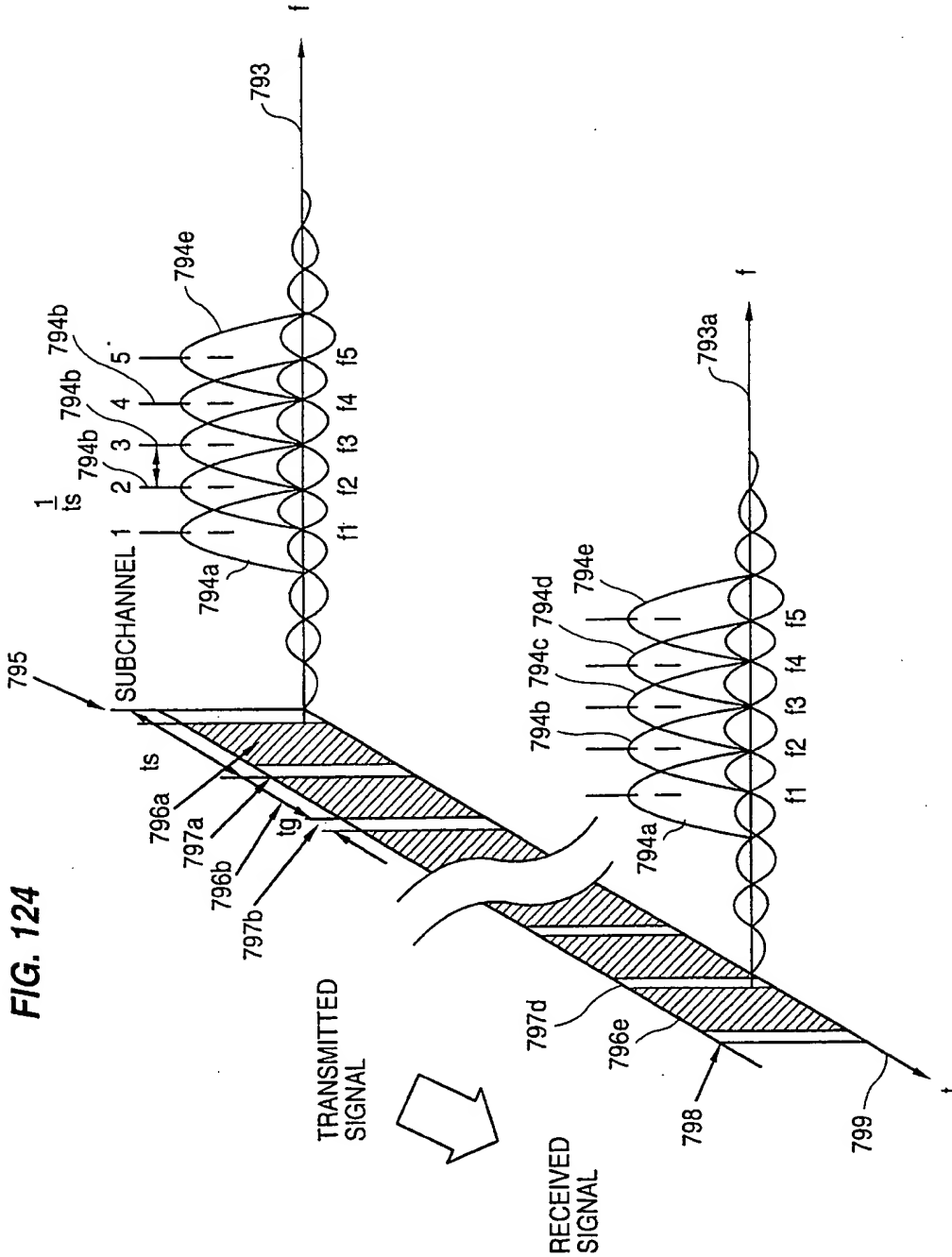


FIG. 125(a)

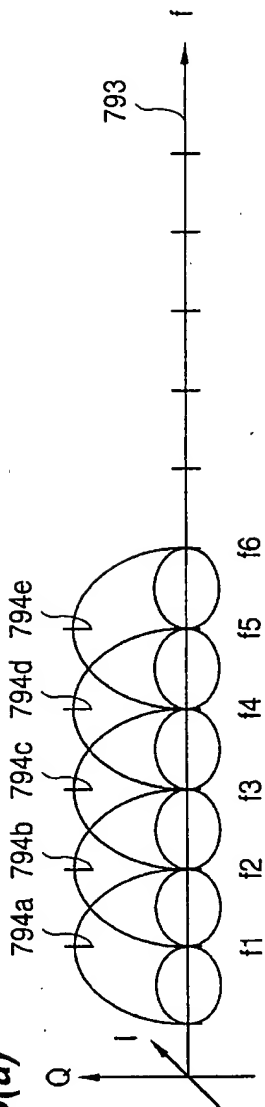
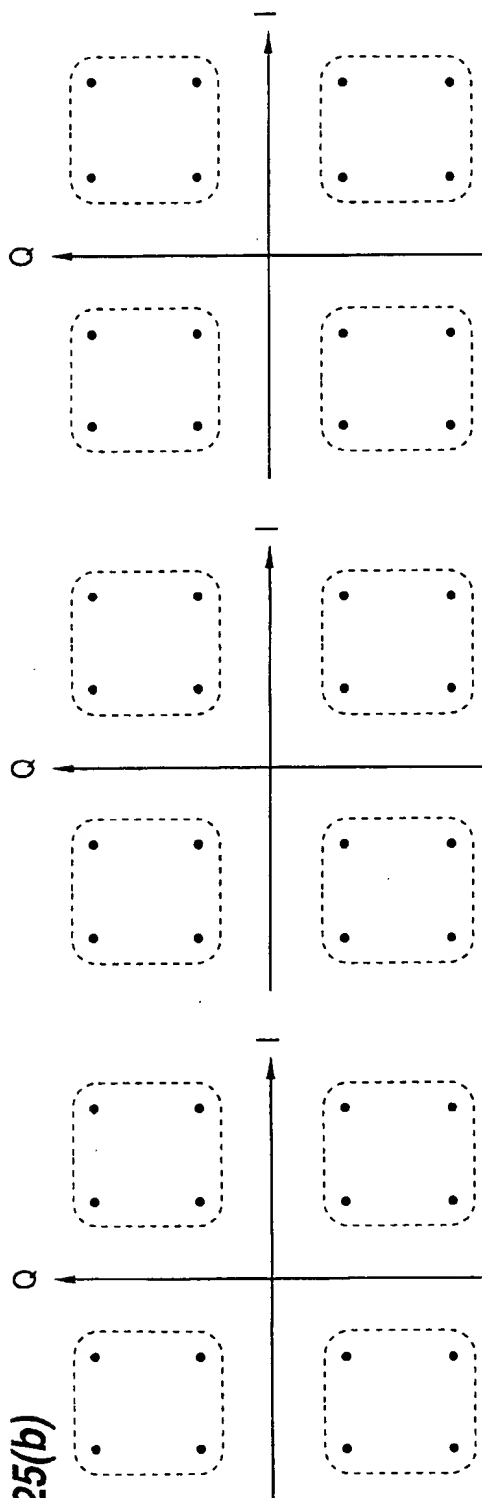
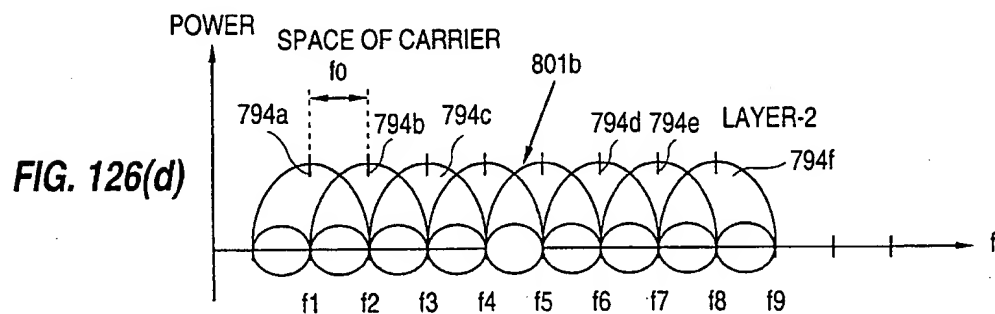
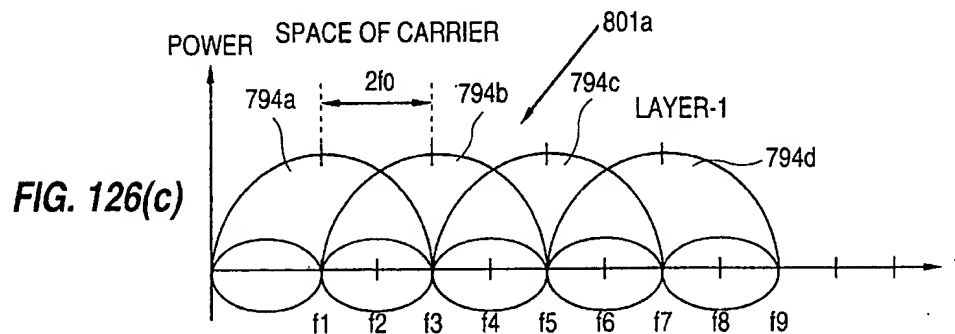
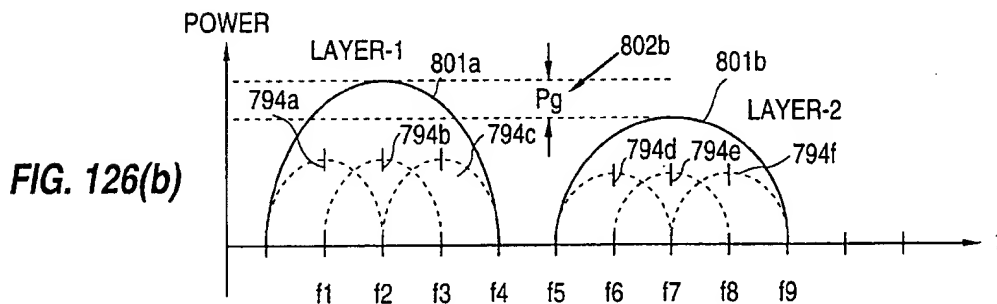
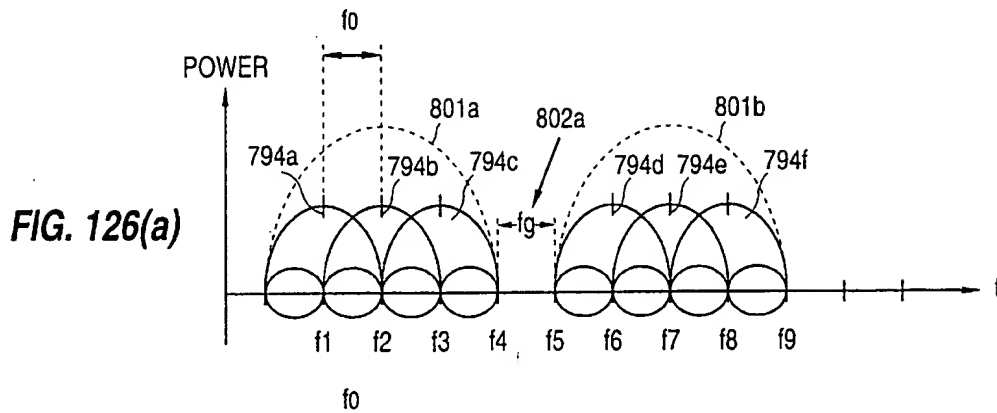


FIG. 125(b)





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FIG. 127

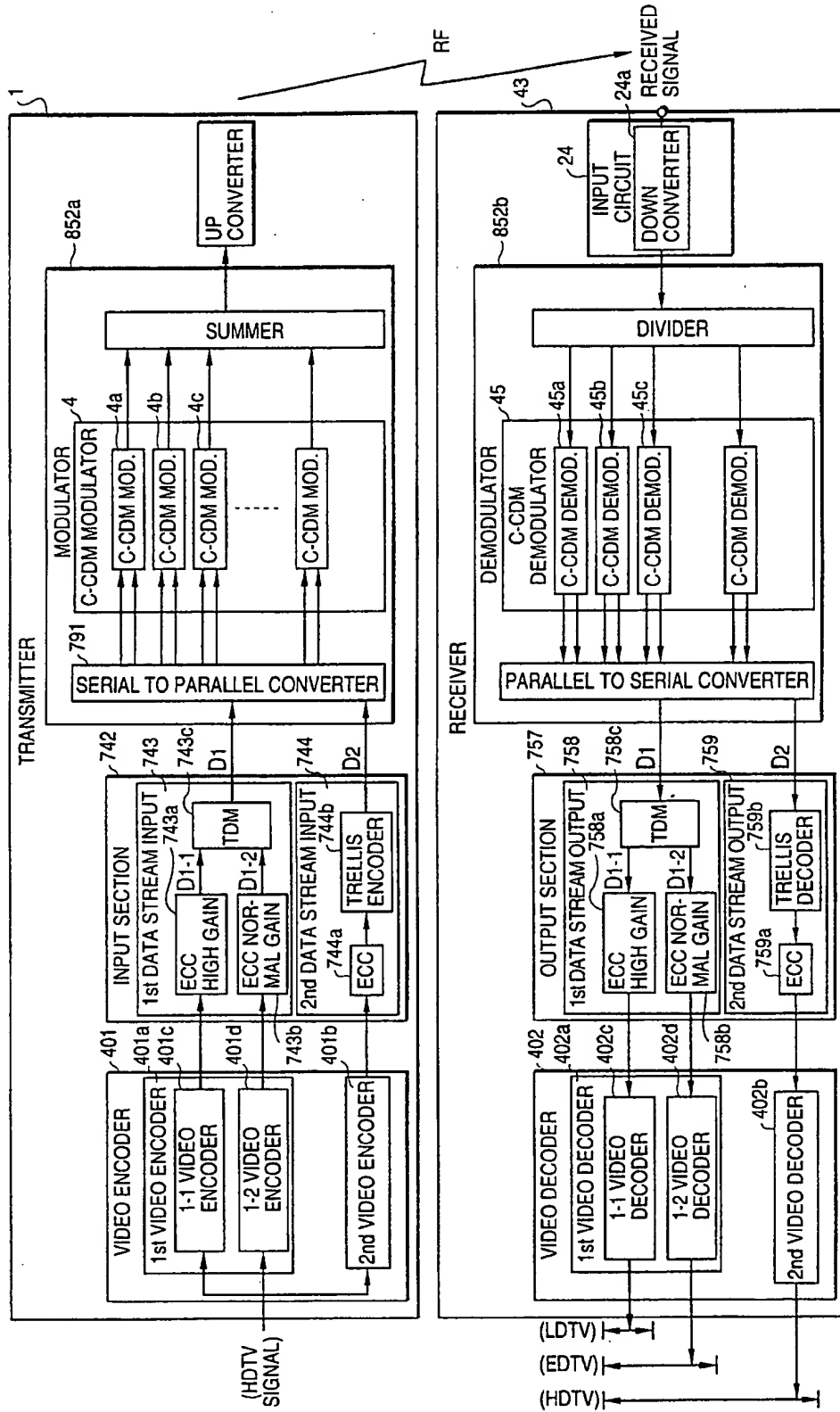


FIG. 128(a)

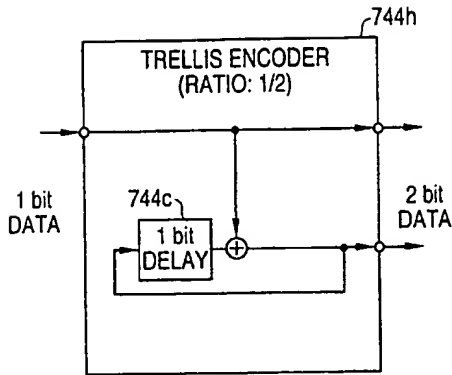


FIG. 128(d)

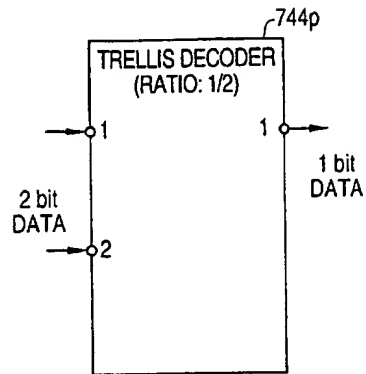


FIG. 128(b)

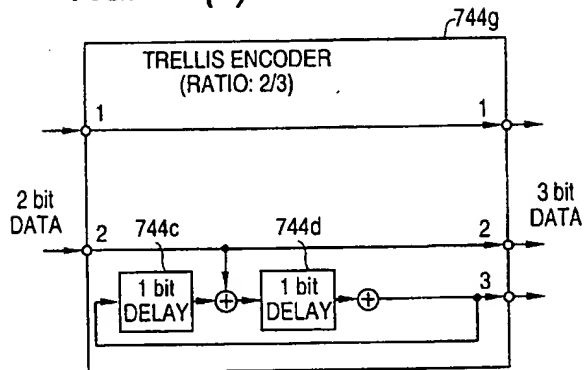


FIG. 128(e)

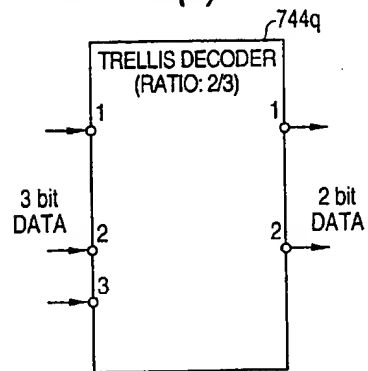


FIG. 128(c)

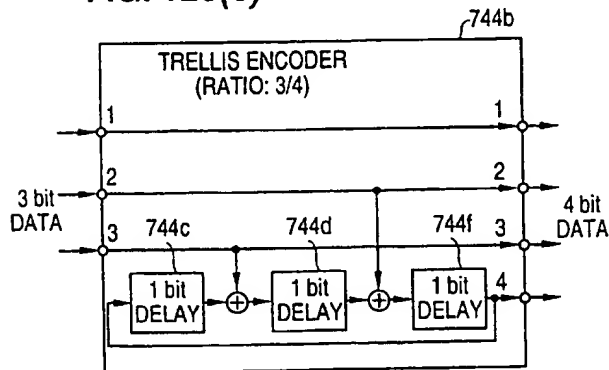
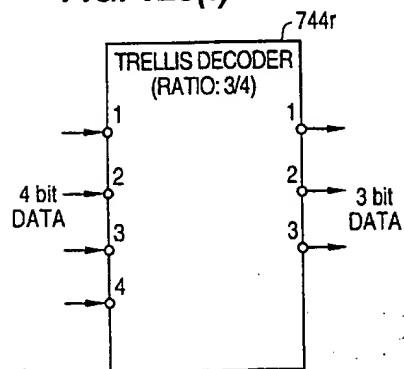


FIG. 128(f)



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FIG. 129

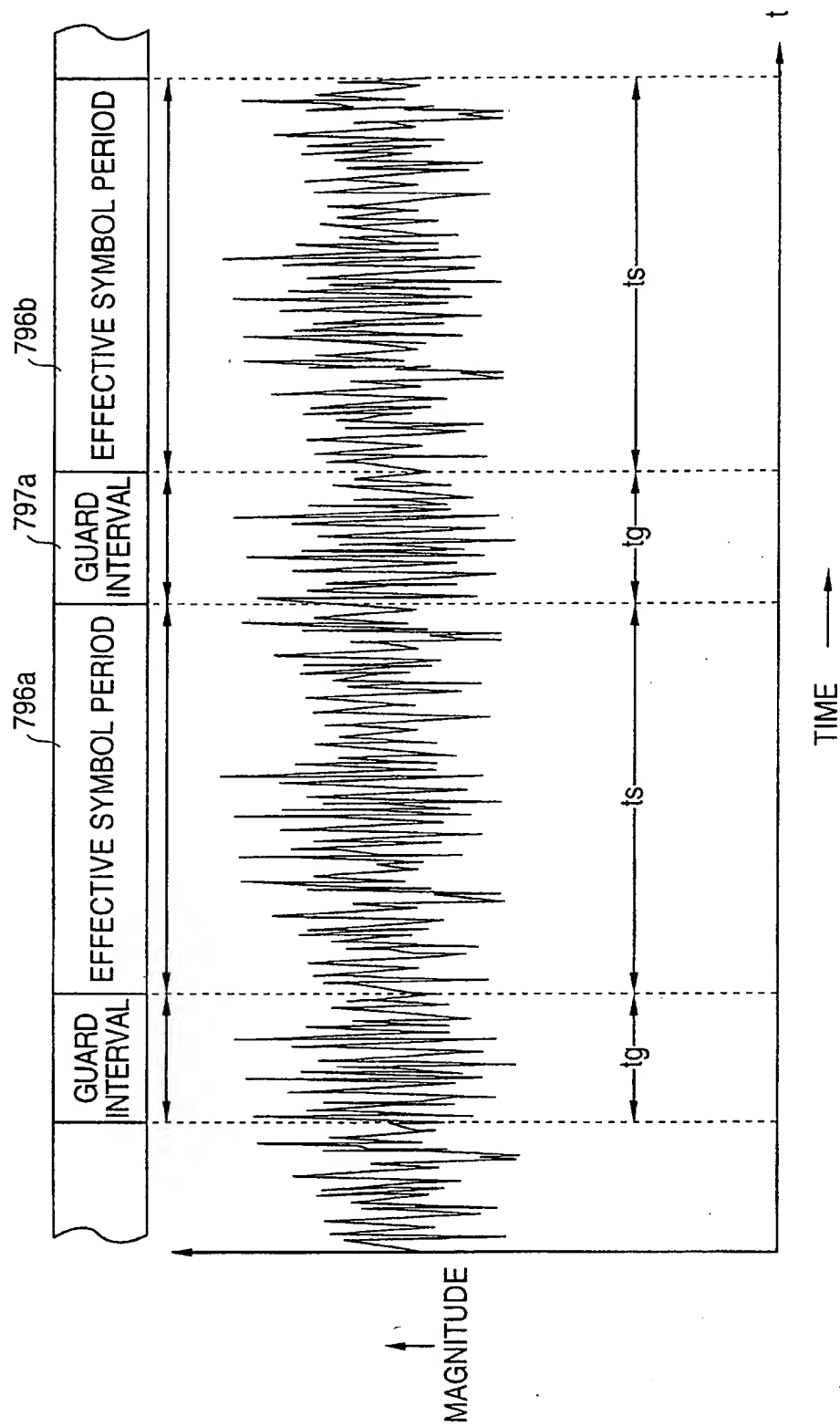


FIG. 130

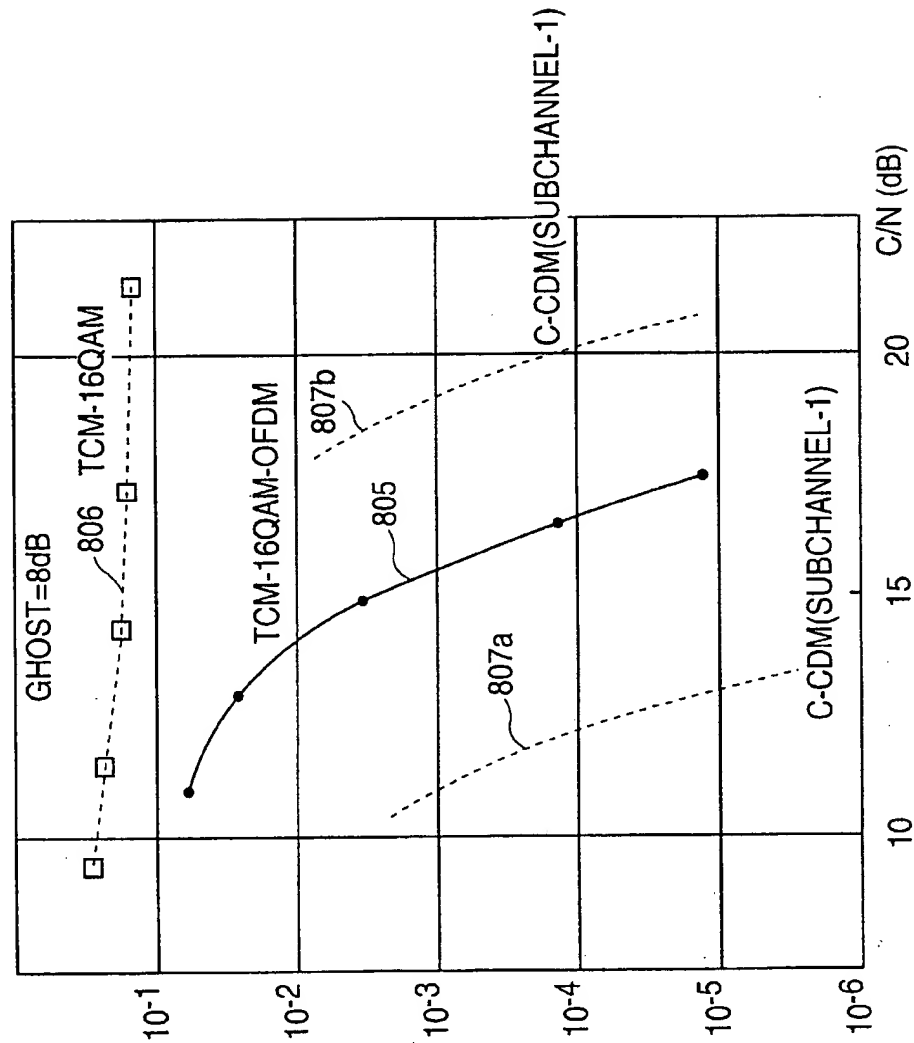


FIG. 131

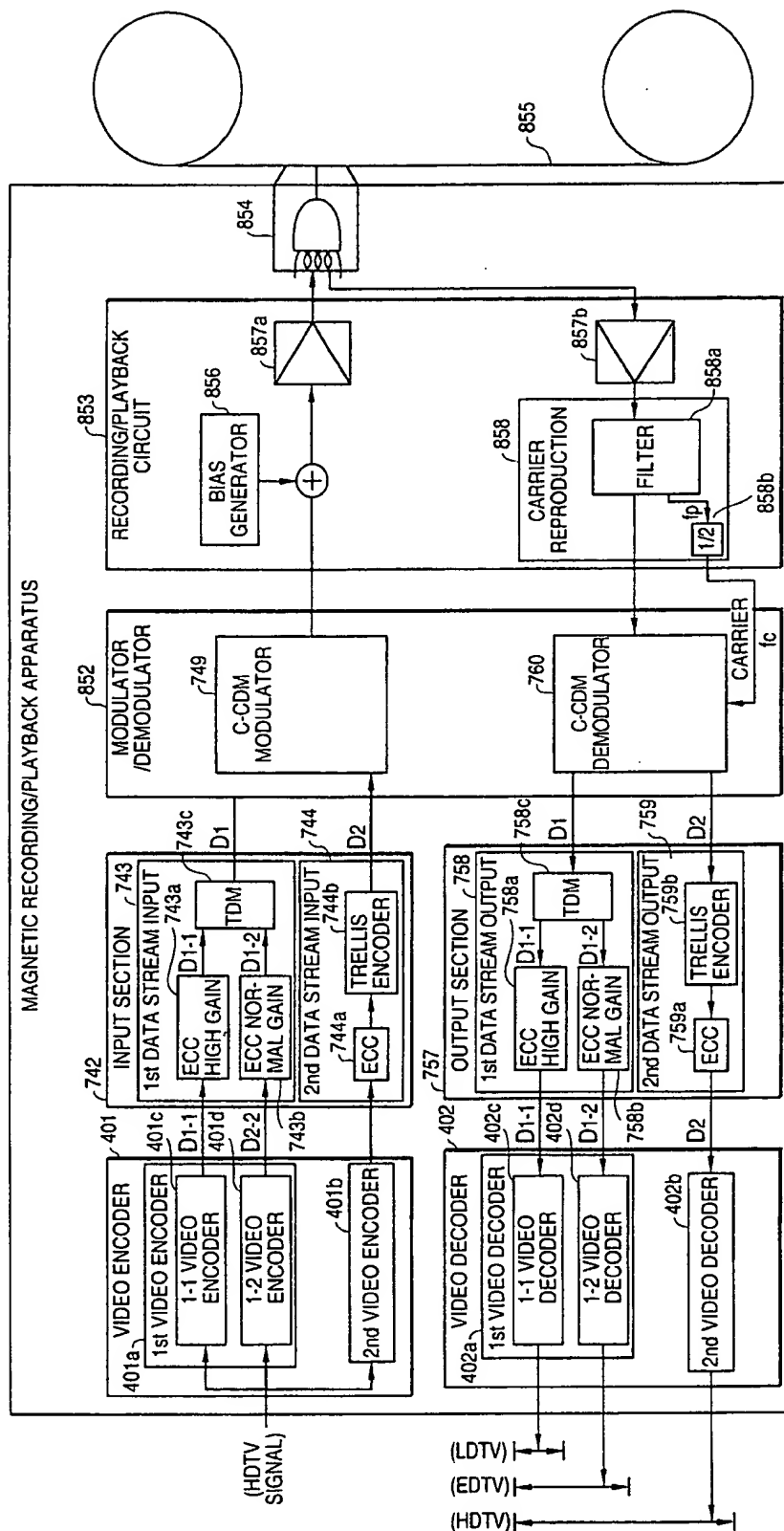


FIG. 133

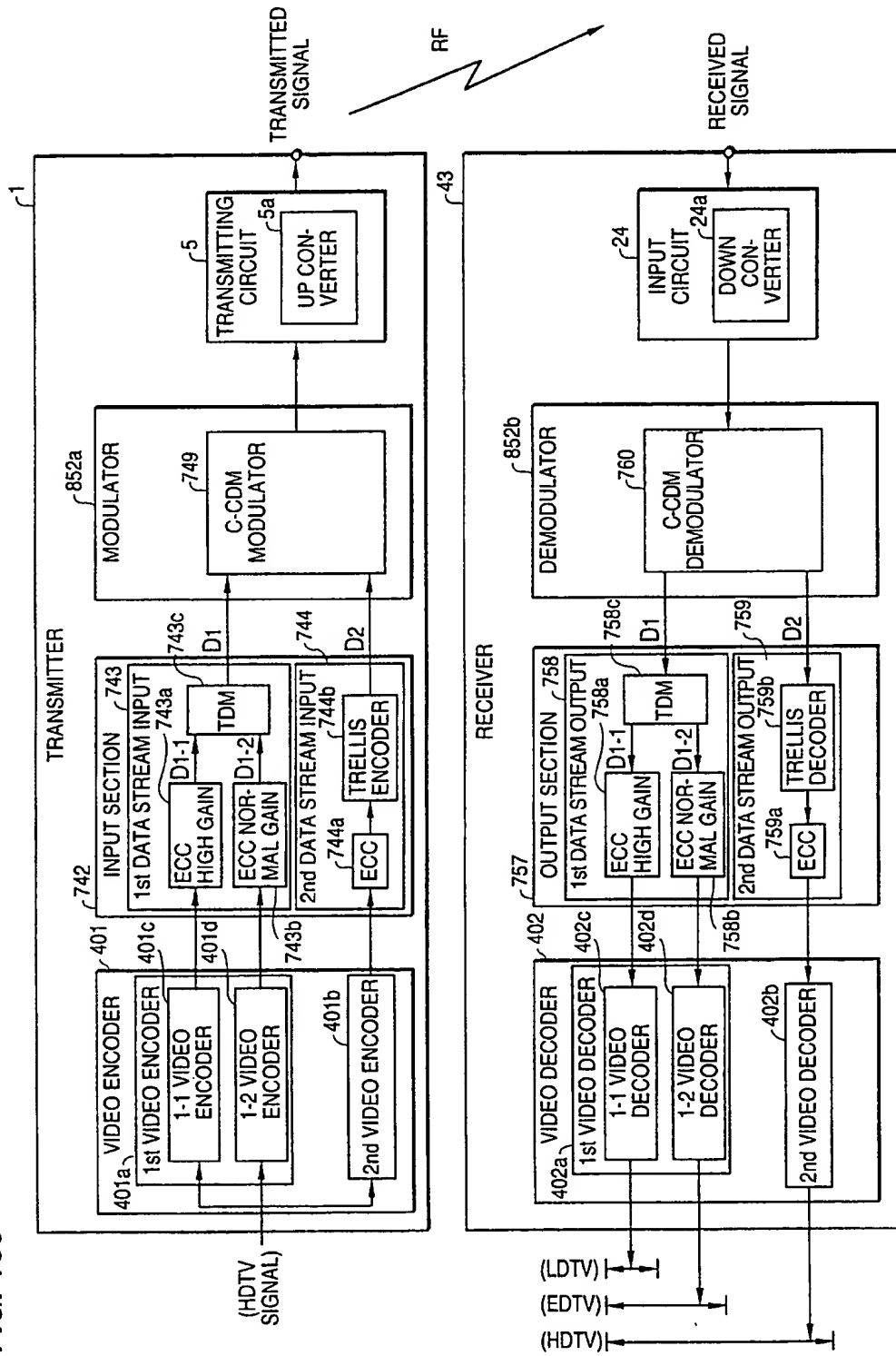


FIG. 134

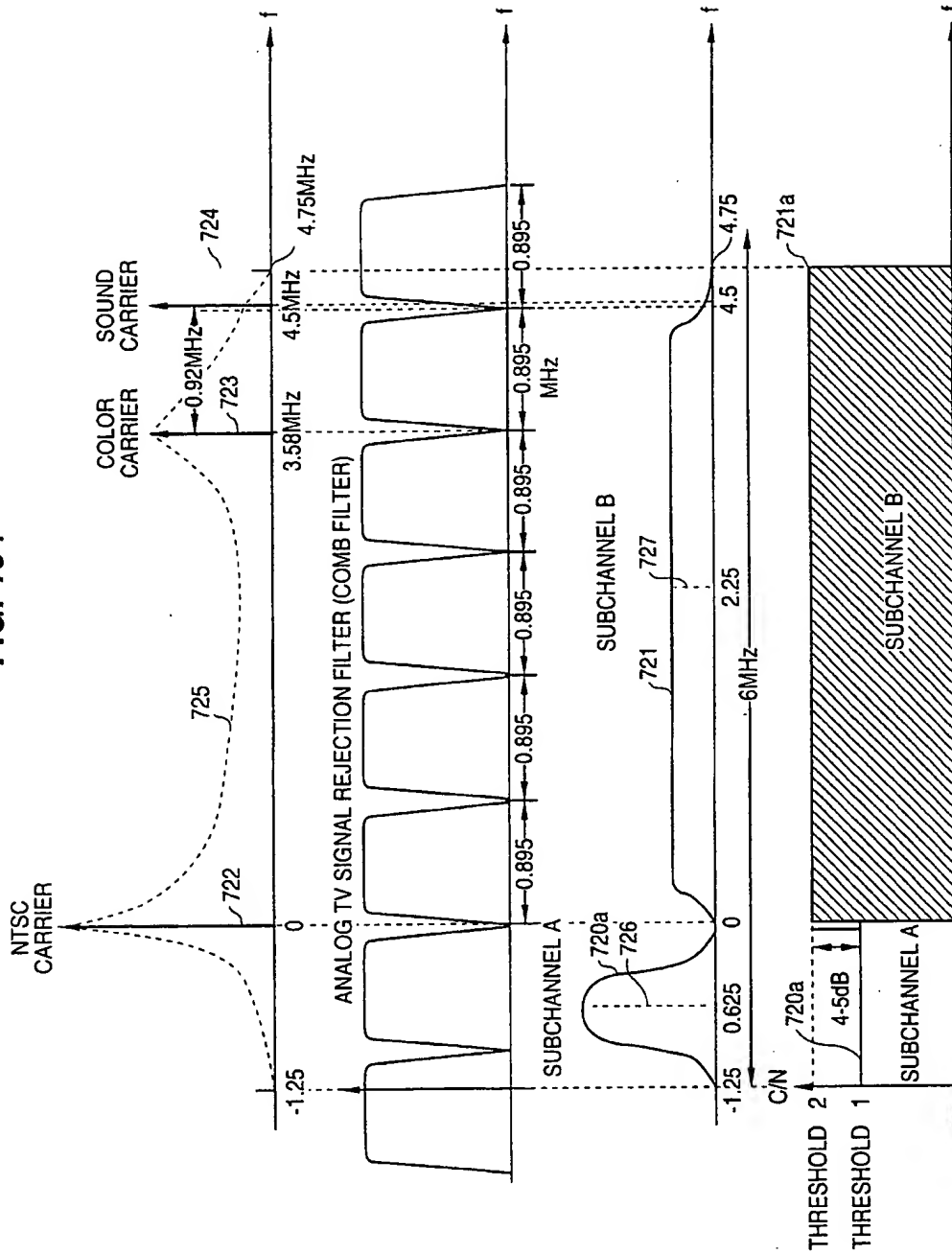


FIG. 135

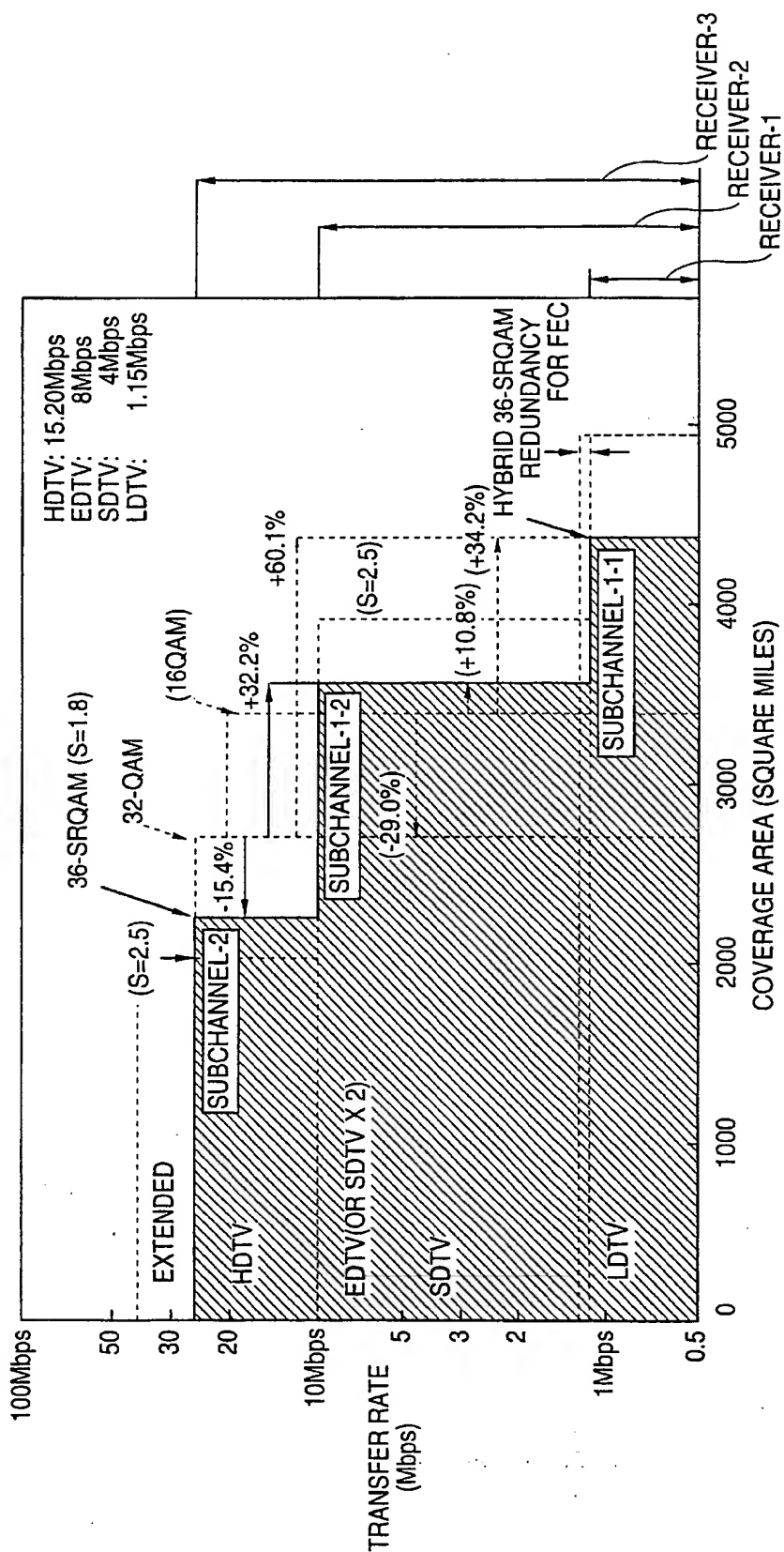


FIG. 136

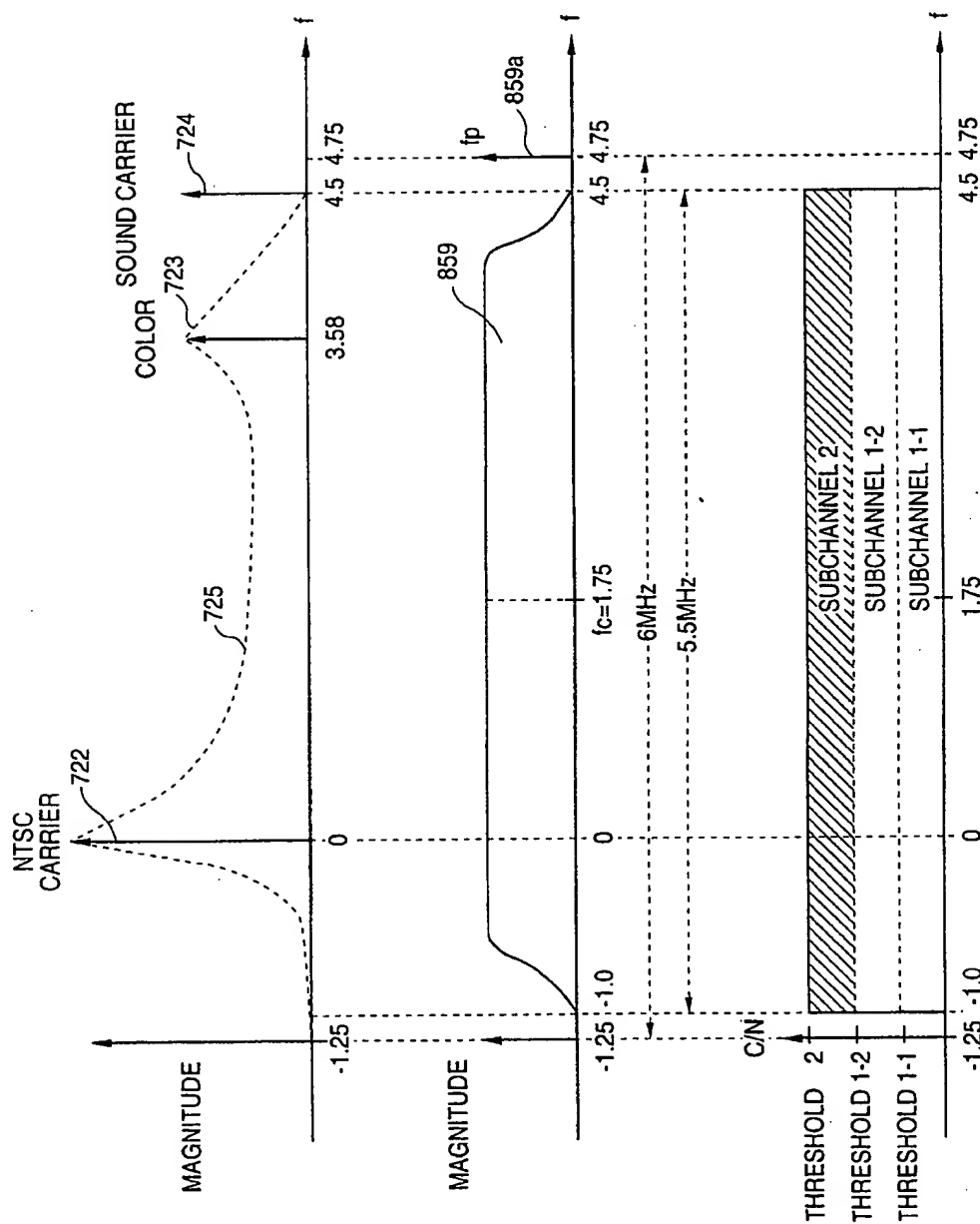
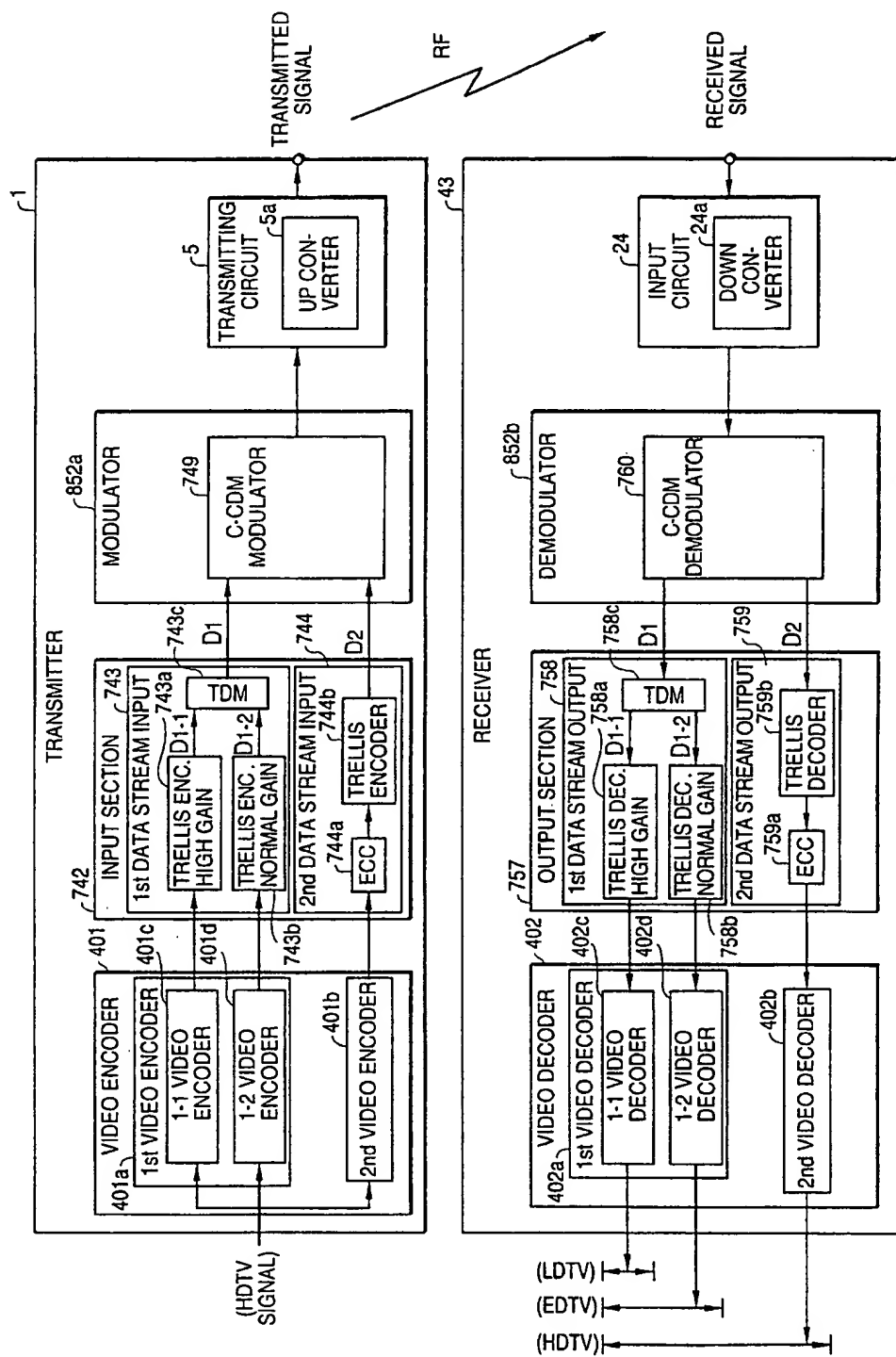


FIG. 137



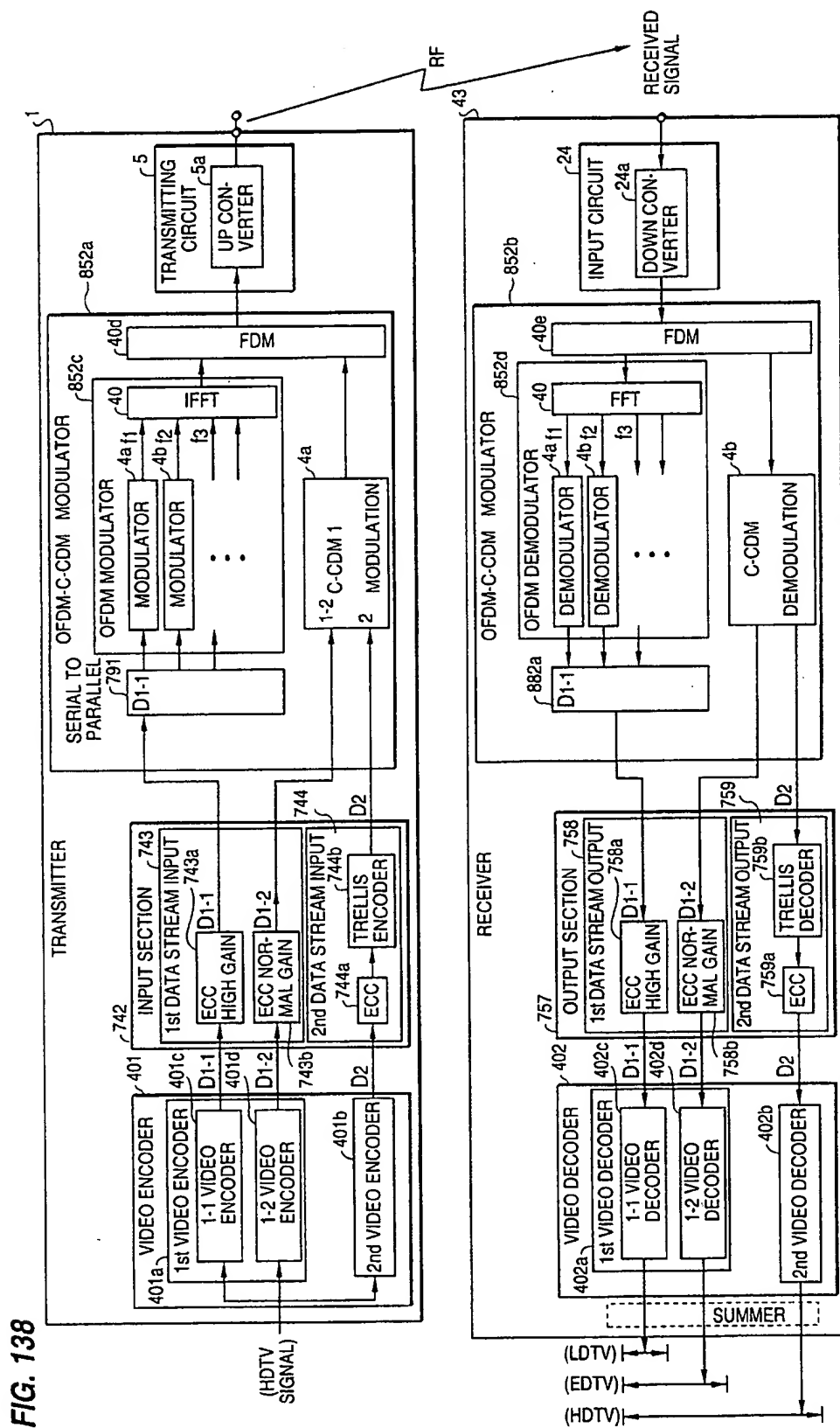
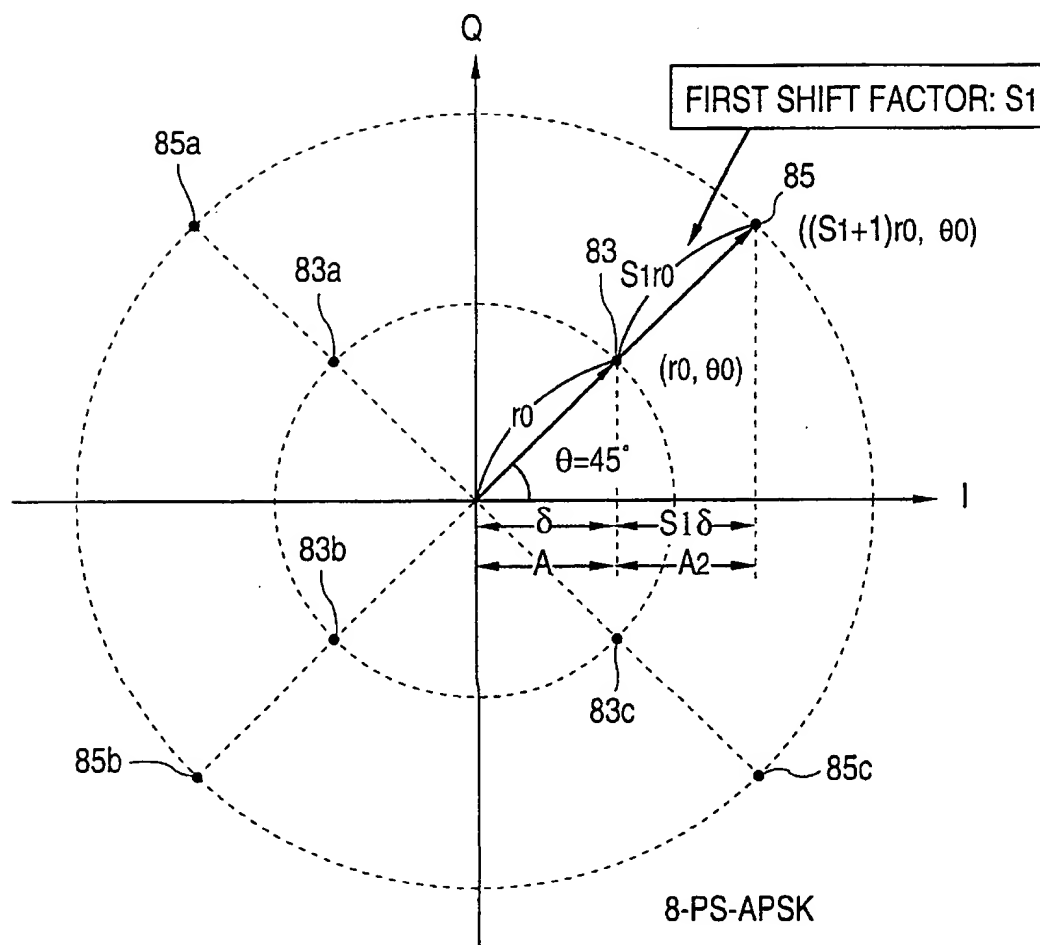
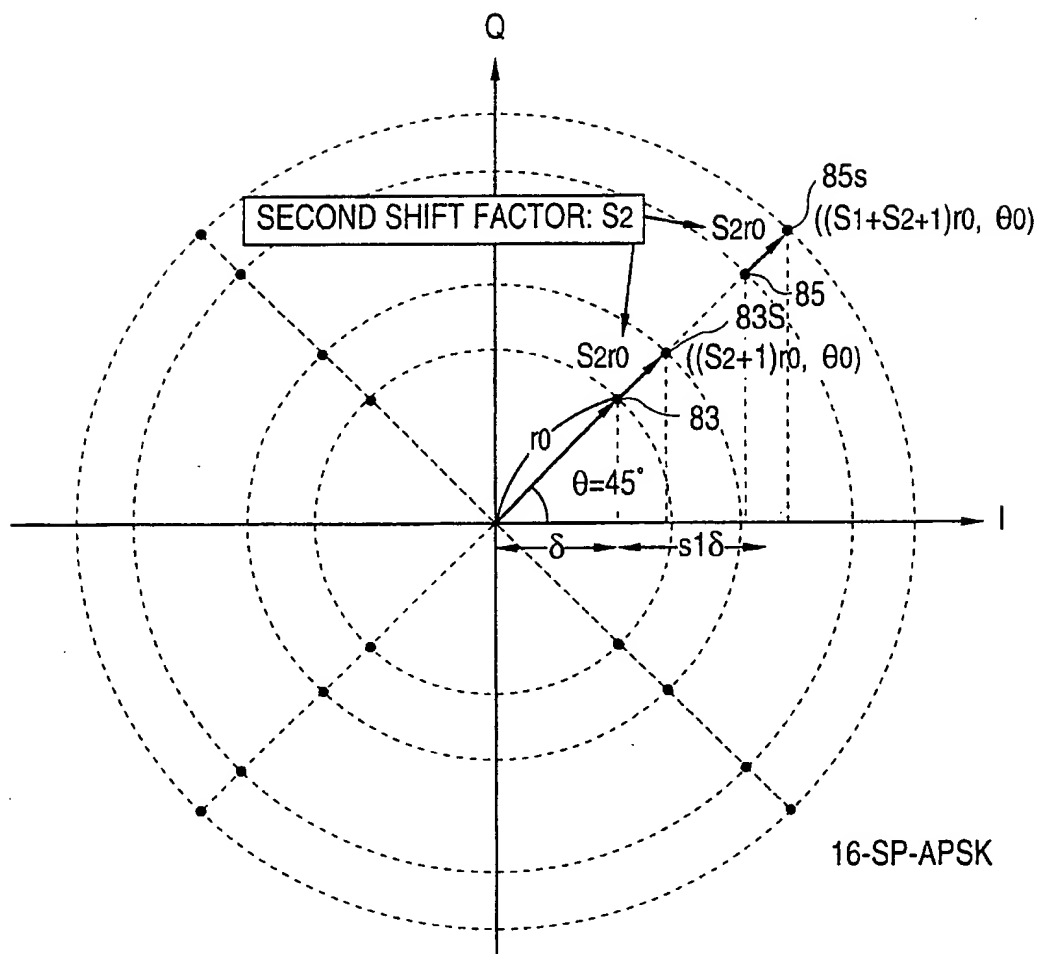


FIG. 139



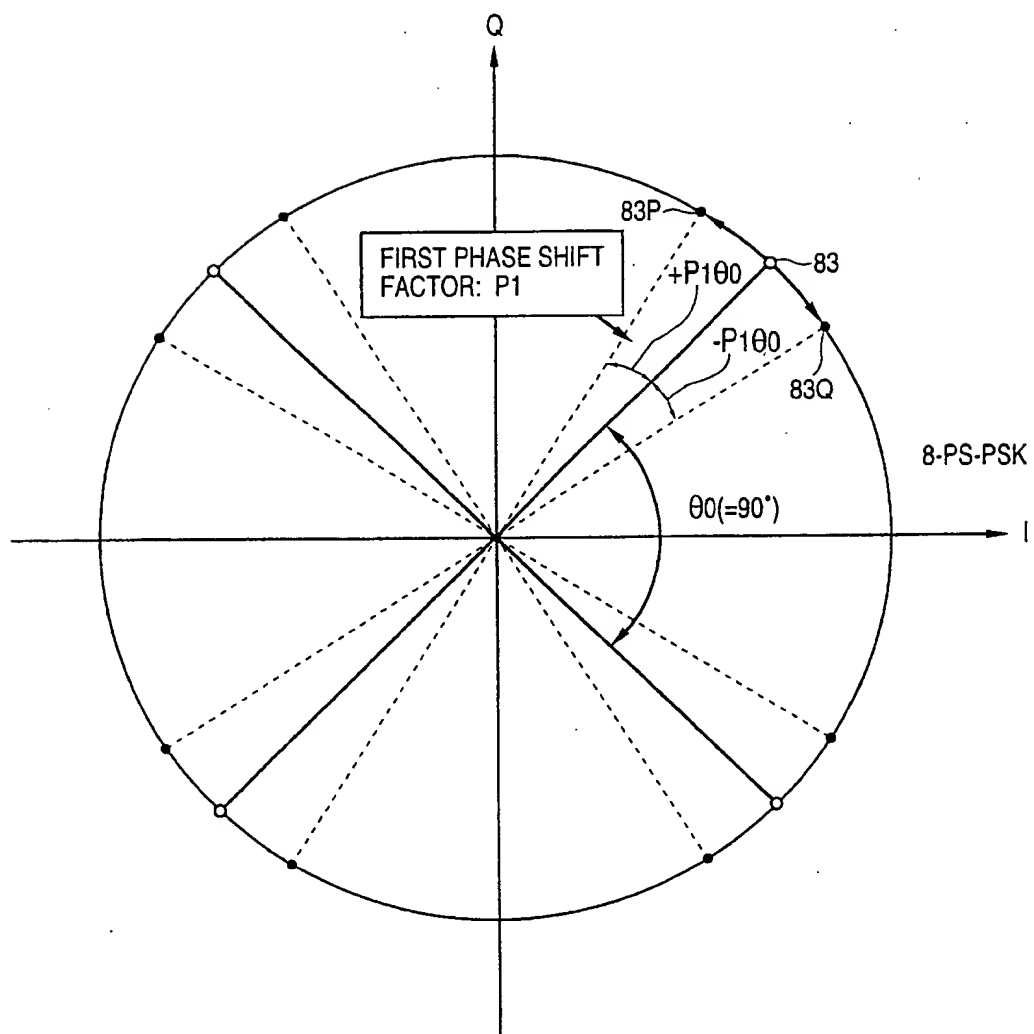
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FIG. 140



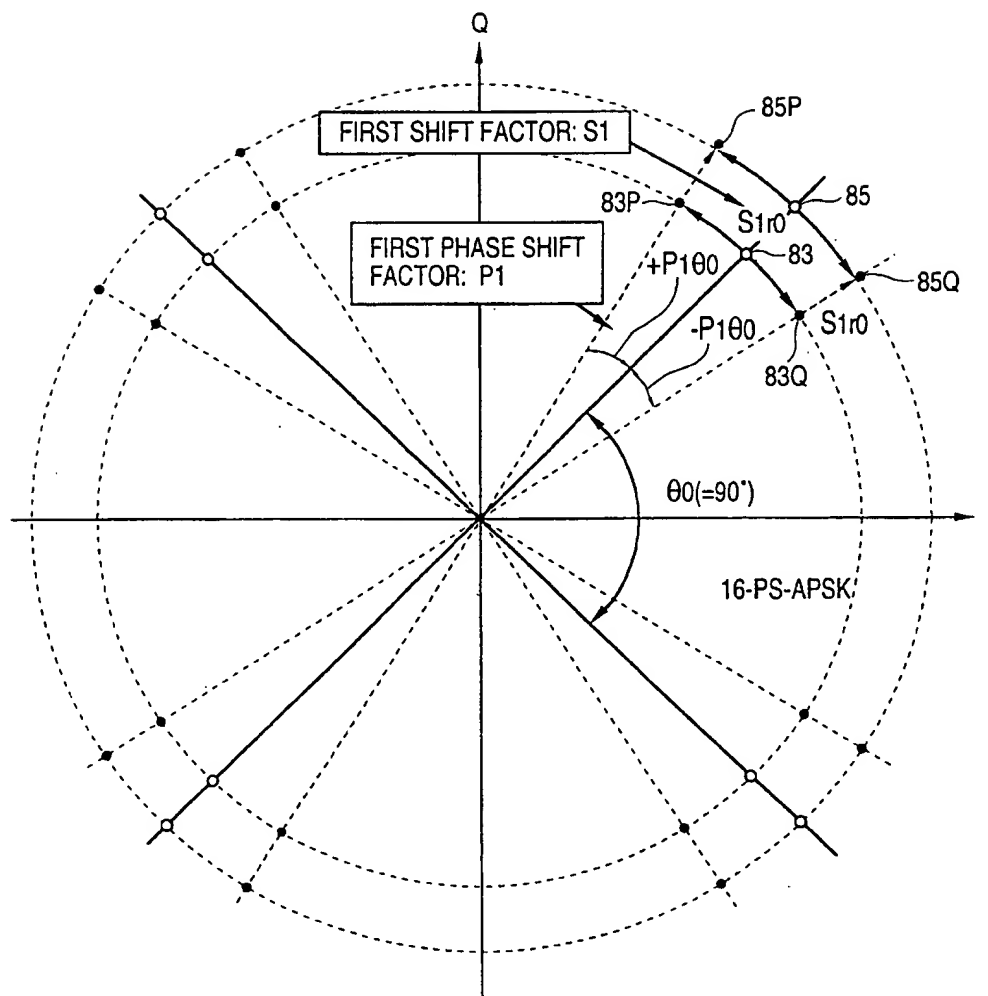
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FIG. 141



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FIG. 142



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FIG. 143

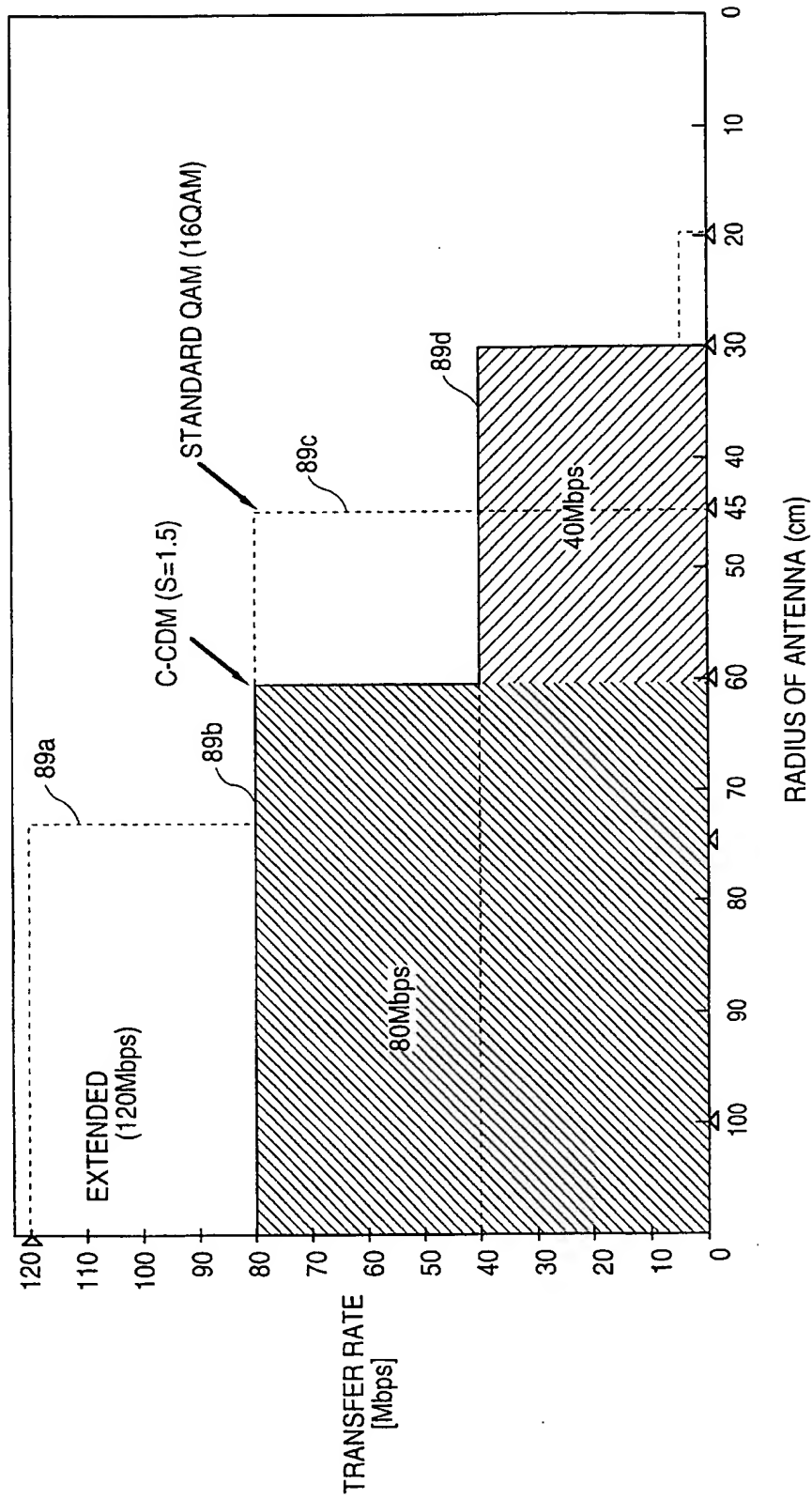


FIG. 144

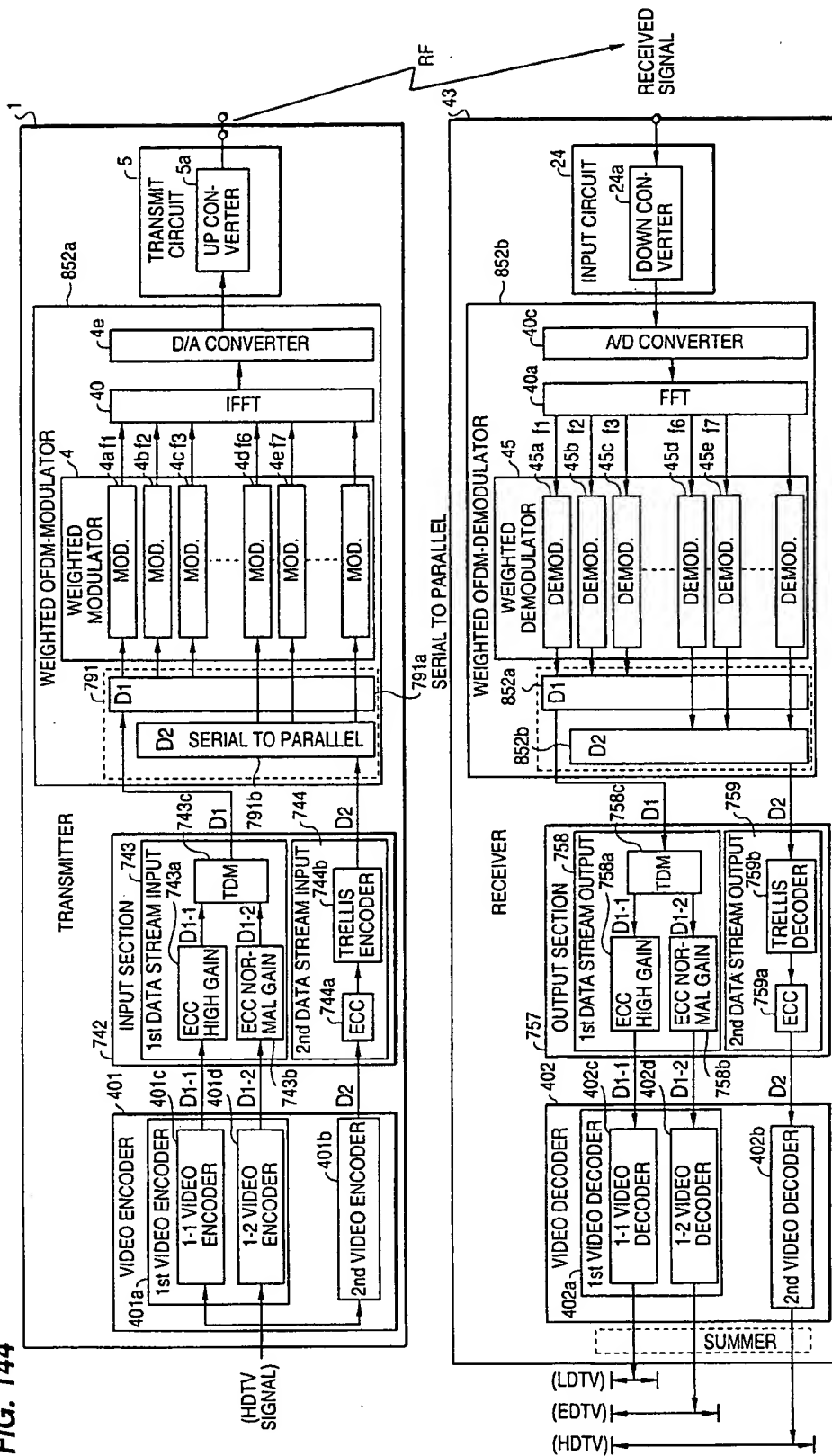


FIG. 145(a)

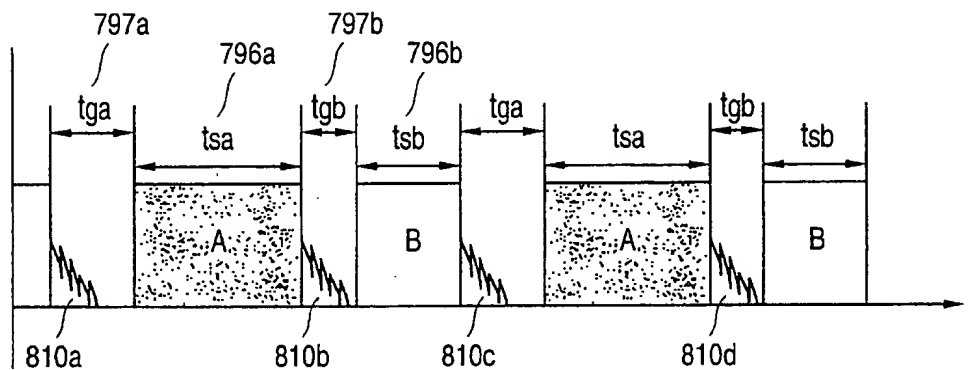
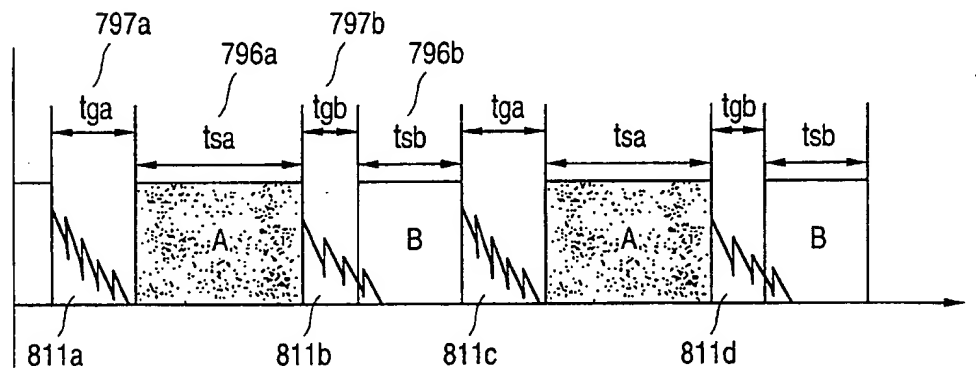


FIG. 145(b)



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FIG. 147

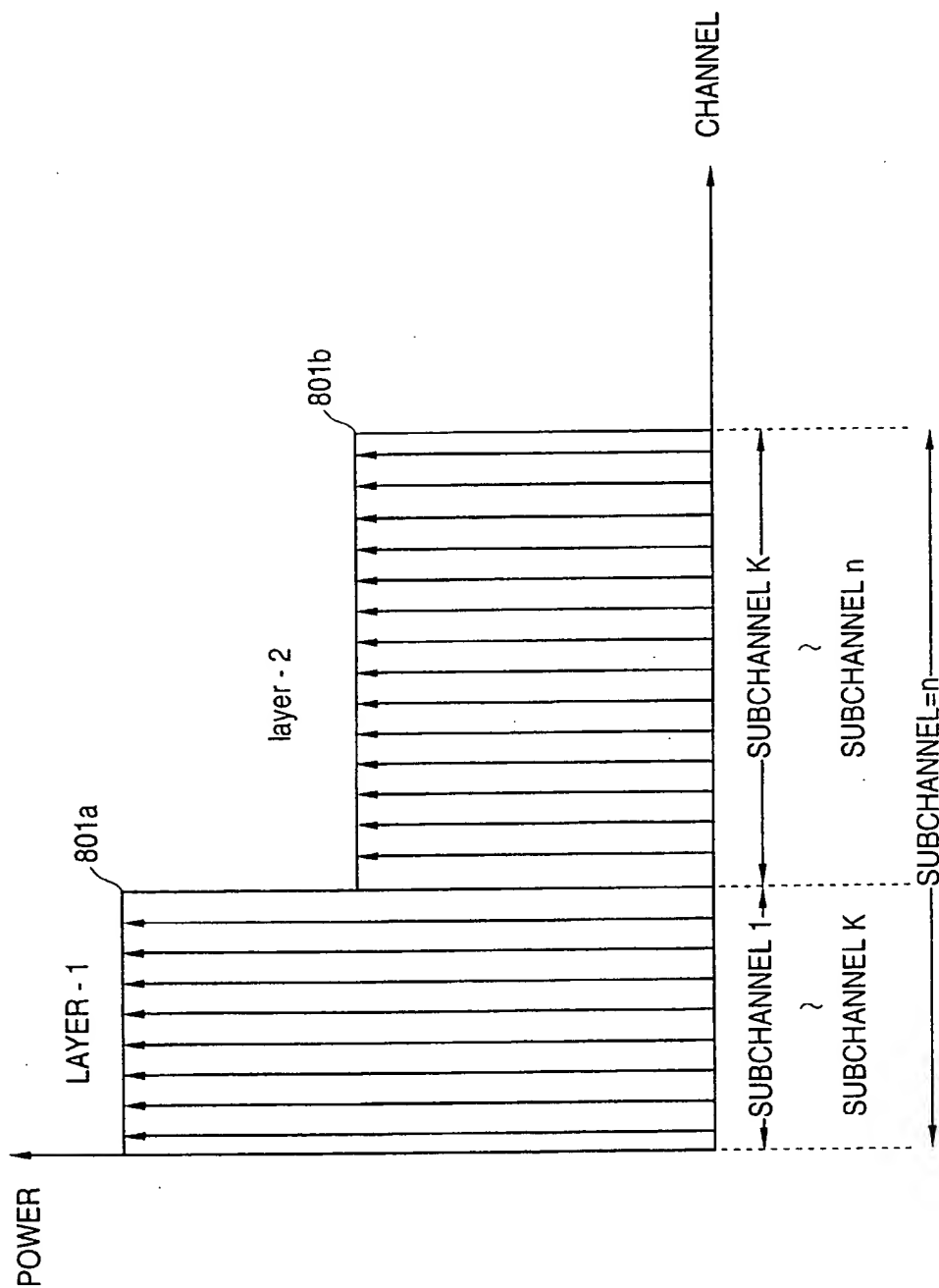


FIG. 148

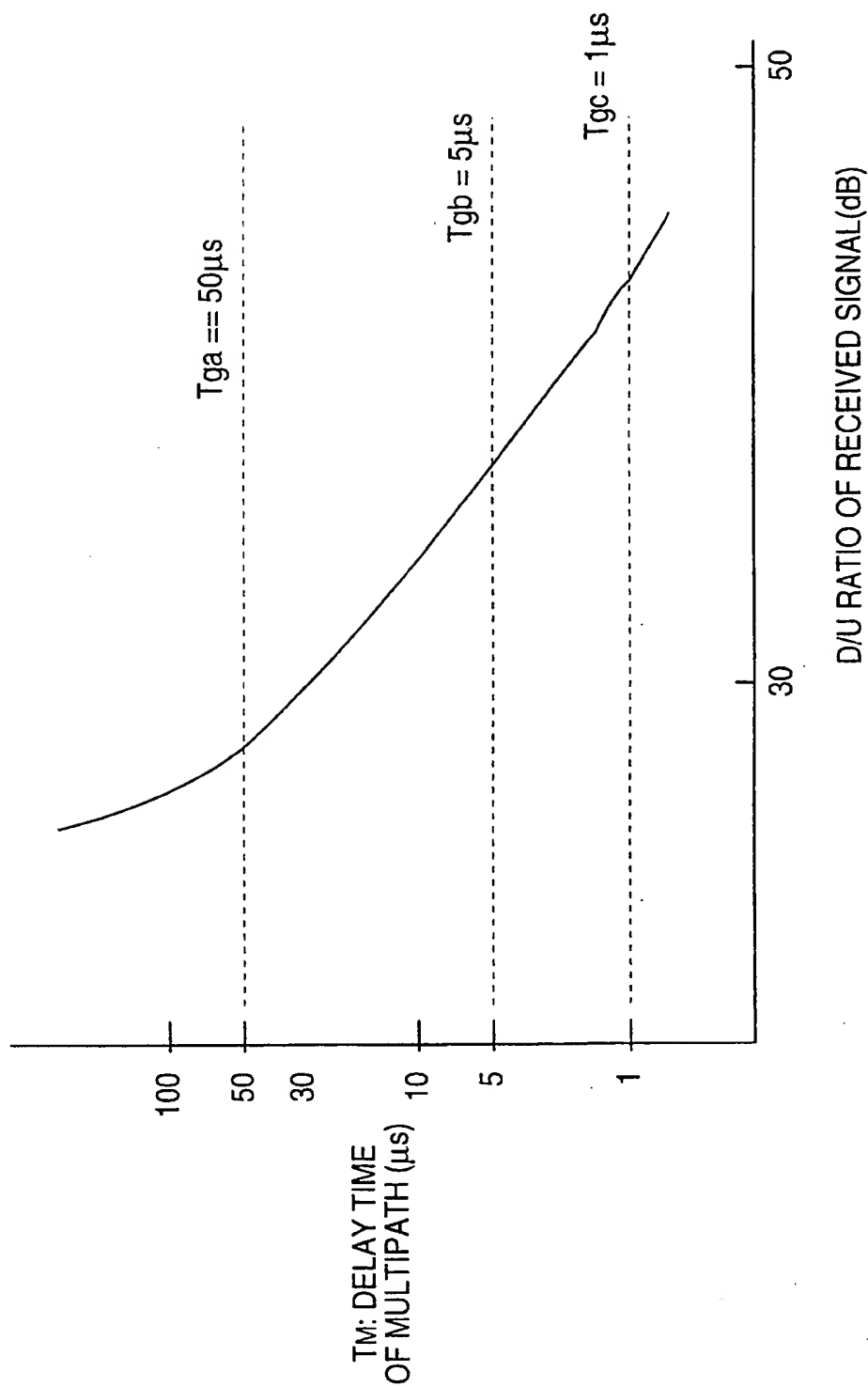


FIG. 149(a)

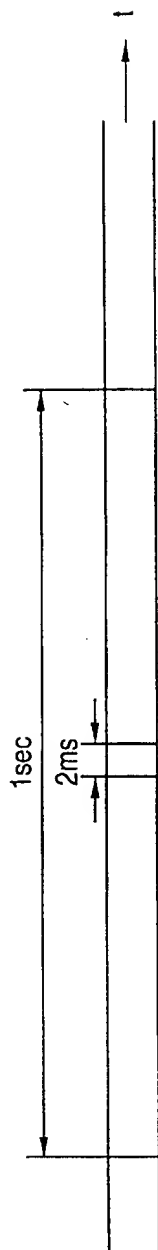


FIG. 149(b)

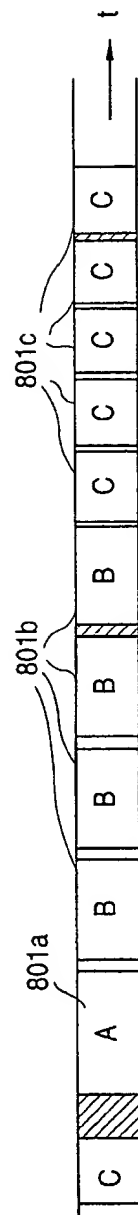


FIG. 149(c)

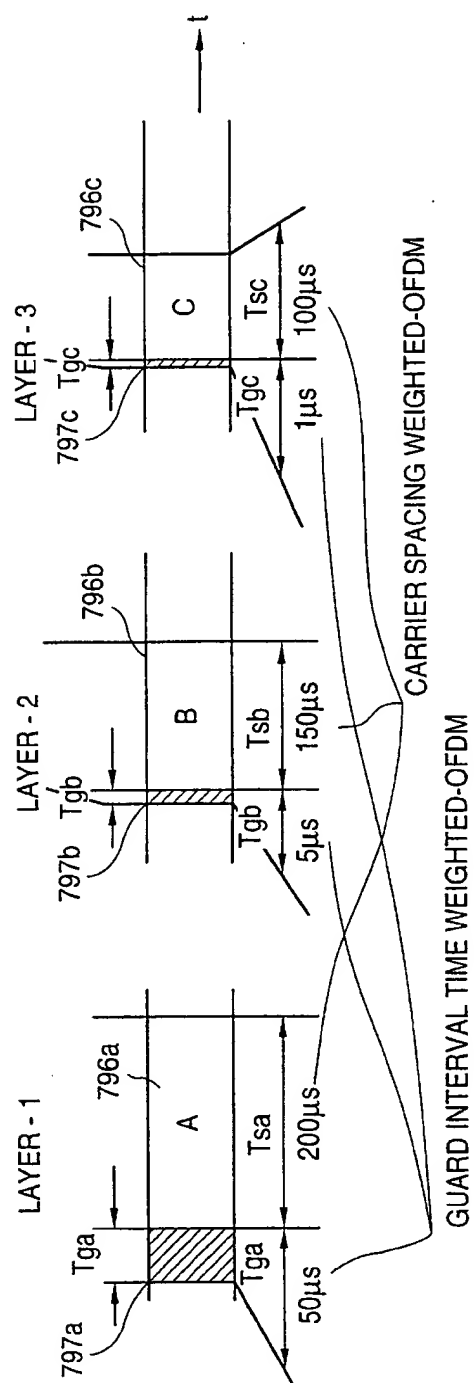


FIG. 150

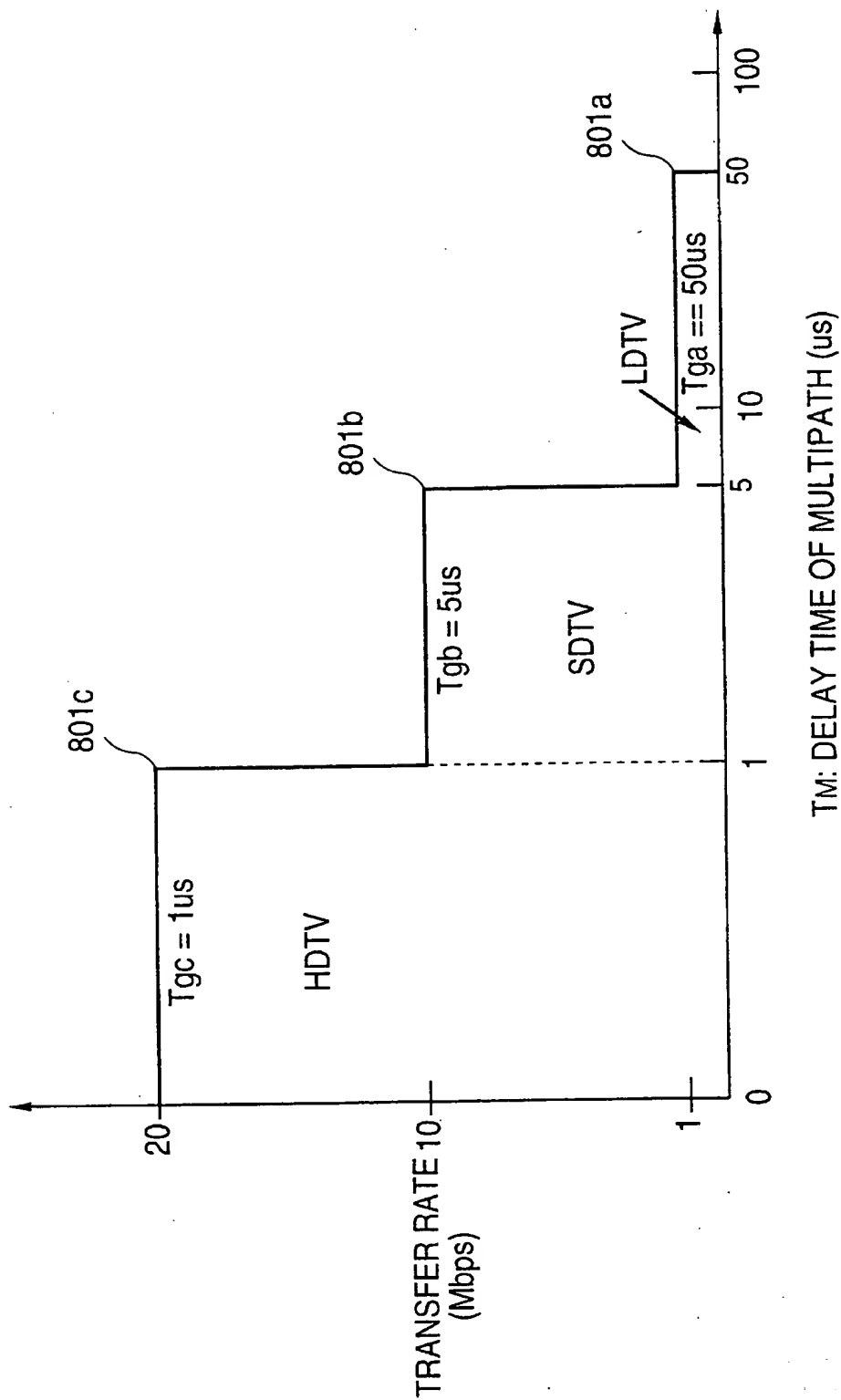


FIG. 151

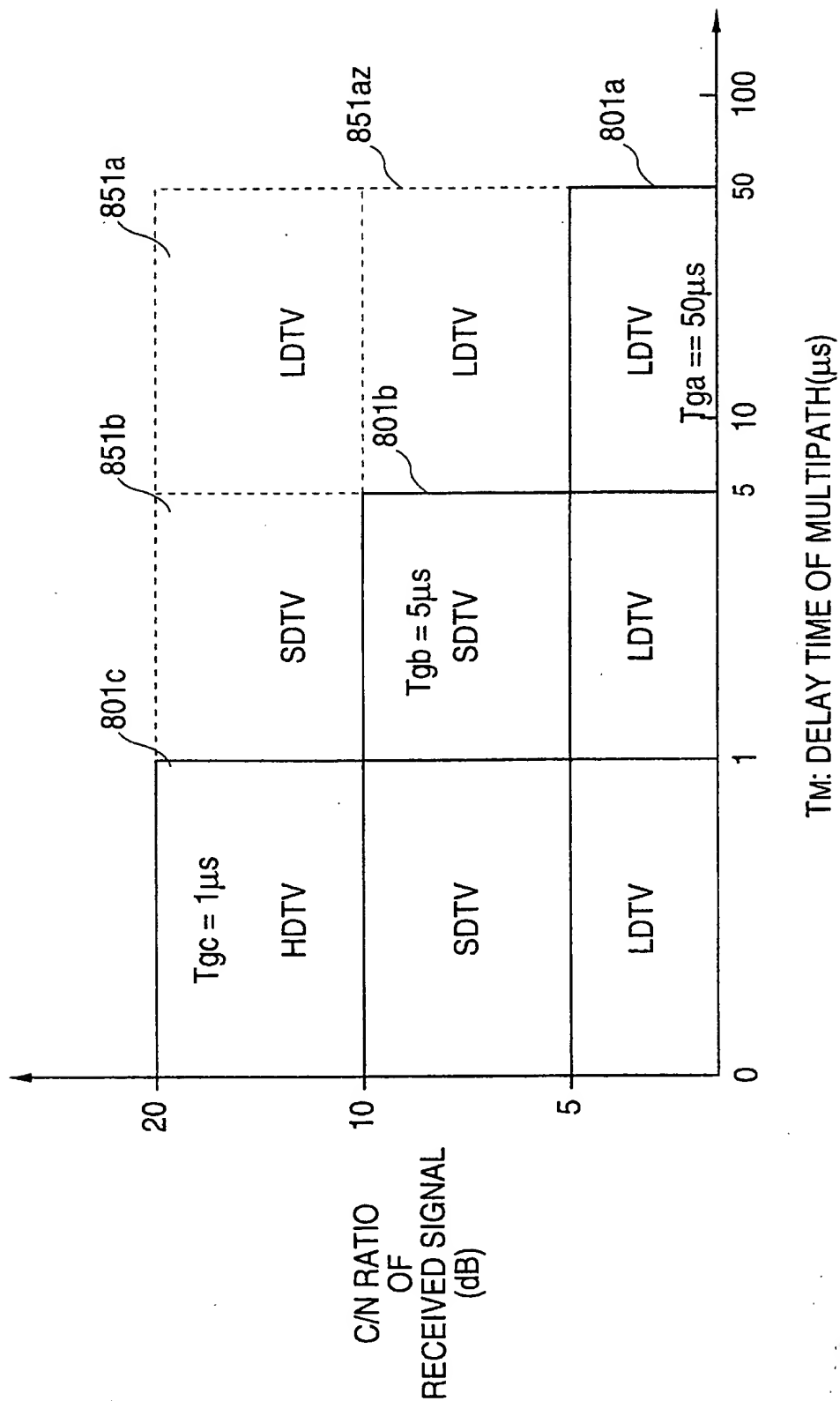


FIG. 152

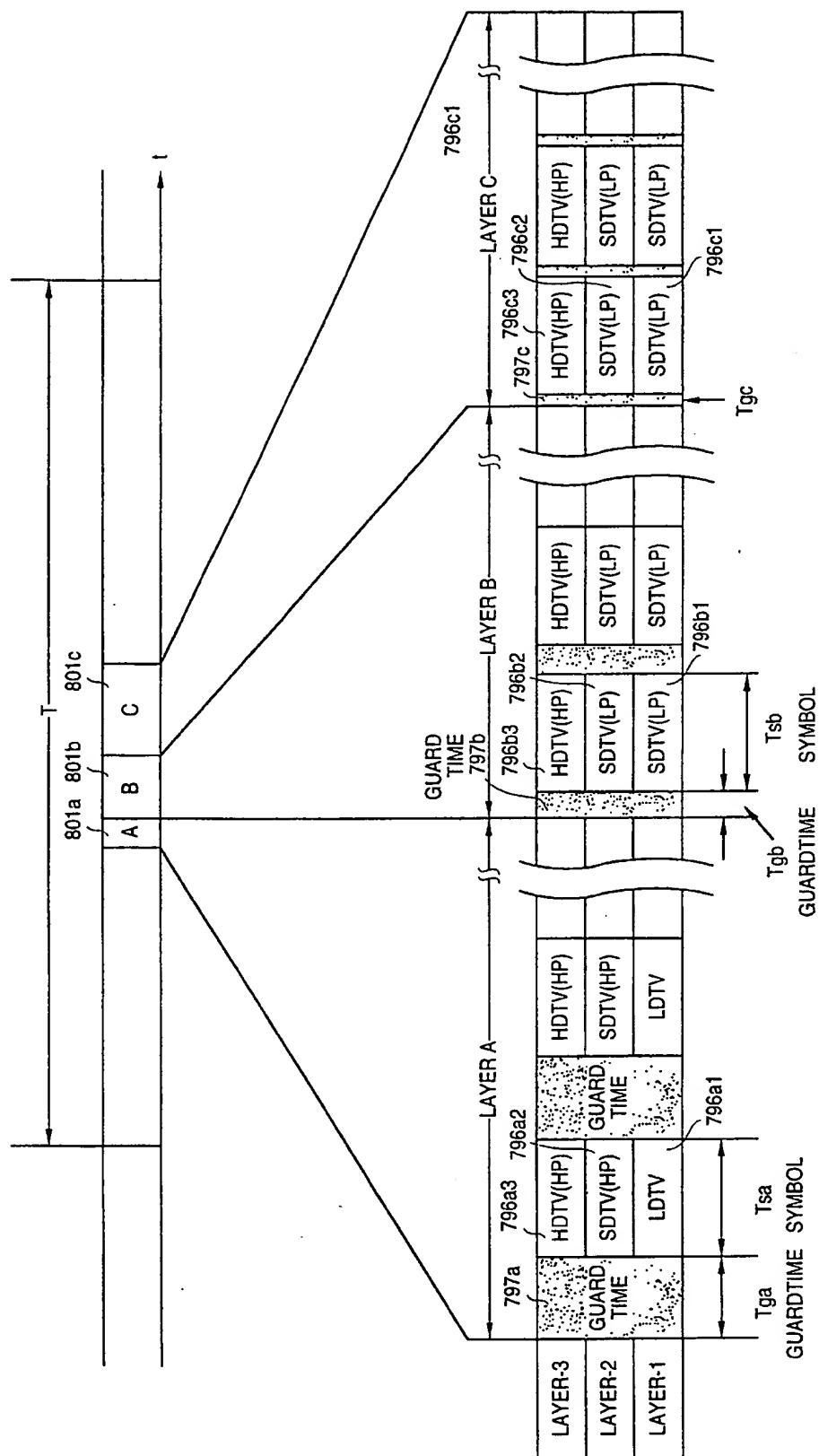
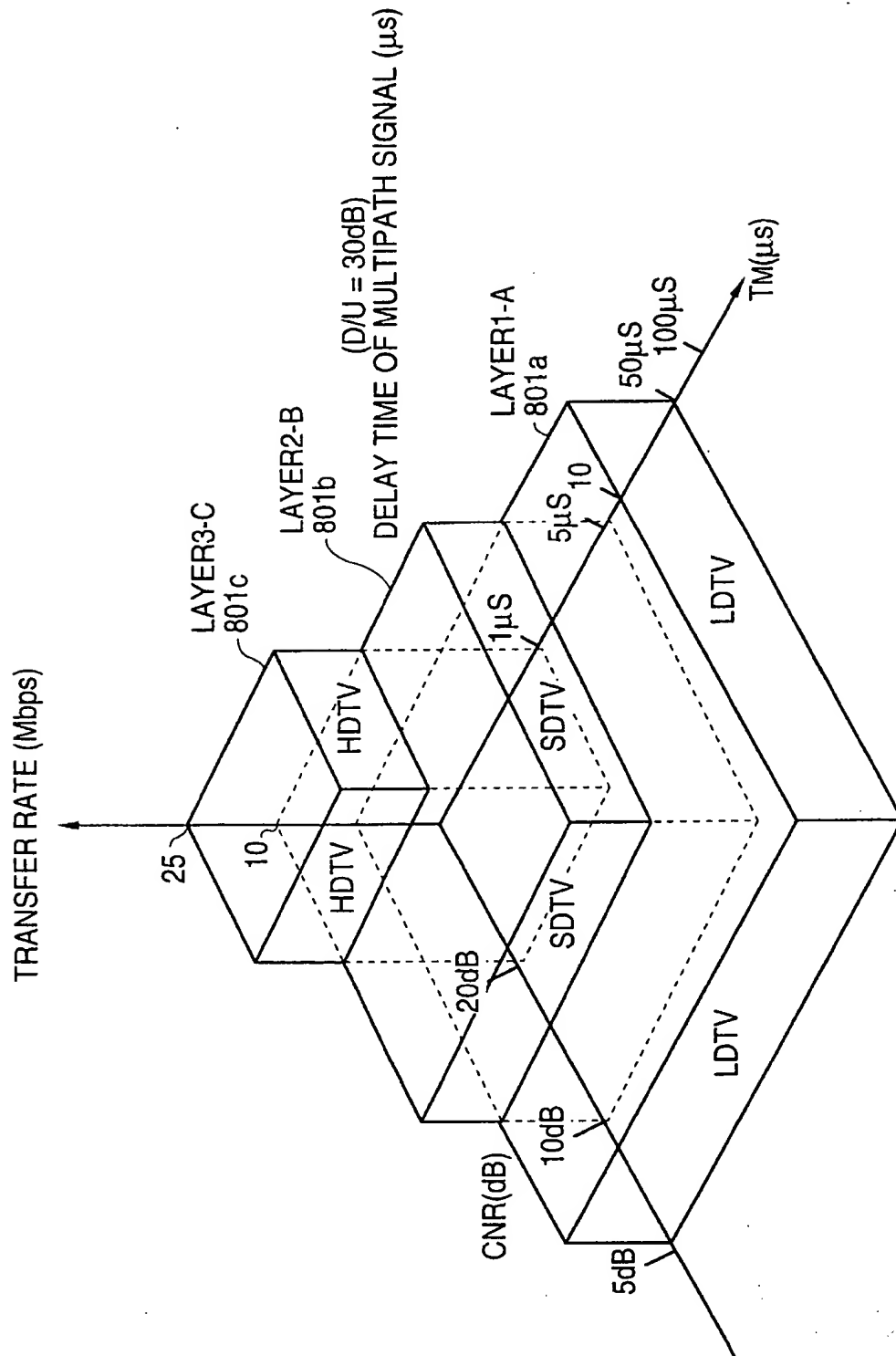


FIG. 153



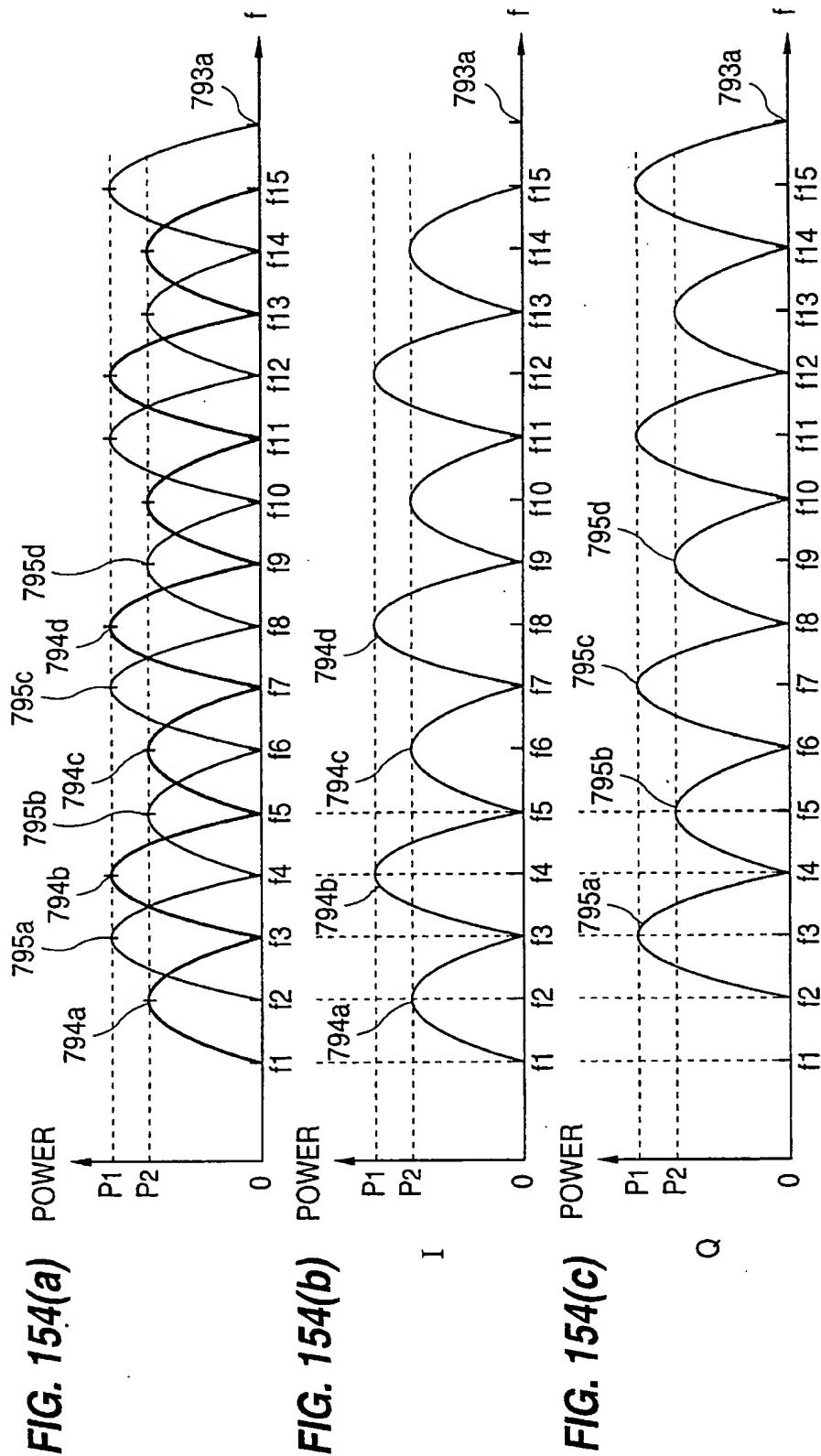


FIG. 155

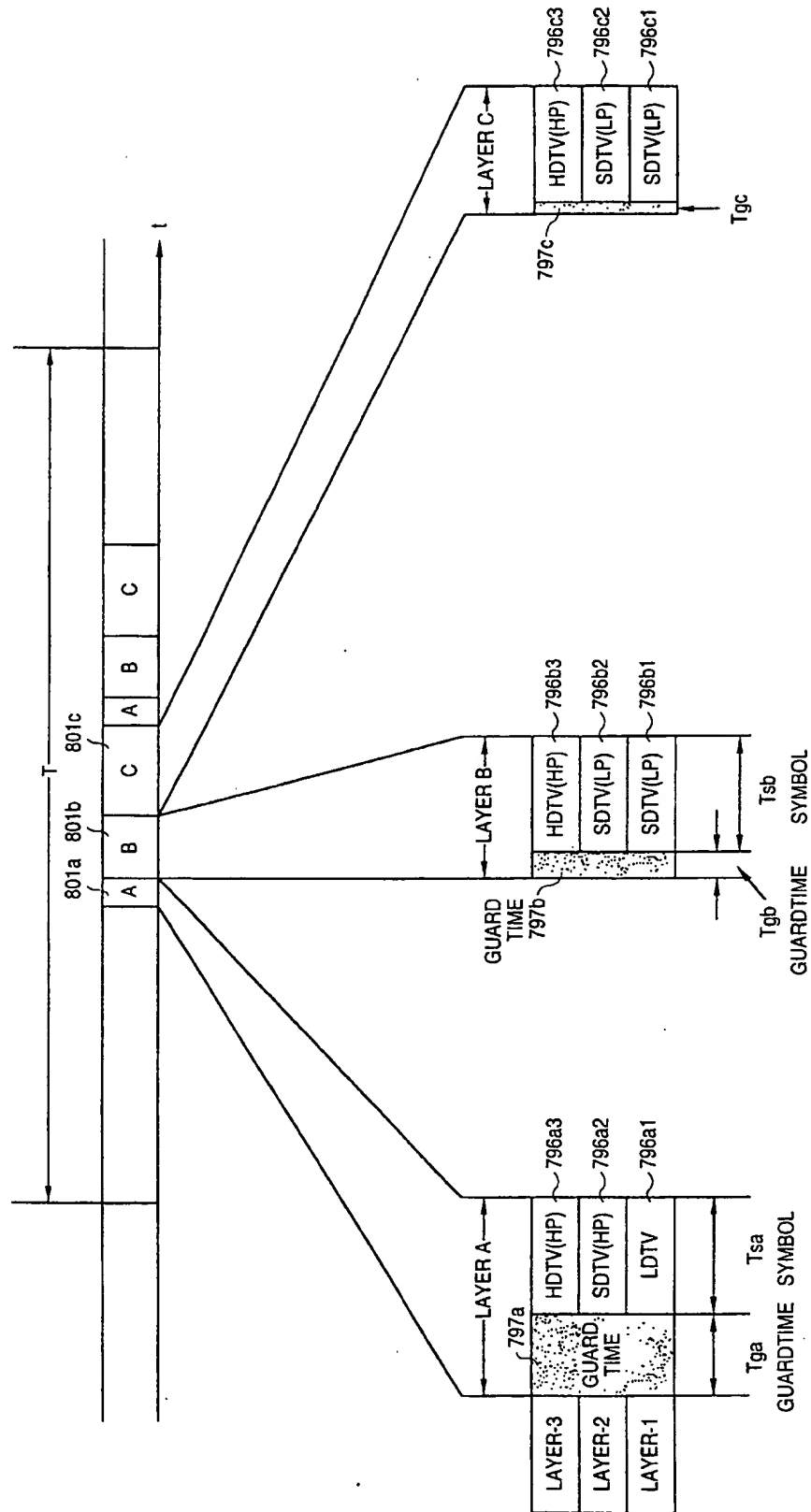


FIG. 156

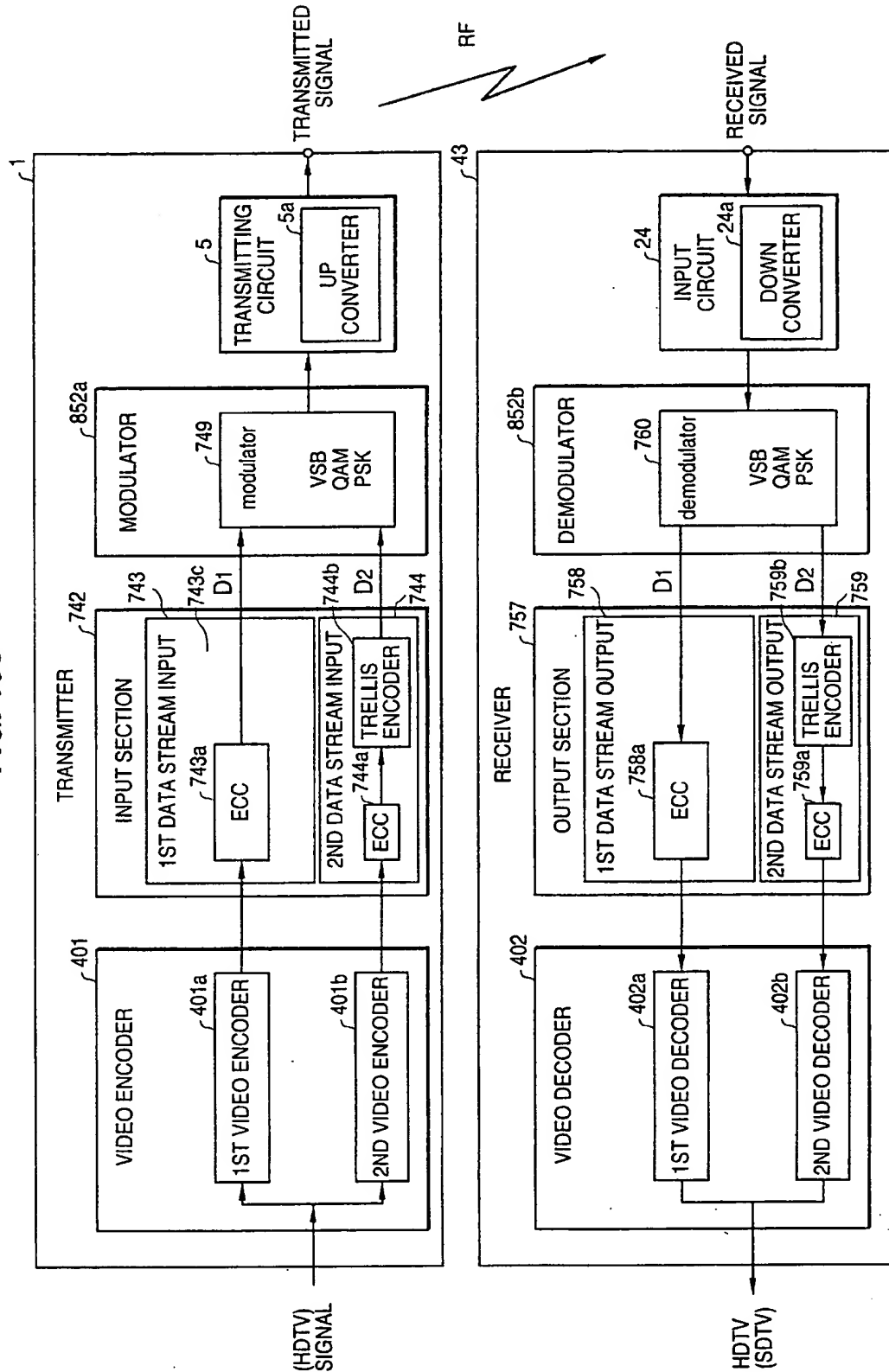


FIG. 157

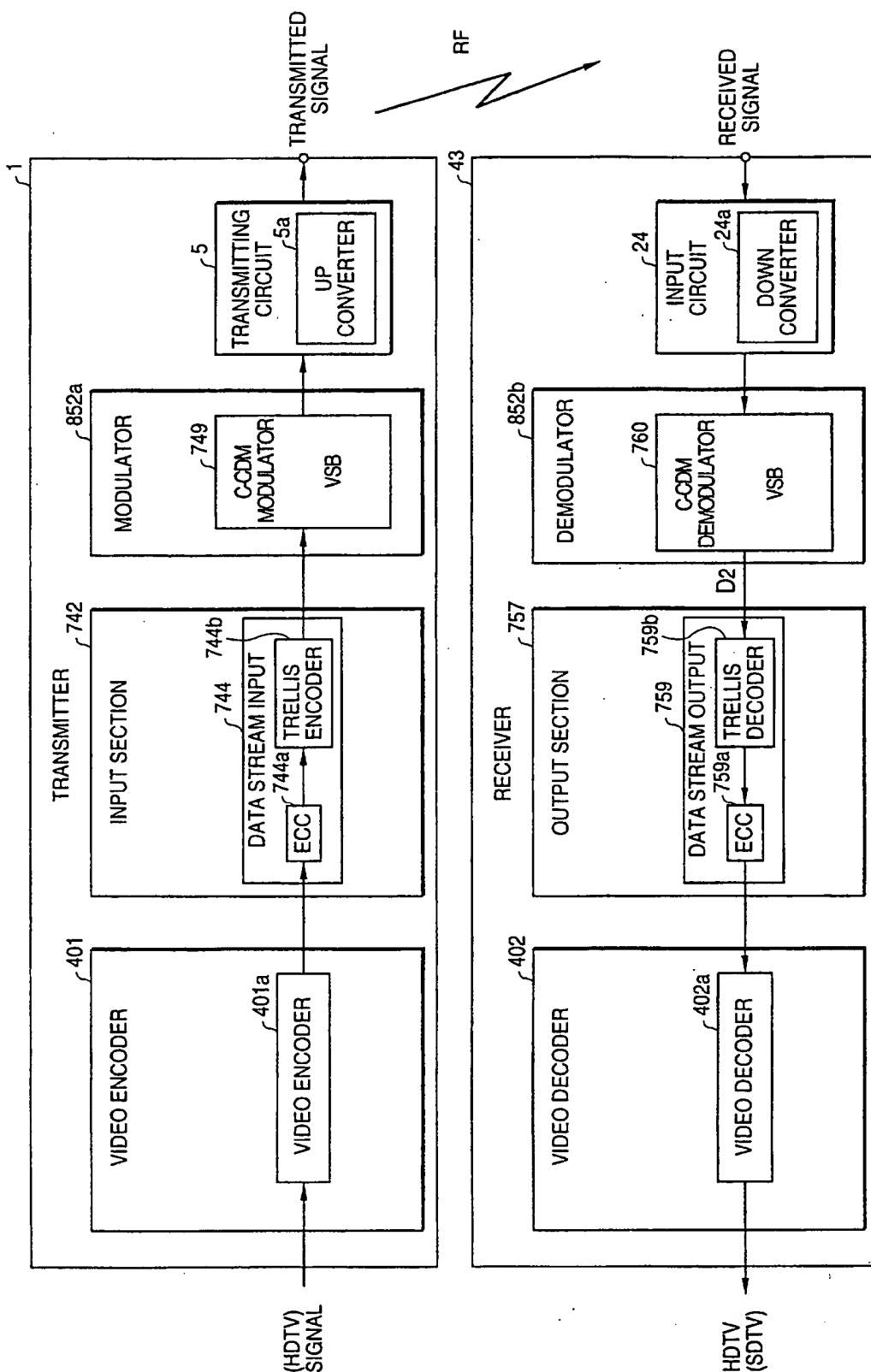
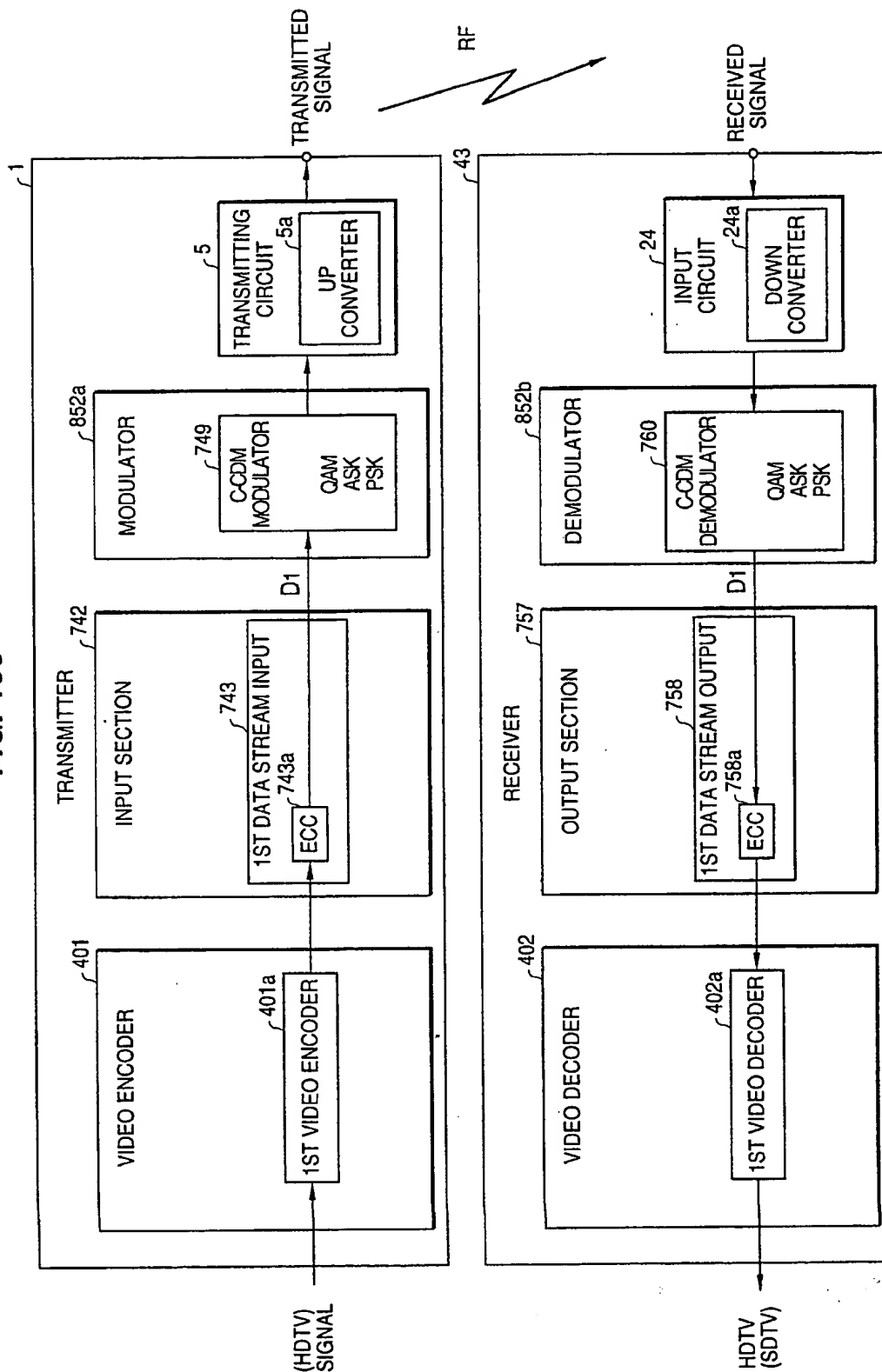


FIG. 158



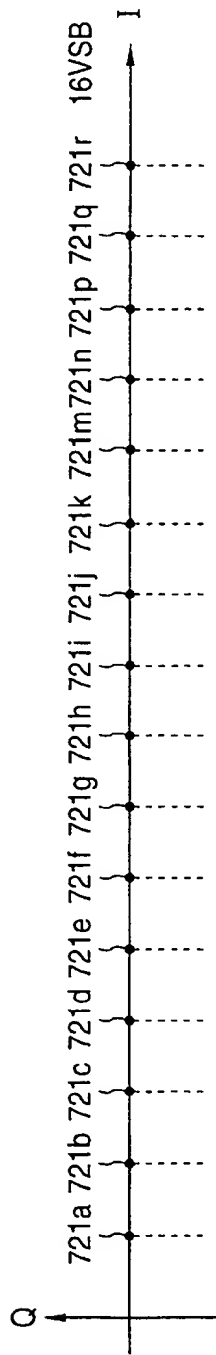


FIG. 159(a)

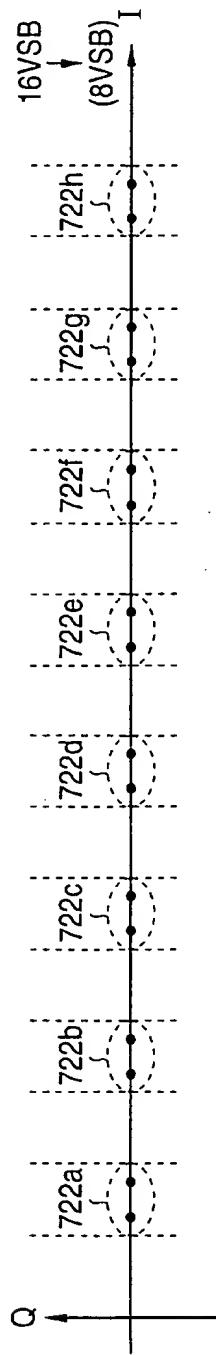


FIG. 159(b)

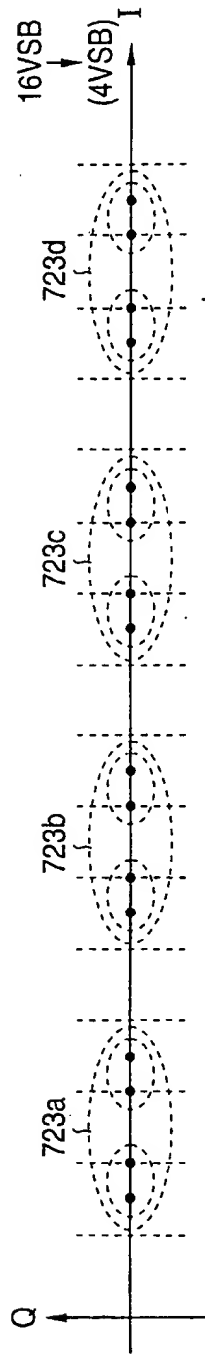


FIG. 159(c)

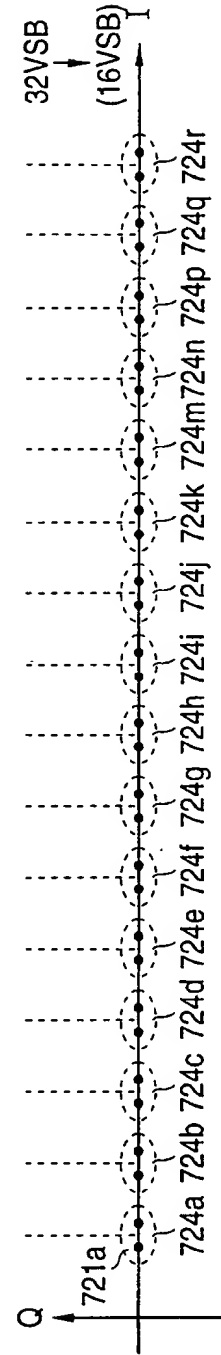


FIG. 159(d)

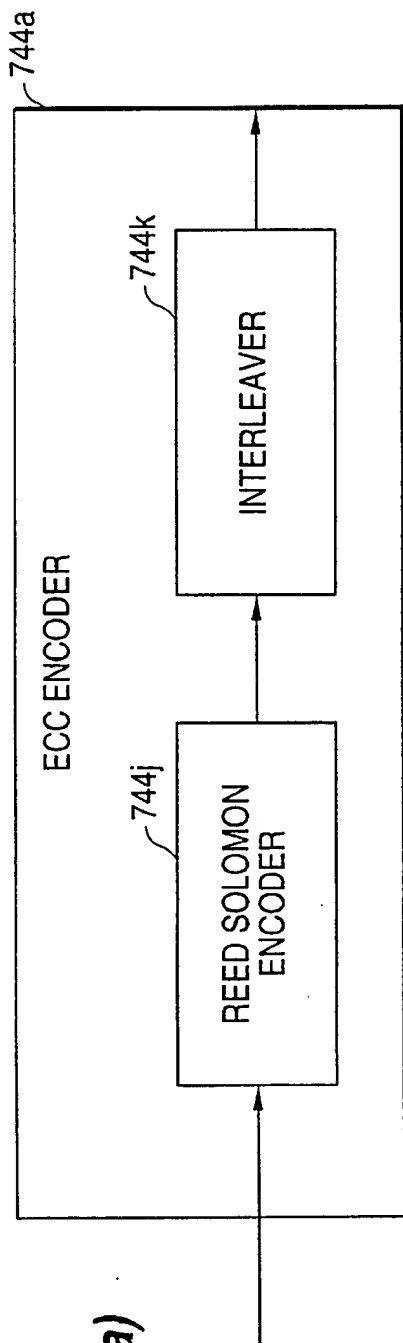


FIG. 160(a)

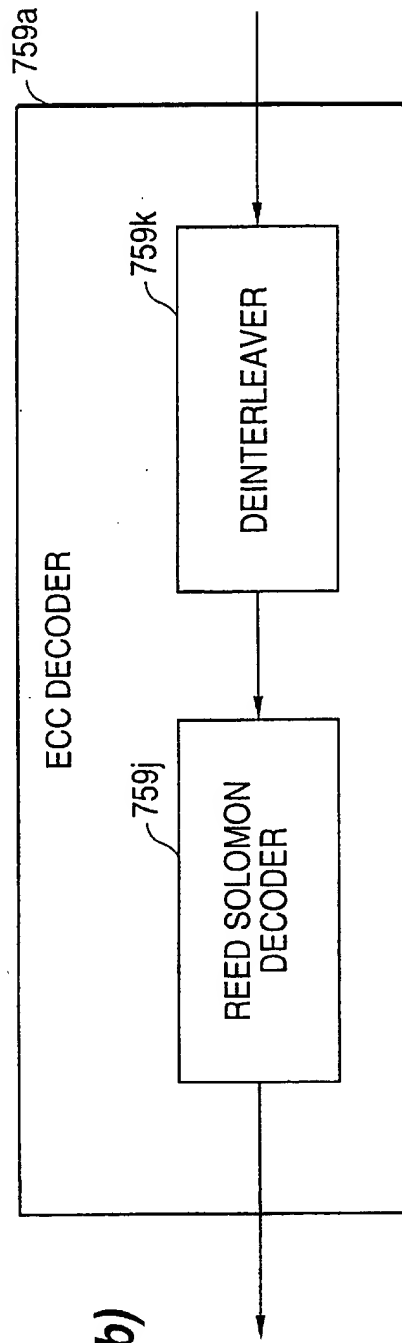


FIG. 160(b)

564020-2E04260

FIG. 161

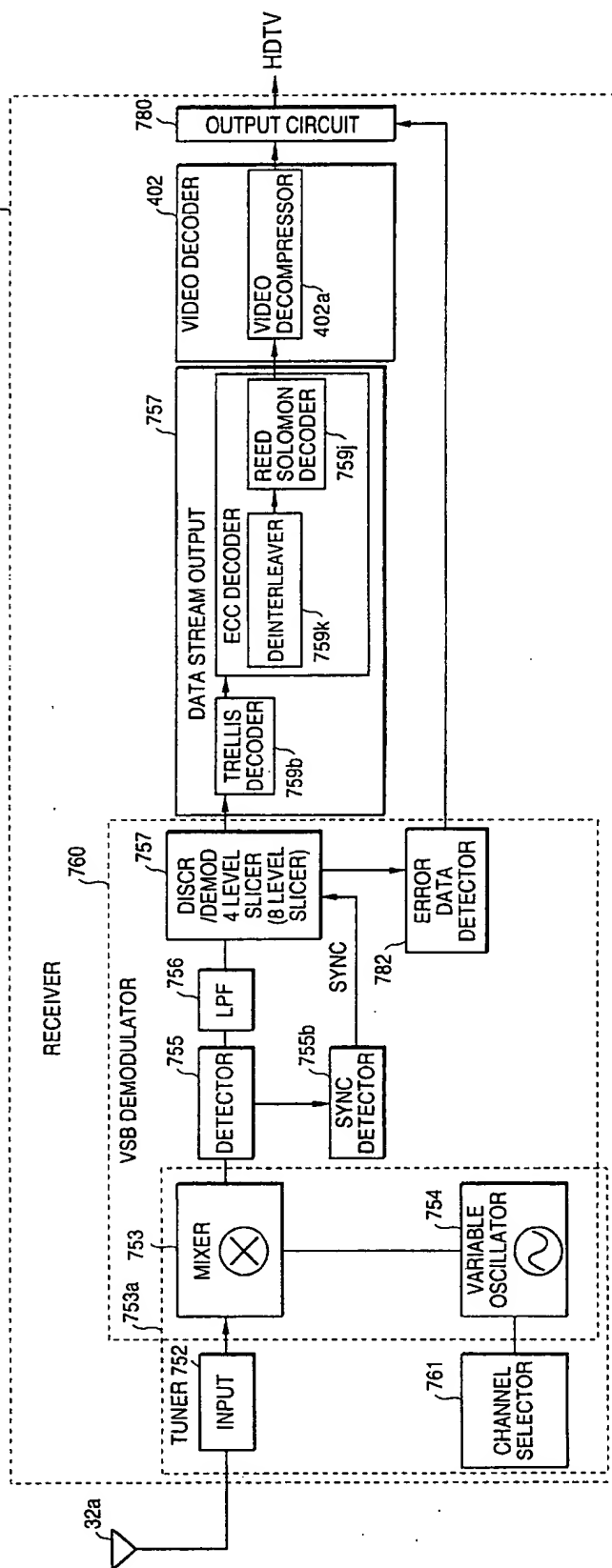


FIG. 162

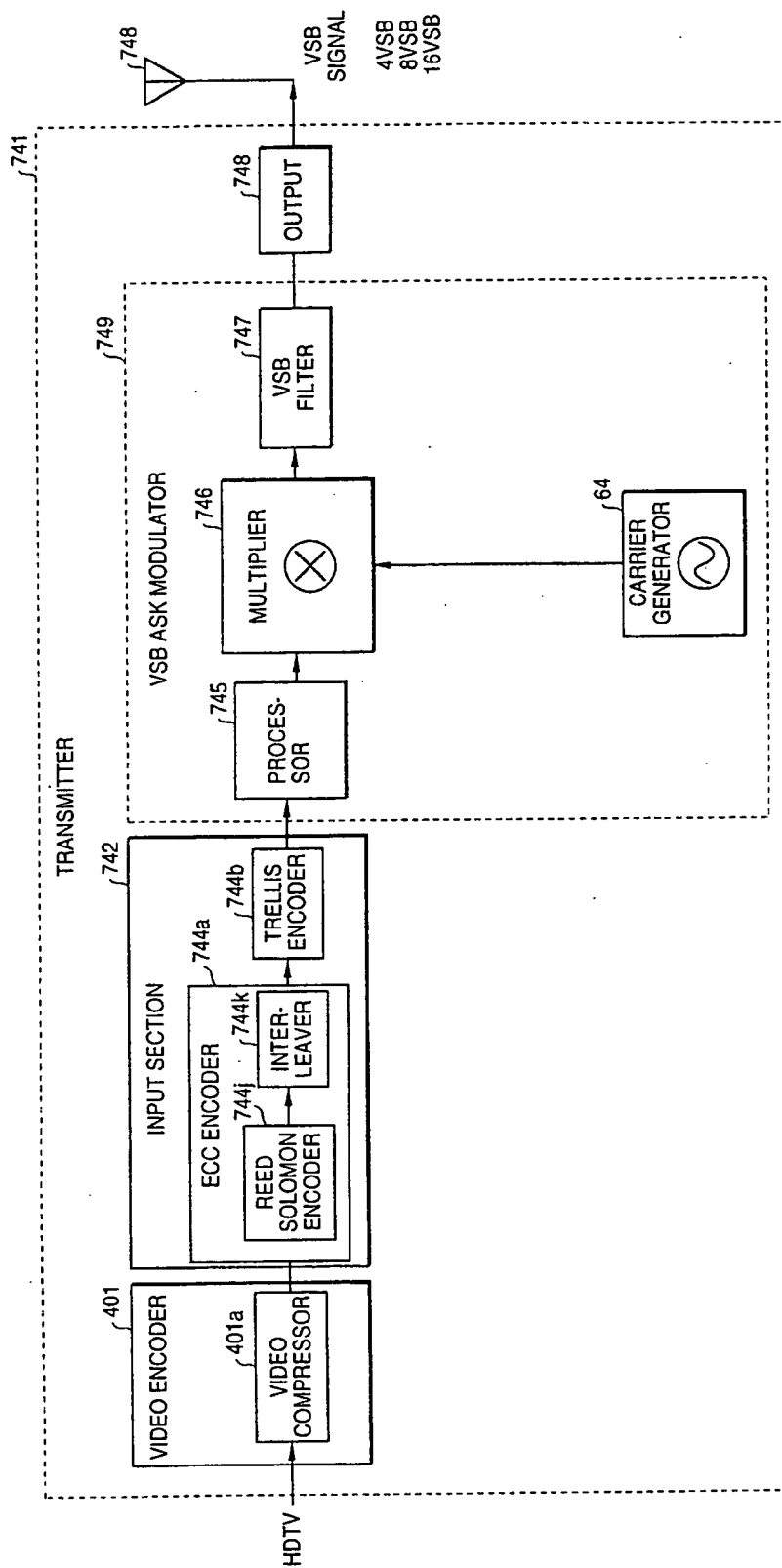


FIG. 163

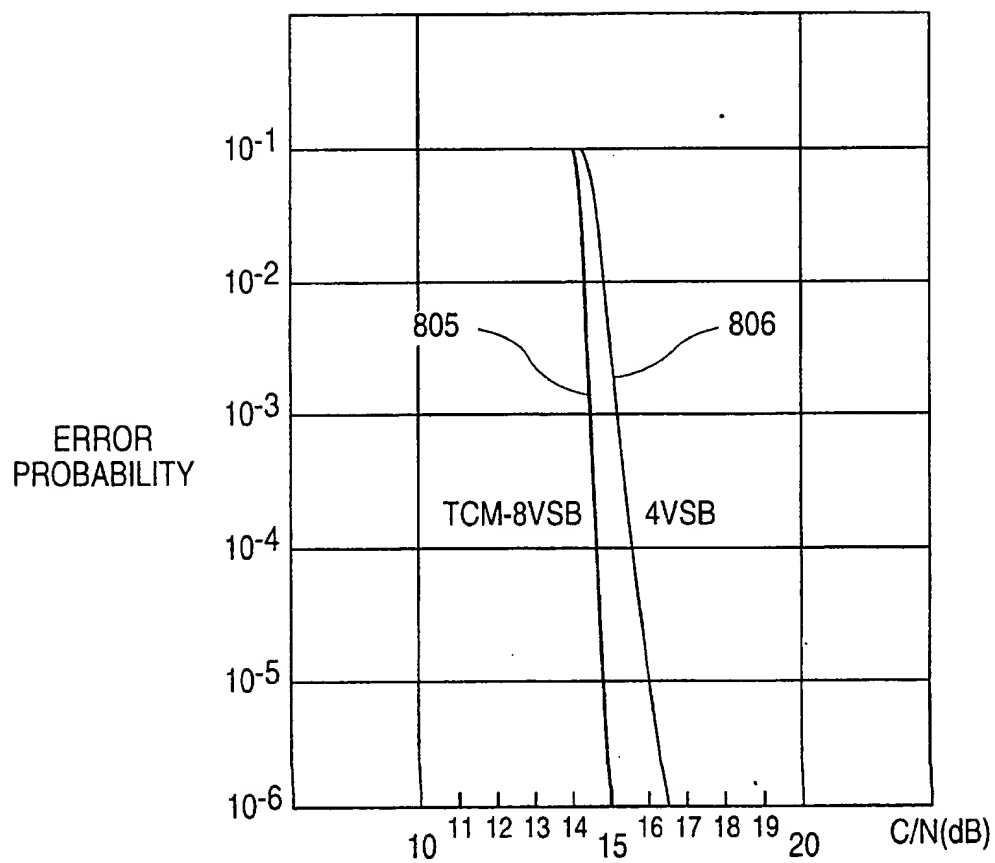
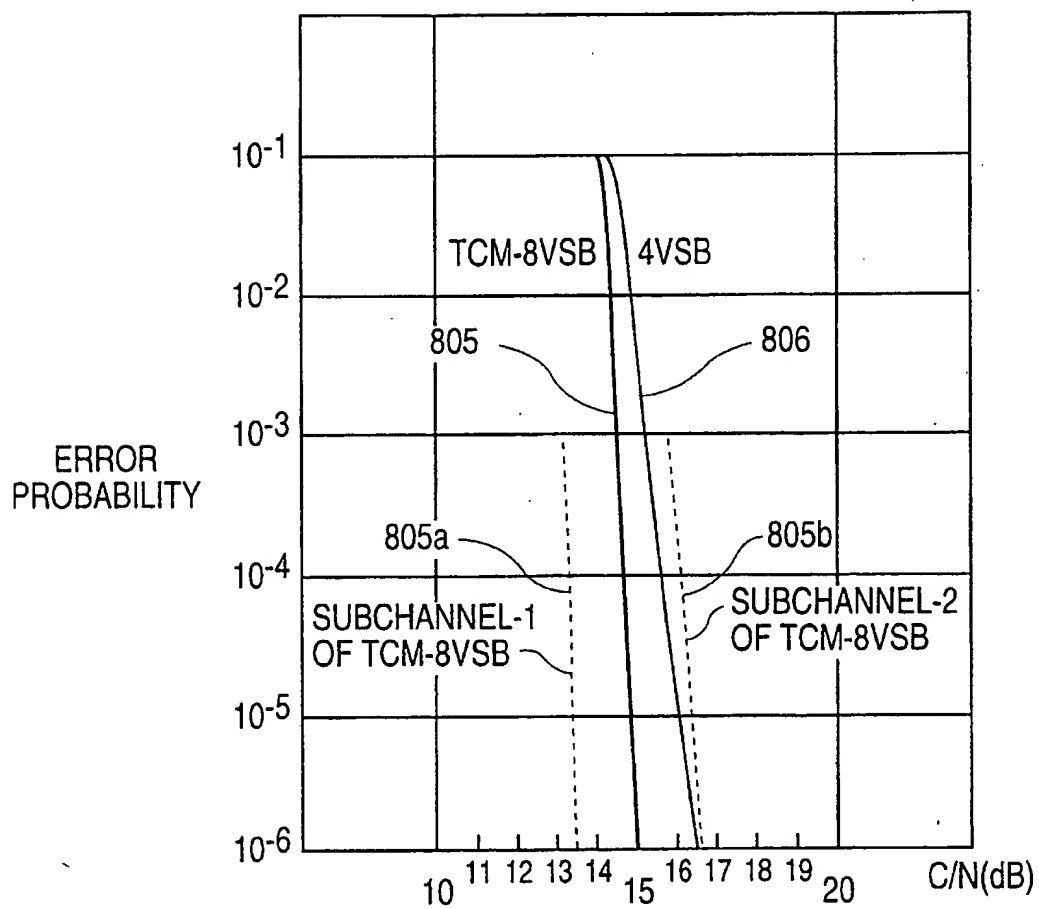


FIG. 164



654020 2E04260

FIG. 165(a)

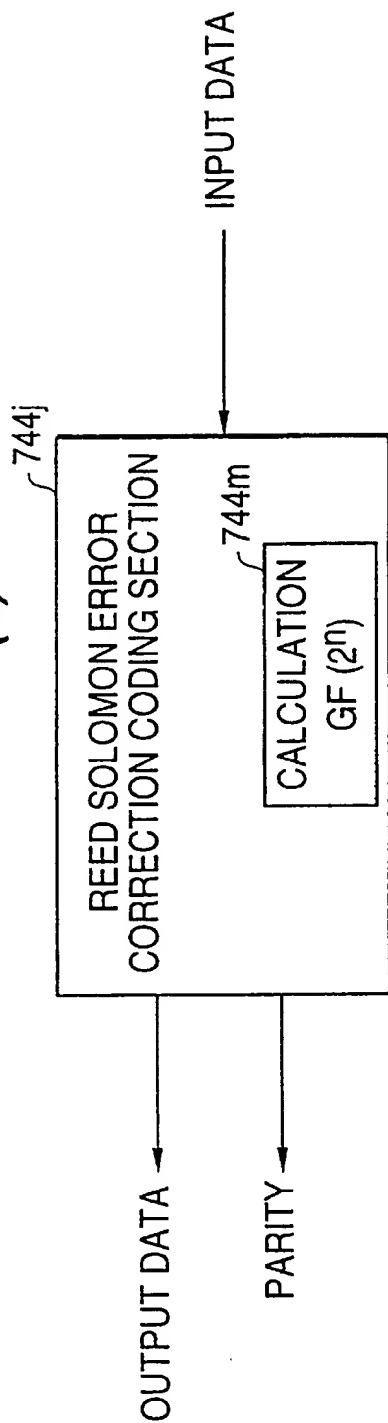


FIG. 165(b)

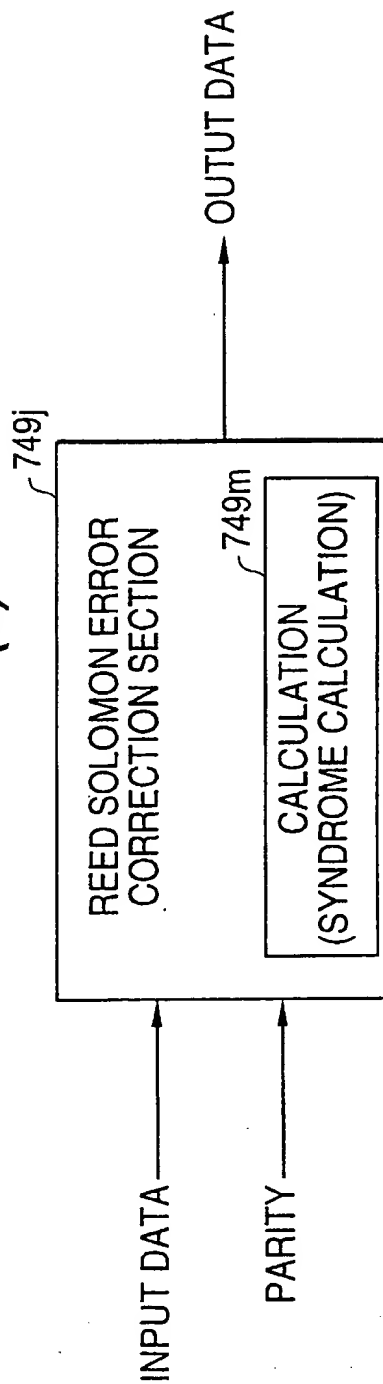
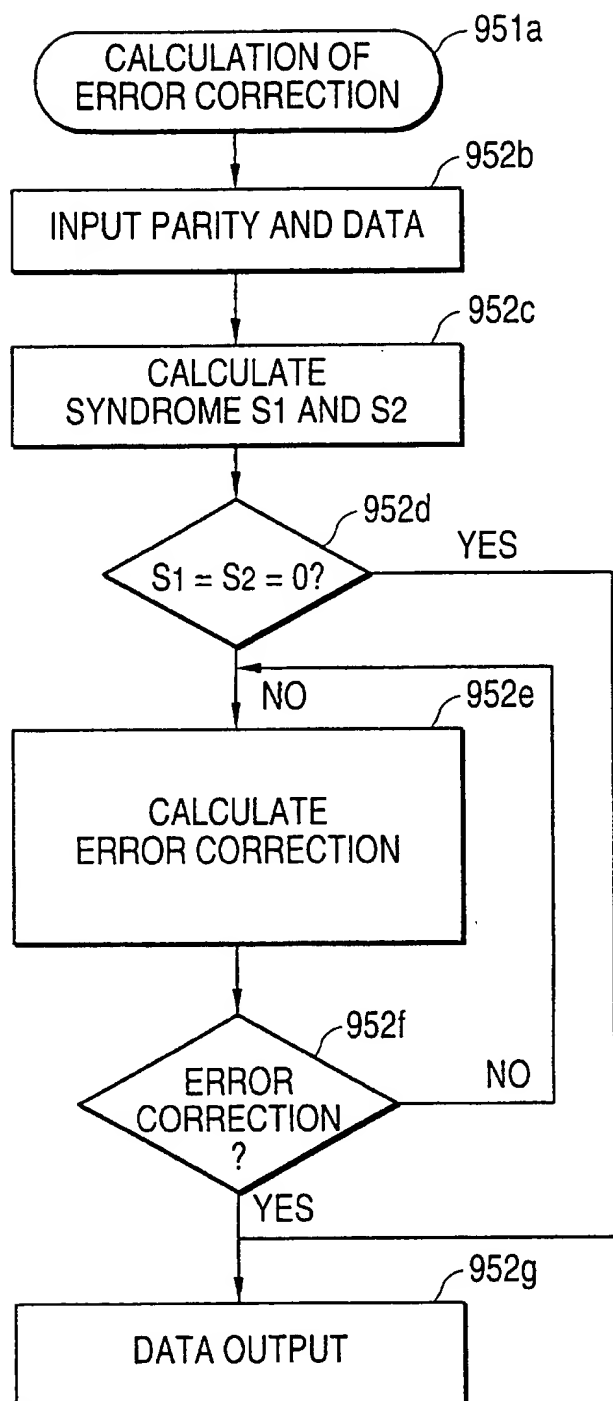
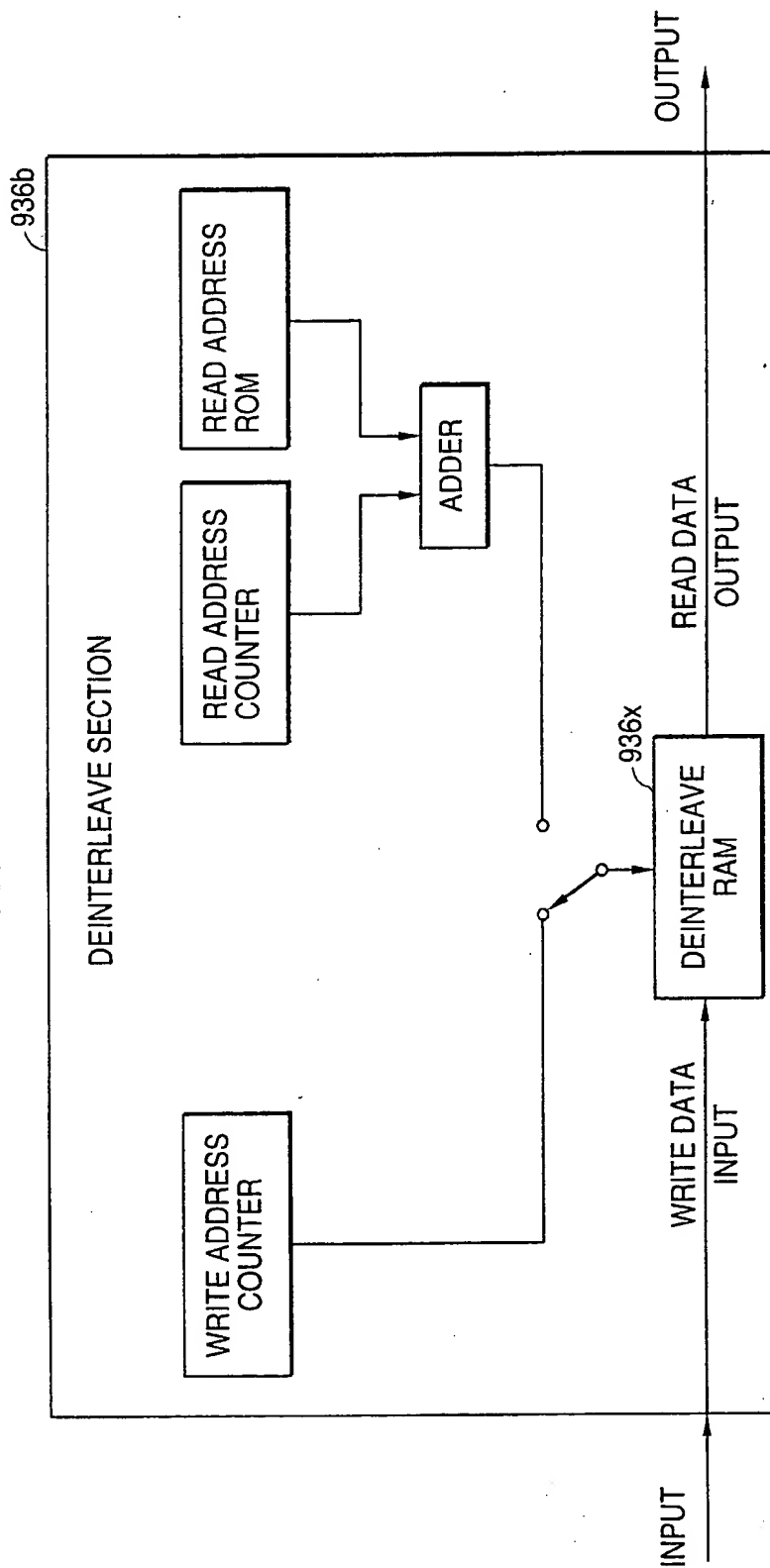


FIG. 166

654030-2E04260

FIG. 167



554020 4E044260

FIG. 168(a)

INTERLEAVE TABLE

	1	2	3	4	5	6	7
	DATA						C2 PARITY
1	A 1	A 2	A 3	A 4	A 5	A 6	PARITY
2	B 1	B 2	B 3	B 4			
3	C 1						
4	D 1						
5	E 1						
6	F 1						
C1 PARITY	PARITY	PARITY	PARITY	PARITY	PARITY	PARITY	PARITY

FIG. 168(b)

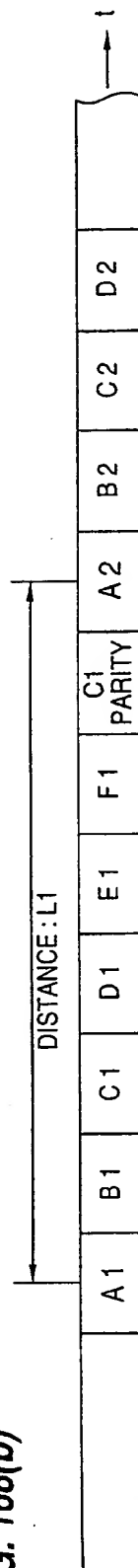


FIG. 169

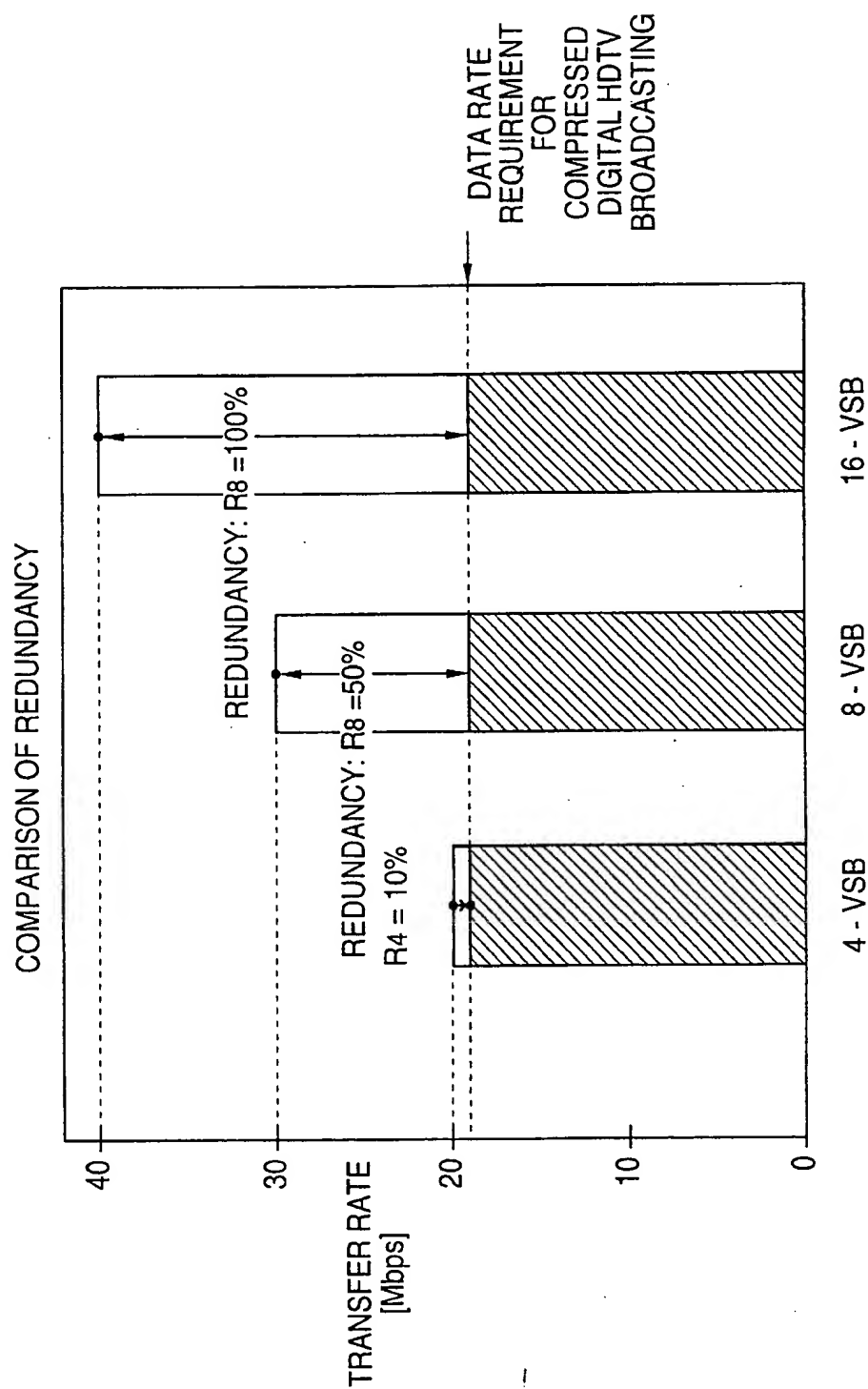


FIG. 170

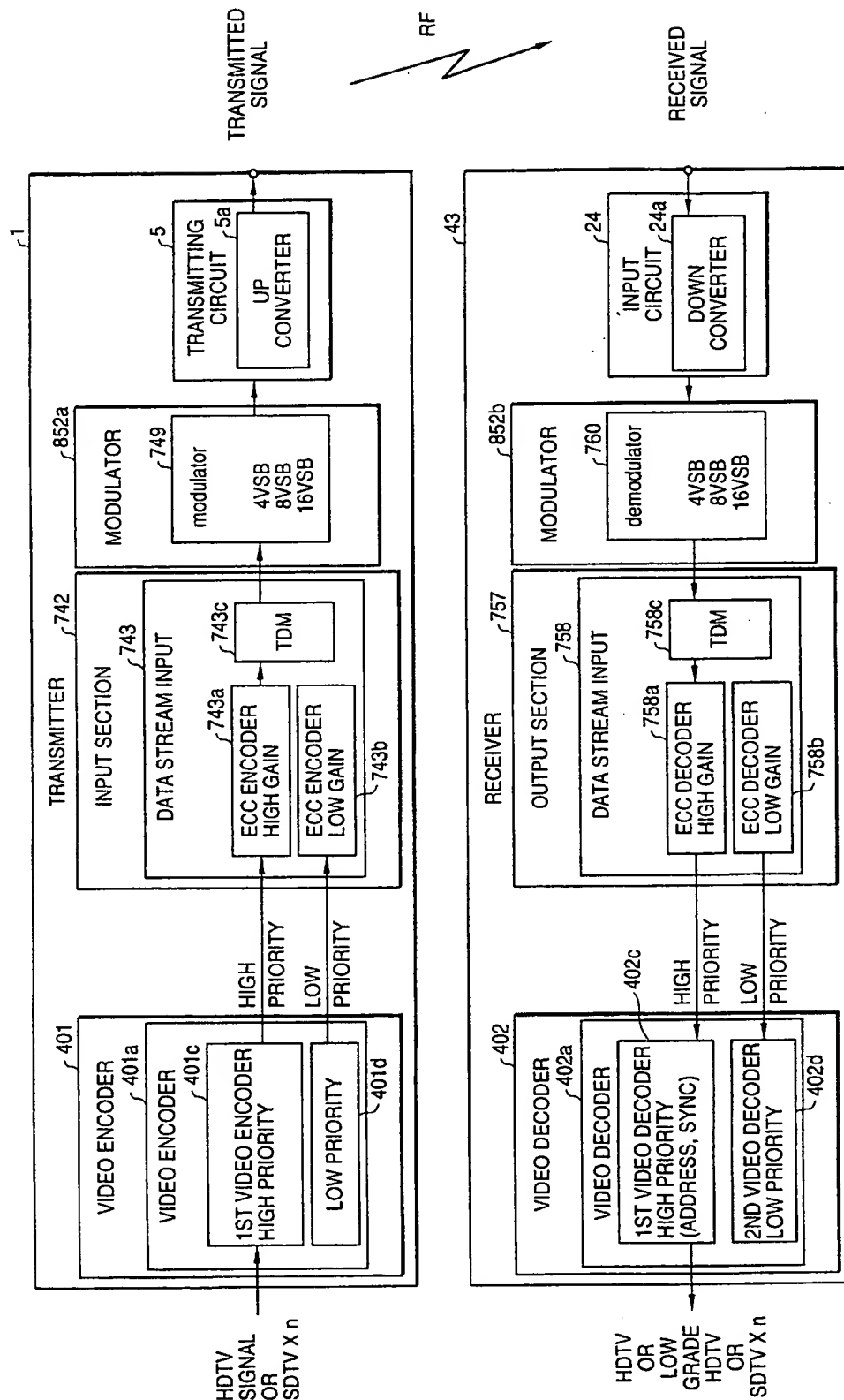


FIG. 171

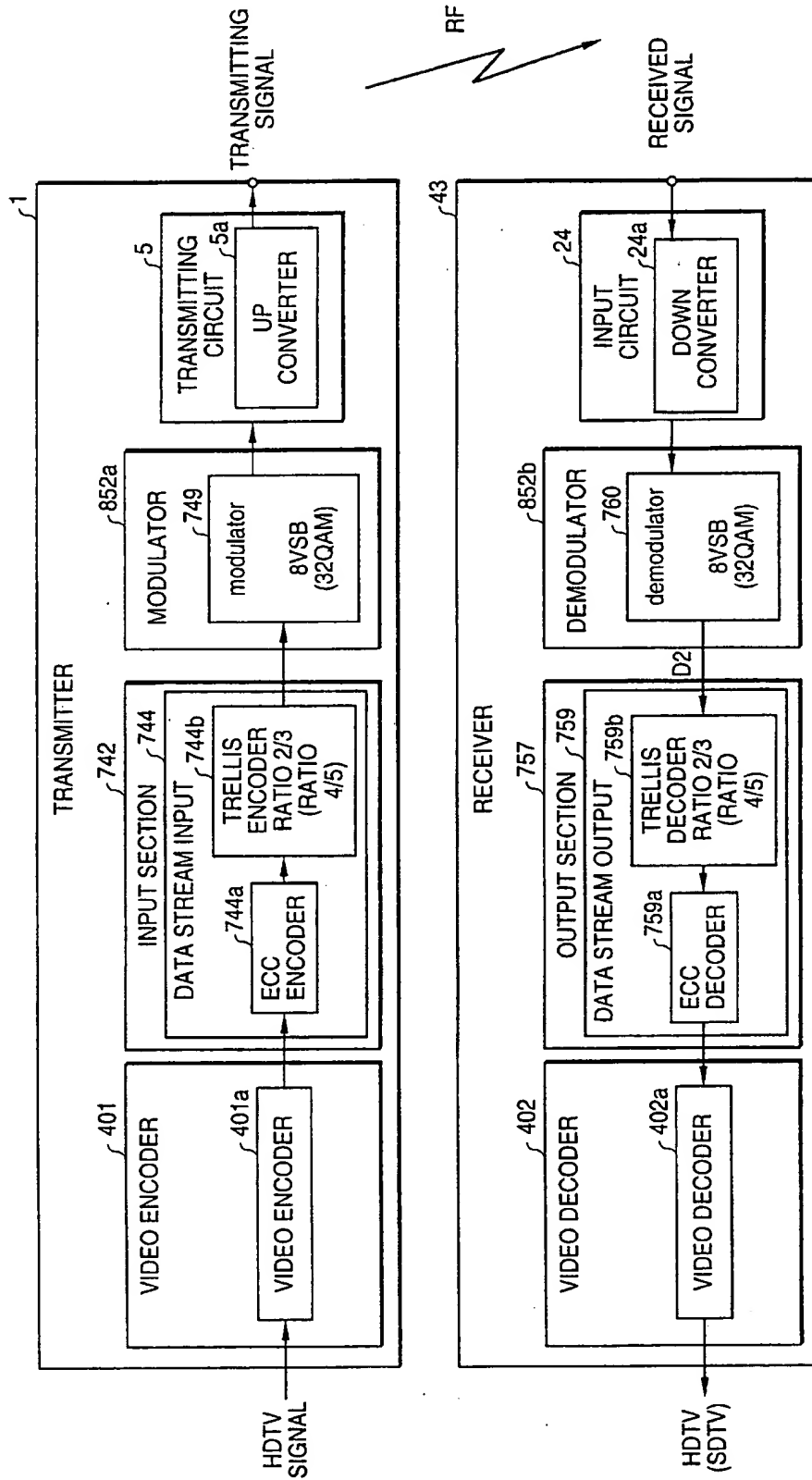


FIG. 172

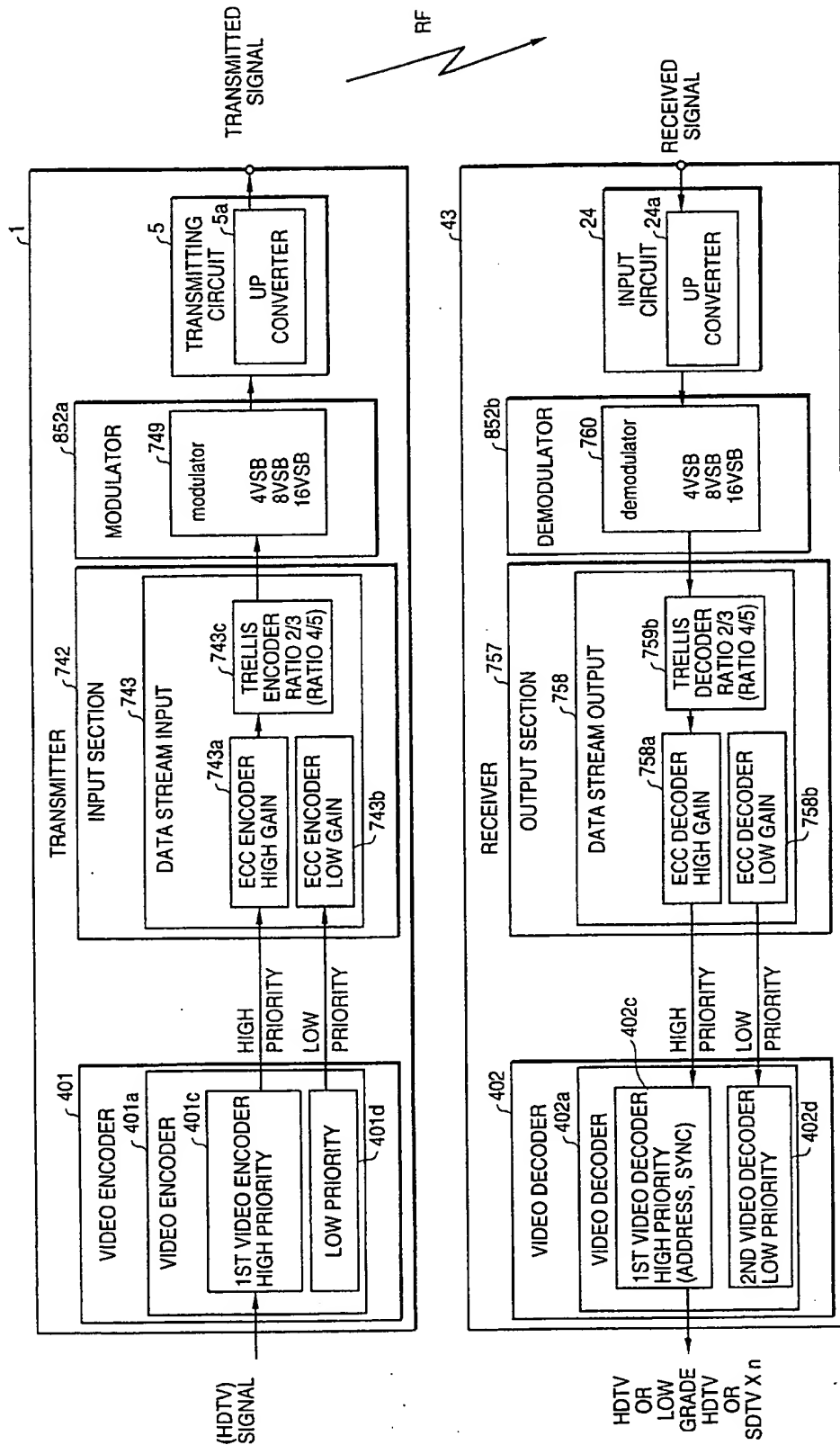


FIG. 173

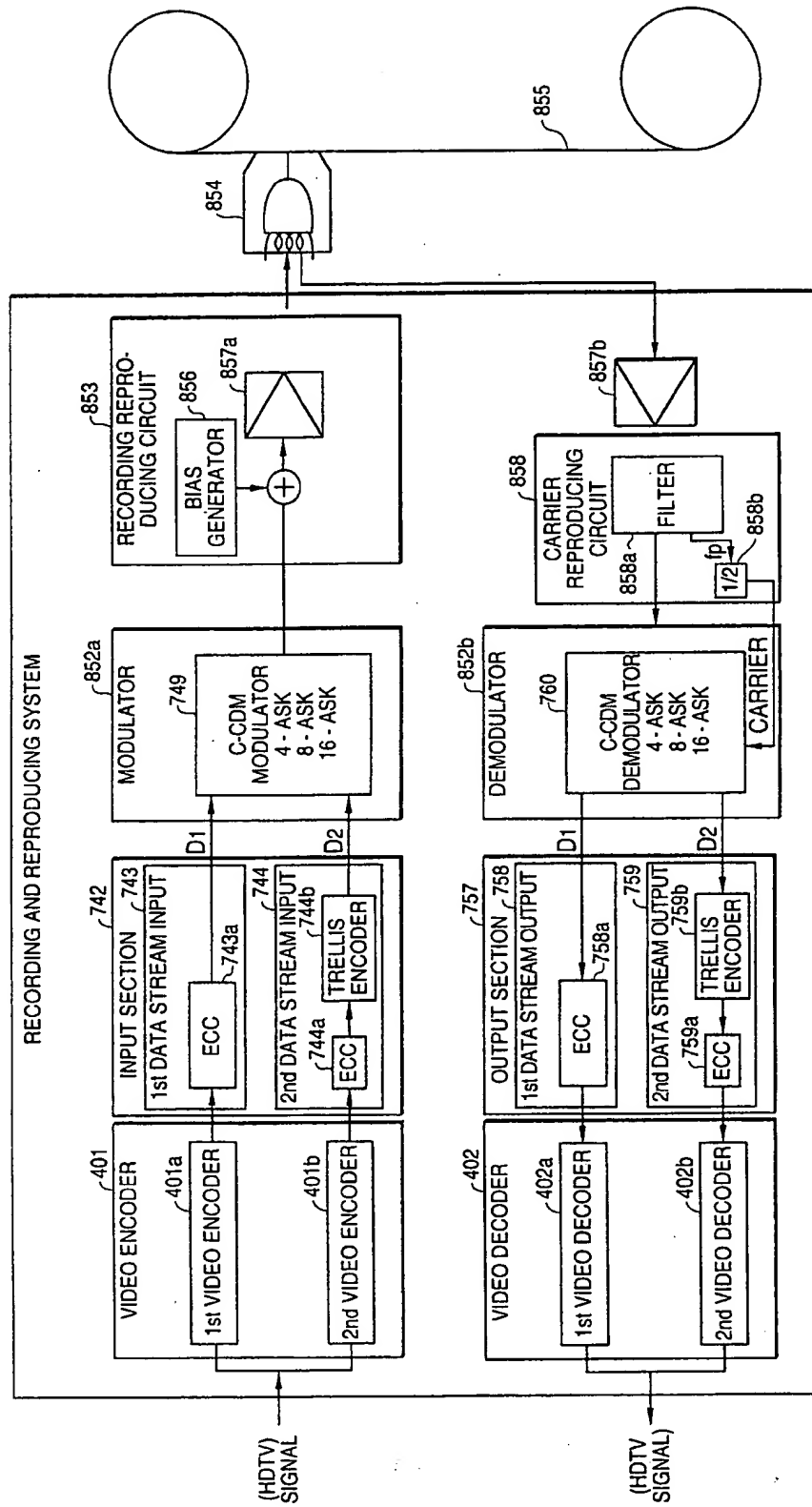


FIG. 174

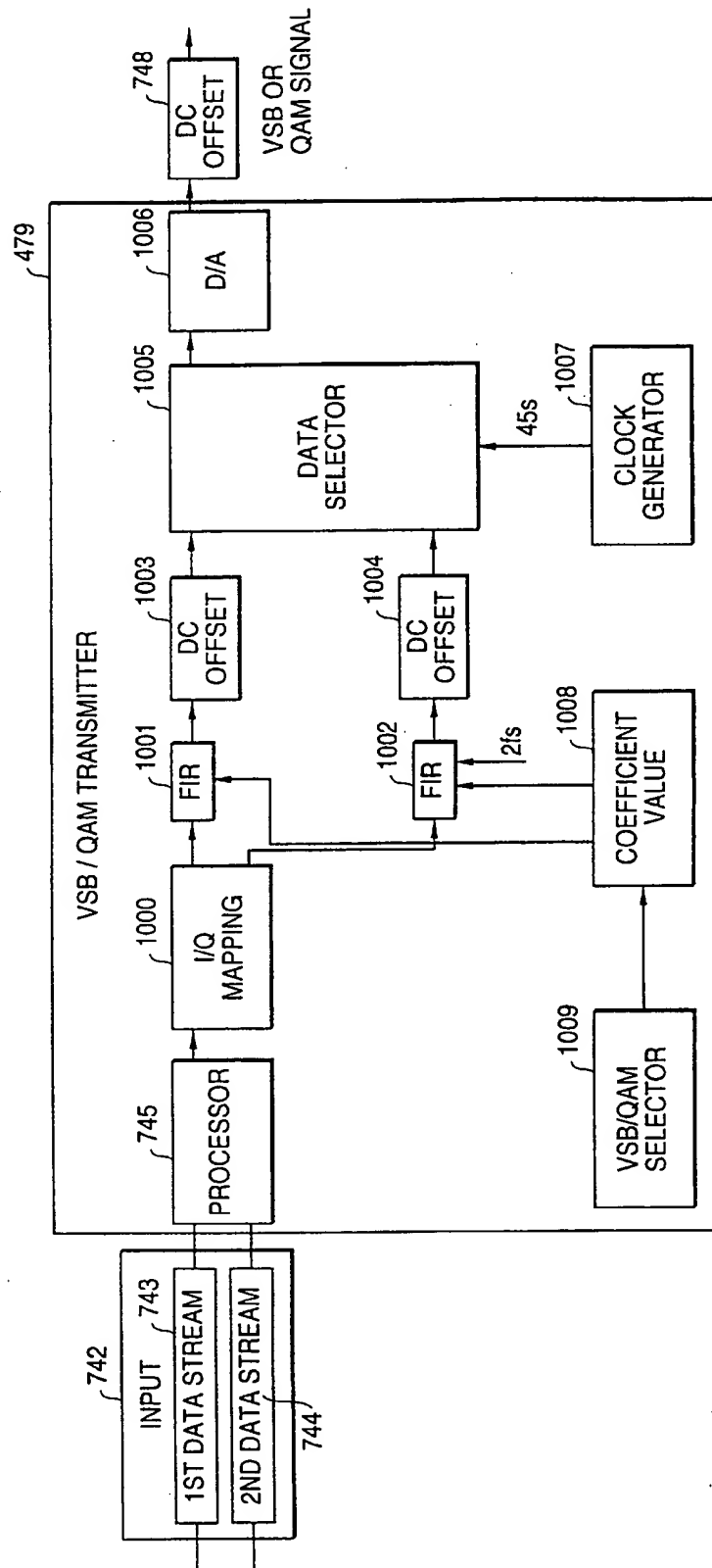


FIG. 175

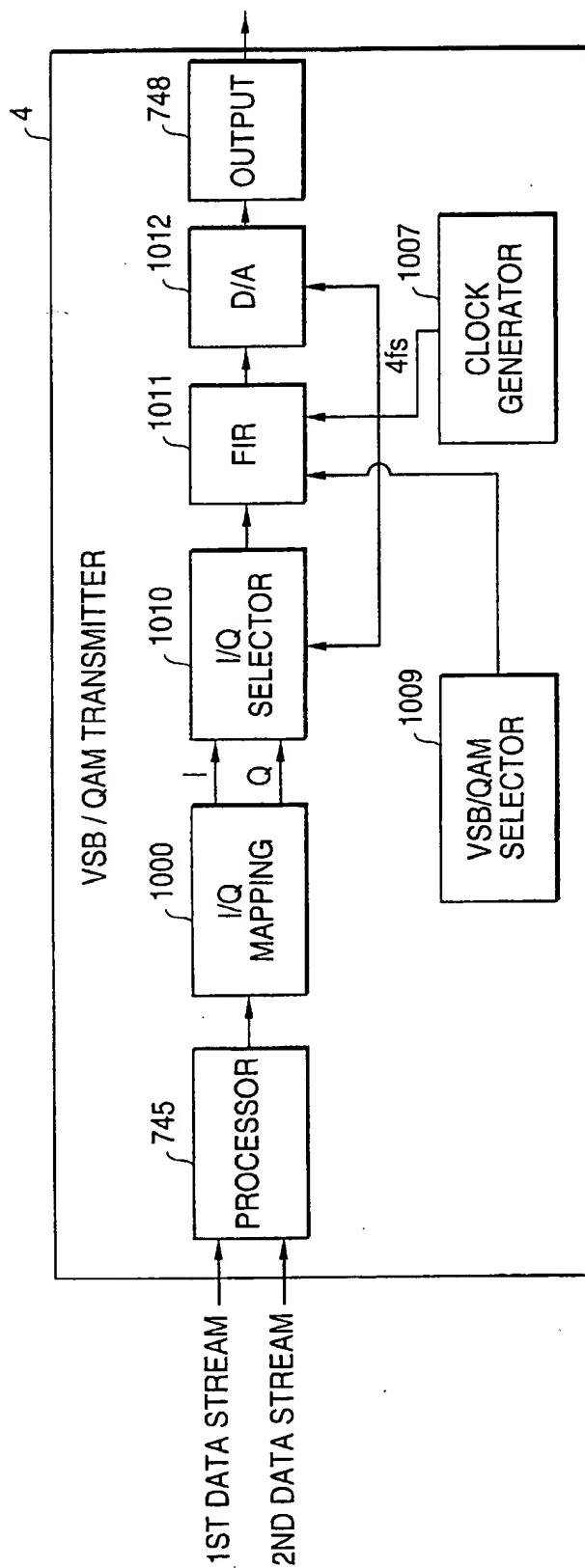


FIG. 176

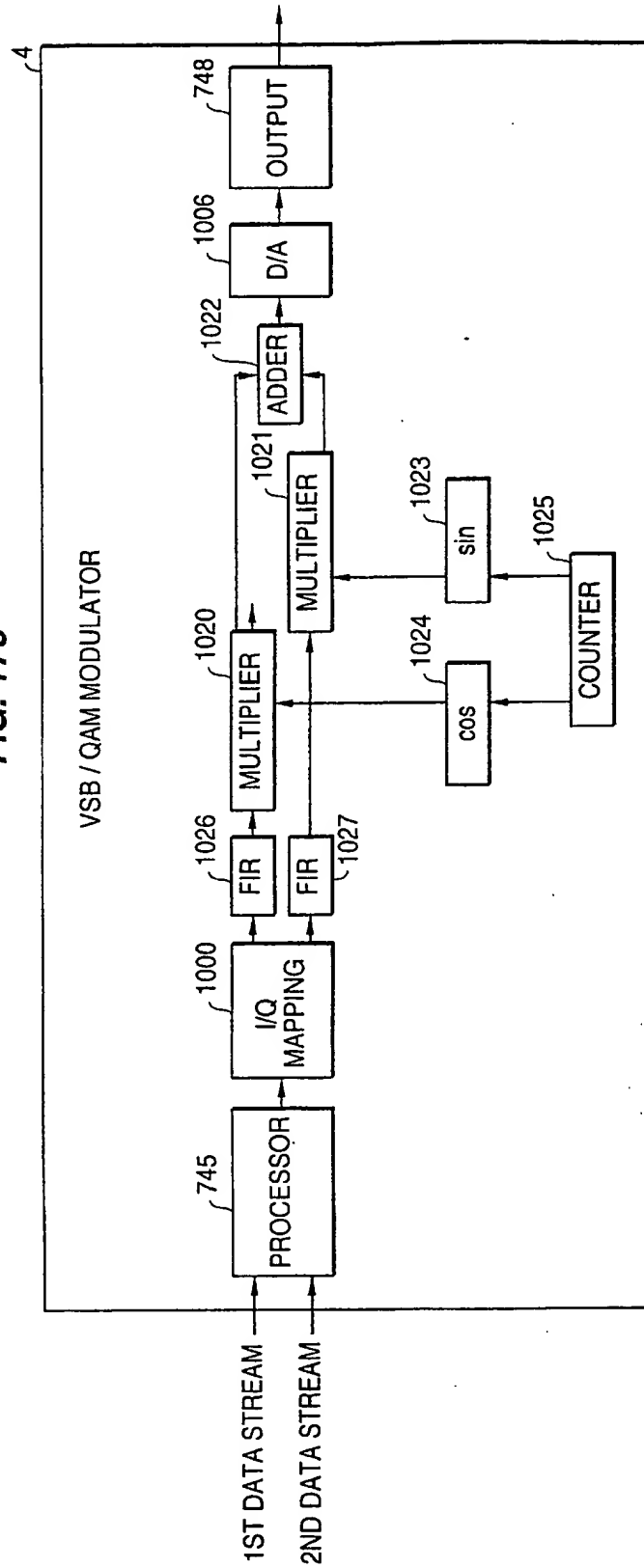


FIG. 177

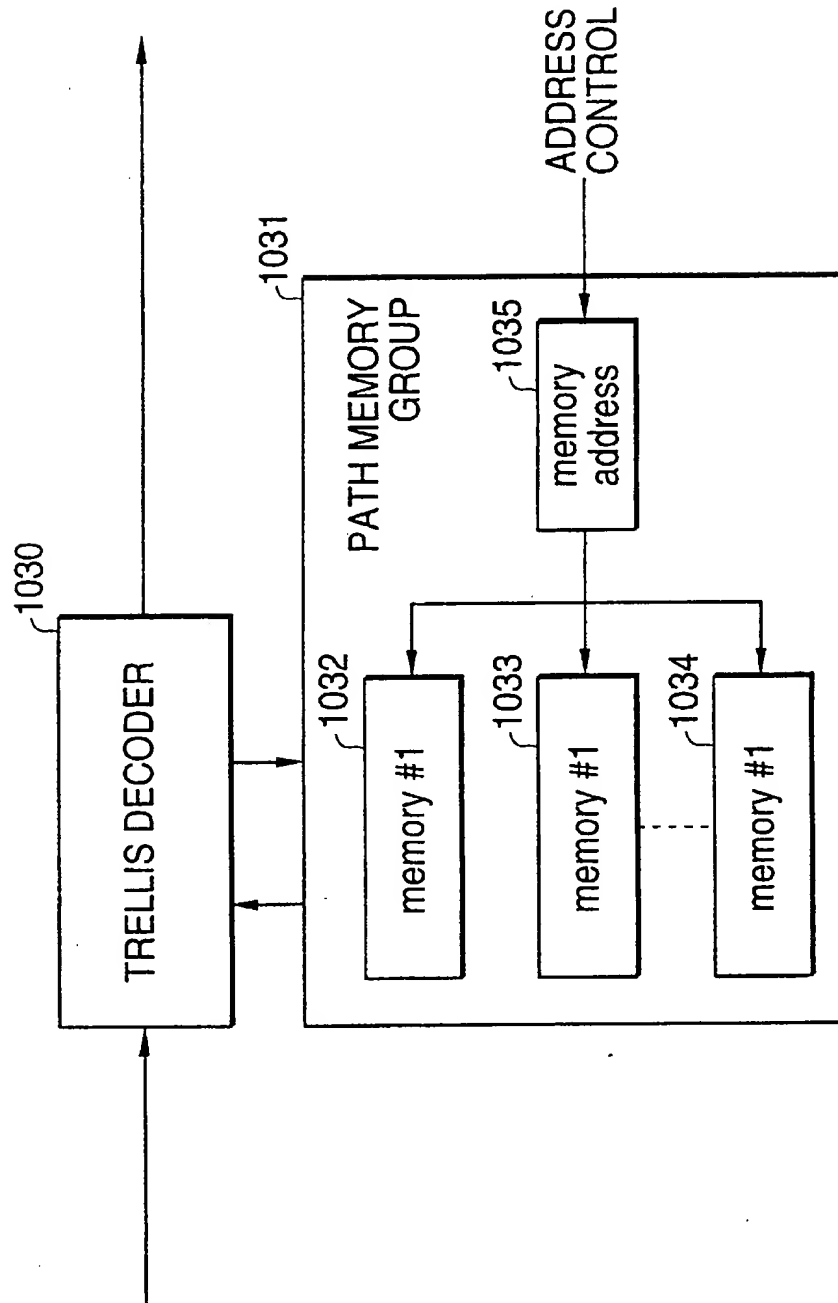


FIG. 178

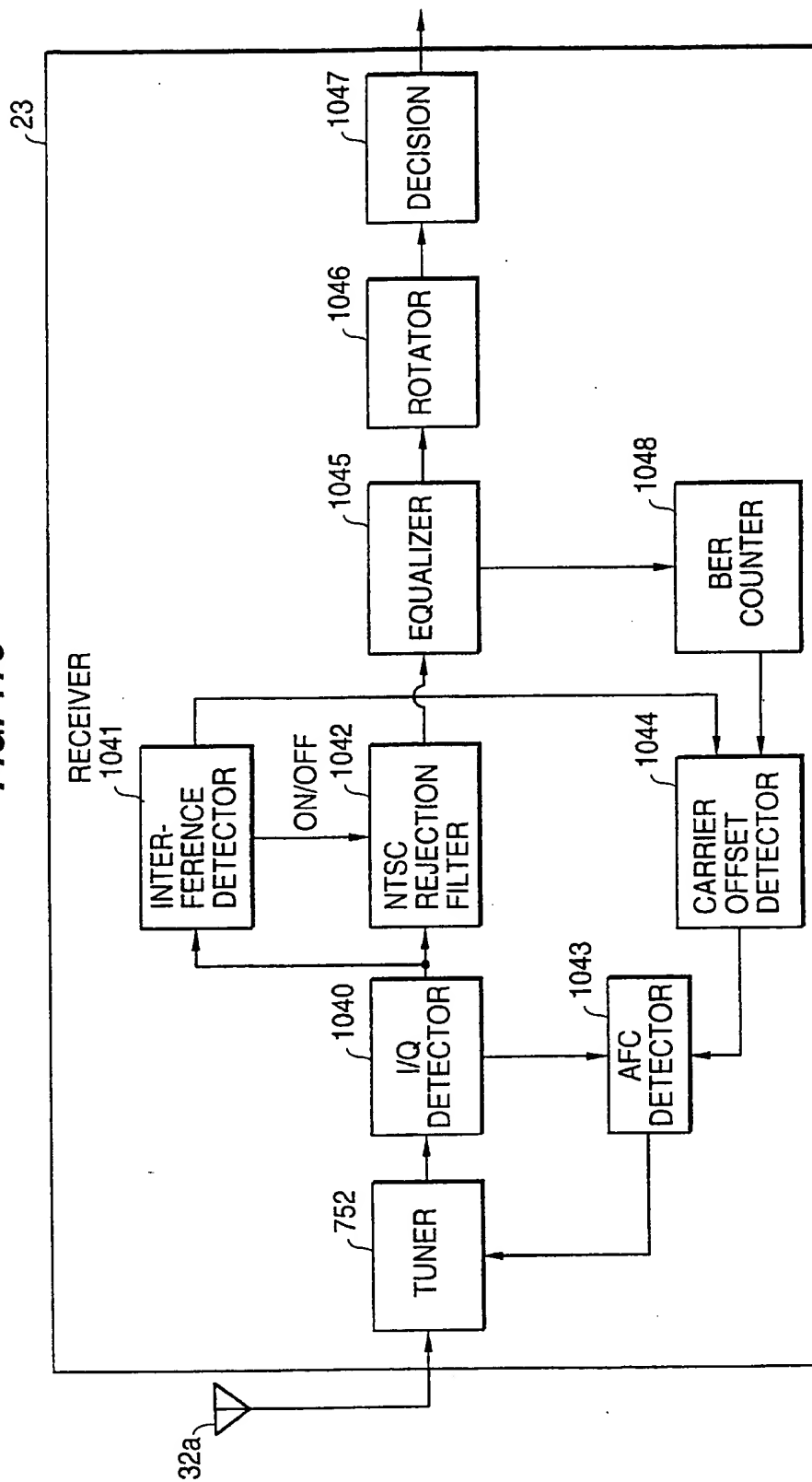


FIG. 179

